

# RX-IF SIMMIC FOR W-CDMA AGC + I/Q DEMODULATOR

# **UPC8194K**

#### **FEATURES**

- **RX-IF**: 190 MHz
- LOW POWER CONSUMPTION:
   VCC = 3.0 V
- SMALL 20 PIN QFN PACKAGE:
   Flat lead style for better performance
- TAPE AND REEL PACKAGING AVAILABLE

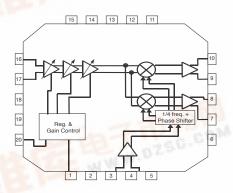
#### **DESCRIPTION**

NEC's UPC8194K is a Silicon Microwave Monolithic integrated circuit designed as a receiver (RX) section for W-CDMA. The UPC8194K is a RX-IF IC including IF-AGC amplifier and demodulator. This IC is suitable for kit-use for W-CDMA IF section.

This IC was developed using NEC's new ultra high seed silicon bipolar process.

NEC's stringent quality assurance and test procedures ensure the highest reliability and perormance.

#### **BLOCK DIAGRAM**



#### **APPLICATIONS**

· W-CDMA

# **ELECTRICAL CHARACTERISTICS** (unless otherwise specified, TA = 25°C, Vcc = 3.0 V, fiF = 192.5 MHz, flo = 760 MHz, Plo = -15 dBm, fi/Q = 2.5 MHz)

	PART NUMBER PACKAGE OUTLINE	UPC8194K QFN-20				
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS mA μA	MIN	TYP	MAX	
lcc	Circuit Current, no input signals power saving mode			9.3 -	12.6 1	
Vg	Voltage Gain, Vcont= 2.5 V Vcont= 0.5 V	dB	70 –	77 -	- -20	
IIP3	Input Third Order Intercept Point, Gain= +65 dB,Rs = $600\Omega$ Balanced,Pin = -76 dBm Gain= -10 dB, Rs = $600\Omega$ Balanced,Pin = -10 dBm	dBm dBm	-60 0	-55 3	C.COM	
LoL	Local Leakage, leakage to I/Q port when local = 760 MHz and output = 30mVp-p balanced	dBc	1   -	_	20	
BW(I/Q)	I/Q Bandwidth, 3 dB down	MHz	10	_	_	
Vout(I/Q)	I/Q Maximum output swing, balanced ouptut	Vp-p	1	_	_	
AE	I/Q Gain Balance, fl/Q = 2.5 MHz	dB	_	_	Δ0.5	
PE	I/Q Phase Error, fI/Q = 2.5 MHz	Deg	_	_	±3	
GACC	Gain Accuracy, VCONT = 1 to 2 V	dB/V	_	Δ4.6	Δ6	
TPS(Rise)	Rise time from power-saving mode	us	_	_	20	
VPS(Rise)	Rising voltage from power-saving mode	V	2.2	_	_	
VPS(fall)	Falling voltage from power-saving mode	V	_		0.5	
GF	Gain Flatness at fif ±2.5 MHz	dB	_	_	Δ0.5	

## STANDARD CHARACTERISTICS FOR REFERENCE (unless otherwise specified, TA = 25°C, Vcc = 3.0 V,

fIF = 192.5 MHz, fLO = 760 MHz, PLO = -15 dBm, fI/Q = 2.5 MHz)

	PART NUMBER PACKAGE OUTLINE	UPC8194K QFN-20			
SYMBOLS	PARAMETERS AND CONDITIONS	UNITS	MIN	TYP	MAX
NF	Noise Figure, Gain = +65 dB	dB	_	9.5	_
EVM	Error Vector Magnitude, IF = 190 MHz, 3.84 Msps QPSK modulation, Gain is adjusted.	%rms	_	3	_
P1dB	Input Power at 1 dB compression point at Gain = +50 dB	dBm	_	-50	_

## **ABSOLUTE MAXIMUM RATINGS**<sup>1</sup>, (TA = 25°C)

SYMBOLS	PARAMETERS	UNITS	RATINGS
Vcc	Supply Voltage	V	4.0
VPS, VCONT	Applied Voltage	V	-0.3 to Vcc+0.3
ТА	Operating Ambient Temperature	°C	-40 to +85
Тѕтс	Storage Temperature	°C	-55 to +150
Po	Power Dissipation	mW	309

#### Notes:

1. Operation in excess of any one of these parameters may result in permanent damage.

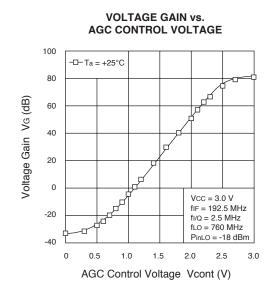
# RECOMMENDED OPERATING CONDITIONS

SYMBOLS	PARAMETERS	UNITS	MIN	TYP	MAX
Vcc	Supply Voltage	V	2.7	3.0	3.3
Та	Operating Ambient Temperature	°C	-25	+25	+85
fIF	IF Frequency	MHz	-	190	-
fLO	Local Frequency	MHz	-	760	_
PLO	Local input Level	dBm	-18	-15	-12
ZI/Q	I/Q load impedance	kΩ	10	20	_
fı/Q	I/Q loutput frequency	MHz	-	-	10

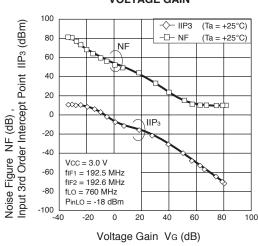
### ORDERING INFORMATION

Part Number	Package
UPC8194K-E1-A	20 Pin plastic QFN

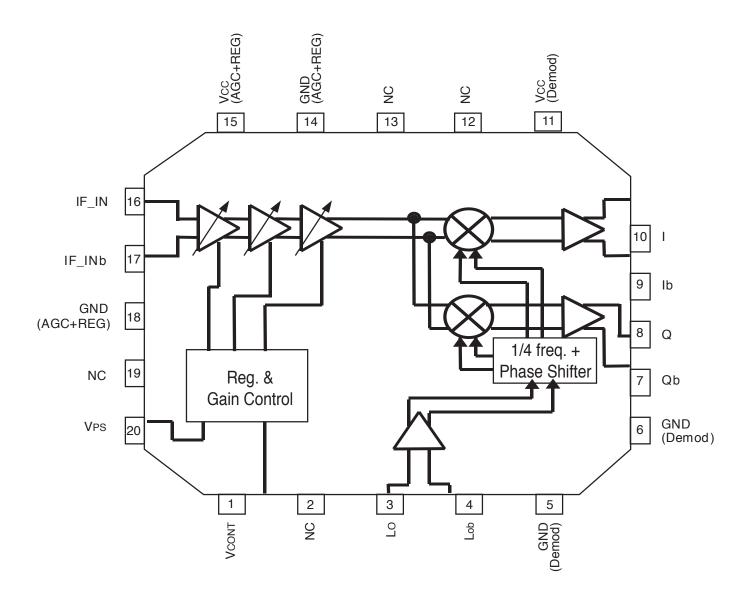
**TYPICAL PERFORMANCE CURVES** (Vcc = 3.0 V, Vps = 2.5 V, fif = 192.5 MHz, fLo = 760 MHz, PLO = -15 dBm, fivq = 2.5 MHz, Vivq = 400 mVp-p balanced)



#### NOISE FIGURE and INPUT 3rd ORDER INTERCEPT POINT vs. VOLTAGE GAIN



## BLOCK DIAGRAM (Units in mm)



# PIN FUNCTIONS (Pin Voltage is measured at Vcc = 3.0 V)

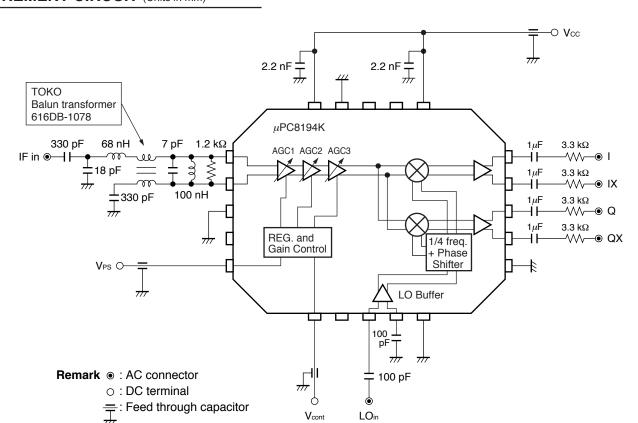
Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V)	Functions and Applications	Internal Equivalent Circuits	
1	Vcont	0 to Vcc	-	Gain control pin of AGC amplifier.  Variable gains are available in accordance with applied voltage.	7 Vcc 54 k 12 k 5 k W	
2 19	N.C.	-	-	No connection. This pin is not connected to internal circuit. This pin should be opened or grounded.	_	
3	LO	-	1.96	Local signal input pin of I/Q demodulator. Input frequency is 760 MHz.	Vcc Vcc	
4	LOb	-	1.96	Bypass pin of local signal input for I/Q demodulator. In the case of single local input, this pin must be decoupled with capacitor ex. 100 to 1 000 pF.	3 4 50 ≥ 50 GND	
5	GND (Demod.)	0	-	Ground pin of I/Q demodulator.  This pin should be grounded with minimum inductance.  Form the ground pattern as widely as possible to minimize ground impeadance.		
7	Qb	-	1.40	I/Q/Ib/Qb signal output pins.	Vcc	
8	Q	-	1.40	Each pin is an emitter follower.	8.5 k	
9	lb	-	1.40	Each of Ib and Qb is differential output of I and Q.	7)(8)(9)(10)	
10	I	-	1.40	Recommendable load impedance is 10 to 20 k $\Omega$ .	GND	
11	VCC (Demod.)	2.7 to 3.3	-	Supply voltage pin of I/Q demodulator (phase shifter + I/Q Mixer).	_	
12	TEST 1	0	-	TEST pin.		
13	TEST 2	0	-	In actual use, this pin should be grounded.	_	
14 18	GND (AGC, REG.)	0	-	Ground pin of AGC amplifier and internal regulator.  This pin should be grounded with minimum inductance.  Form the ground pattern as widely as possible to minimize ground impedance.	_	

## **UPC8194K**

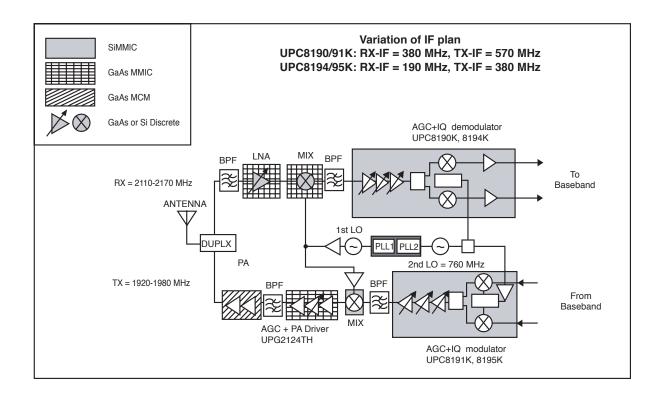
## PIN FUNCTIONS (Pin Voltage is measured at Vcc = 3.0 V)

Pin No.	Pin Name	Applied Voltage (V)	Pin Voltage (V)	Functions	and Applications	Internal Equivalent Circuits
15	VCC (AGC, REG.)	2.7 to 3.3	-	Supply voltage pin internal regulator.	of AGC amplifier and	nd —
16	IF_IN	-	2.75	IF signal input pin. This pin is input of Balance input betw Input frequency is	reen 16, 17 pin.	Vcc 1.1 k
17	IF_INb	-	2.75	IF signal input pin. In the case of signal local input, this pin must be decoupled with capacitor.		
20	VPS	High: 2.2 to Vcc Low:	-	Power saving pin. This pin modulator Active/Sleep state	can control with bias as follows.	Vcc 100 k
		0 to 0.5		VPS (V)	State	100 k S
				0 to 0.5	Sleep Mode	GND
				2.2 to 3	Active Mode	

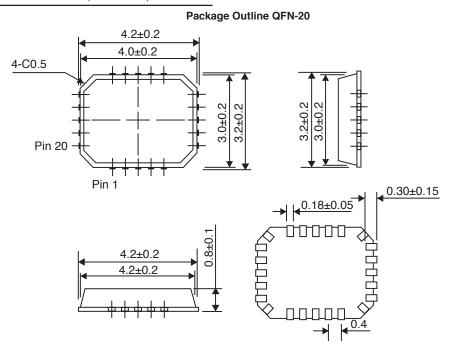
## MEASUREMENT CIRCUIT (Units in mm)



#### **APPPLICATION EXAMPLE: W-CDMA**



#### **OUTLINE DIMENSIONS** (Units in mm)



#### Life Support Applications

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Subject: Compliance with EU Directives

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CEL Pb-free products have the same base part number with a suffix added. The suffix –A indicates that the device is Pb-free. The –AZ suffix is used to designate devices containing Pb which are exempted from the requirement of RoHS directive (\*). In all cases the devices have Pb-free terminals. All devices with these suffixes meet the requirements of the RoHS directive.

This status is based on CEL's understanding of the EU Directives and knowledge of the materials that go into its products as of the date of disclosure of this information.

Restricted Substance per RoHS	Concentration Limit per RoHS (values are not yet fixed)	Concentration contained in CEL devices		
Lead (Pb)	< 1000 PPM	-A Not Detected	-AZ (*)	
Mercury	< 1000 PPM	Not De	etected	
Cadmium	< 100 PPM	Not Detected		
Hexavalent Chromium	< 1000 PPM	Not Detected		
PBB	< 1000 PPM	Not Detected		
PBDE	< 1000 PPM	Not Detected		

If you should have any additional questions regarding our devices and compliance to environmental standards, please do not hesitate to contact your local representative.

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