

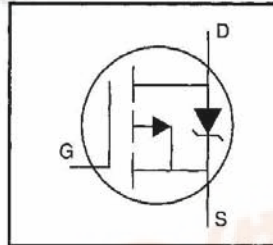
# International IR Rectifier

PD - 94866

## IRFI9Z34GPbF

HEXFET® Power MOSFET

- Isolated Package
- High Voltage Isolation= 2.5KVRMS ⑤
- Sink to Lead Creepage Dist.= 4.8mm
- P-Channel
- 175°C Operating Temperature
- Dynamic dv/dt Rating
- Low Thermal Resistance
- Lead-Free



$$V_{DSS} = -60V$$

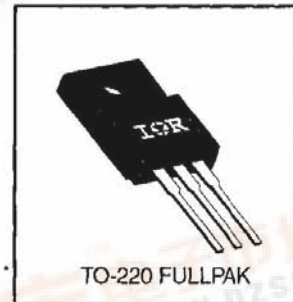
$$R_{DS(on)} = 0.14\Omega$$

$$I_D = -12A$$

### Description

Third Generation HEXFETs from International Rectifier provide the designer with the best combination of fast switching, ruggedized device design, low on-resistance and cost-effectiveness.

The TO-220 Fullpak eliminates the need for additional insulating hardware in commercial-industrial applications. The moulding compound used provides a high isolation capability and a low thermal resistance between the tab and external heatsink. This isolation is equivalent to using a 100 micron mica barrier with standard TO-220 product. The Fullpak is mounted to a heatsink using a single clip or by a single screw fixing.



### Absolute Maximum Ratings

|                             | Parameter                                        | Max.                  | Units |
|-----------------------------|--------------------------------------------------|-----------------------|-------|
| $I_D$ @ $T_C = 25^\circ C$  | Continuous Drain Current, $V_{GS}$ @ -10 V       | -12                   | A     |
| $I_D$ @ $T_C = 100^\circ C$ | Continuous Drain Current, $V_{GS}$ @ -10 V       | -8.5                  |       |
| $I_{DM}$                    | Pulsed Drain Current ①                           | -48                   |       |
| $P_D$ @ $T_C = 25^\circ C$  | Power Dissipation                                | 42                    | W     |
|                             | Linear Derating Factor                           | 0.28                  | W/°C  |
| $V_{GS}$                    | Gate-to-Source Voltage                           | ±20                   | V     |
| $E_{AS}$                    | Single Pulse Avalanche Energy ②                  | 370                   | mJ    |
| $I_{AR}$                    | Avalanche Current ①                              | -12                   | A     |
| $E_{AR}$                    | Repetitive Avalanche Energy ①                    | 4.2                   | mJ    |
| dv/dt                       | Peak Diode Recovery dv/dt ③                      | -4.5                  | V/ns  |
| $T_J$                       | Operating Junction and Storage Temperature Range | -55 to +175           | °C    |
|                             | Soldering Temperature, for 10 seconds            | 300 (1.6mm from case) |       |
|                             | Mounting Torque, 6-32 or M3 screw                | 10 lbf•in (1.1 N•m)   |       |

### Thermal Resistance

|                 | Parameter           | Min. | Typ. | Max. | Units |
|-----------------|---------------------|------|------|------|-------|
| $R_{\theta JC}$ | Junction-to-Case    | —    | —    | 3.6  | °C/W  |
| $R_{\theta JA}$ | Junction-to-Ambient | —    | —    | 65   |       |

# IRFI9Z34GPbF

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## Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (unless otherwise specified)

|                                 | Parameter                            | Min. | Typ.   | Max. | Units               | Test Conditions                                                     |
|---------------------------------|--------------------------------------|------|--------|------|---------------------|---------------------------------------------------------------------|
| $V_{(BR)DSS}$                   | Drain-to-Source Breakdown Voltage    | -60  | —      | —    | V                   | $V_{GS}=0V$ , $I_D=-250\mu A$                                       |
| $\Delta V_{(BR)DSS}/\Delta T_J$ | Breakdown Voltage Temp. Coefficient  | —    | -0.060 | —    | V/ $^\circ\text{C}$ | Reference to $25^\circ\text{C}$ , $I_D=-1mA$                        |
| $R_{DS(on)}$                    | Static Drain-to-Source On-Resistance | —    | —      | 0.14 | $\Omega$            | $V_{GS}=-10V$ , $I_D=-7.2A$ ④                                       |
| $V_{GS(th)}$                    | Gate Threshold Voltage               | -2.0 | —      | -4.0 | V                   | $V_{DS}=V_{GS}$ , $I_D=-250\mu A$                                   |
| $g_{fs}$                        | Forward Transconductance             | 5.4  | —      | —    | S                   | $V_{DS}=-25V$ , $I_D=-7.2A$ ④                                       |
| $I_{DSS}$                       | Drain-to-Source Leakage Current      | —    | —      | -100 | $\mu A$             | $V_{DS}=-60V$ , $V_{GS}=0V$                                         |
|                                 |                                      | —    | —      | -500 |                     | $V_{DS}=-48V$ , $V_{GS}=0V$ , $T_J=150^\circ\text{C}$               |
| $I_{GSS}$                       | Gate-to-Source Forward Leakage       | —    | —      | -100 | nA                  | $V_{GS}=-20V$                                                       |
|                                 | Gate-to-Source Reverse Leakage       | —    | —      | 100  |                     | $V_{GS}=20V$                                                        |
| $Q_g$                           | Total Gate Charge                    | —    | —      | 34   | nC                  | $I_D=-18A$                                                          |
| $Q_{gs}$                        | Gate-to-Source Charge                | —    | —      | 9.9  |                     | $V_{DS}=-48V$                                                       |
| $Q_{gd}$                        | Gate-to-Drain ("Miller") Charge      | —    | —      | 16   |                     | $V_{GS}=-10V$ See Fig. 6 and 13 ④                                   |
| $t_{d(on)}$                     | Turn-On Delay Time                   | —    | 18     | —    | ns                  | $V_{DD}=-30V$                                                       |
| $t_r$                           | Rise Time                            | —    | 120    | —    |                     | $I_D=-18A$                                                          |
| $t_{d(off)}$                    | Turn-Off Delay Time                  | —    | 20     | —    |                     | $R_G=12\Omega$                                                      |
| $t_f$                           | Fall Time                            | —    | 58     | —    |                     | $R_D=1.5\Omega$ See Figure 10 ④                                     |
| $L_D$                           | Internal Drain Inductance            | —    | 4.5    | —    | nH                  | Between lead, 6 mm (0.25in.) from package and center of die contact |
| $L_S$                           | Internal Source Inductance           | —    | 7.5    | —    |                     |                                                                     |
| $C_{iss}$                       | Input Capacitance                    | —    | 1100   | —    | pF                  | $V_{GS}=0V$                                                         |
| $C_{oss}$                       | Output Capacitance                   | —    | 620    | —    |                     | $V_{DS}=-25V$                                                       |
| $C_{rss}$                       | Reverse Transfer Capacitance         | —    | 100    | —    |                     | $f=1.0MHz$ See Figure 5                                             |
| $C$                             | Drain to Sink Capacitance            | —    | 12     | —    | pF                  | $f=1.0MHz$                                                          |

## Source-Drain Ratings and Characteristics

|          | Parameter                              | Min.                                                                      | Typ. | Max. | Units   | Test Conditions                                                |
|----------|----------------------------------------|---------------------------------------------------------------------------|------|------|---------|----------------------------------------------------------------|
| $I_S$    | Continuous Source Current (Body Diode) | —                                                                         | —    | -12  | A       | MOSFET symbol showing the integral reverse p-n junction diode. |
| $I_{SM}$ | Pulsed Source Current (Body Diode) ①   | —                                                                         | —    | -48  |         |                                                                |
| $V_{SD}$ | Diode Forward Voltage                  | —                                                                         | —    | -6.3 | V       | $T_J=25^\circ\text{C}$ , $I_S=-12A$ , $V_{GS}=0V$ ④            |
| $t_{rr}$ | Reverse Recovery Time                  | —                                                                         | 100  | 200  | ns      | $T_J=25^\circ\text{C}$ , $I_F=-18A$                            |
| $Q_{rr}$ | Reverse Recovery Charge                | —                                                                         | 0.28 | 0.52 | $\mu C$ | $di/dt=100A/\mu s$ ④                                           |
| $t_{on}$ | Forward Turn-On Time                   | Intrinsic turn-on time is negligible (turn-on is dominated by $L_S+L_D$ ) |      |      |         |                                                                |

Notes:

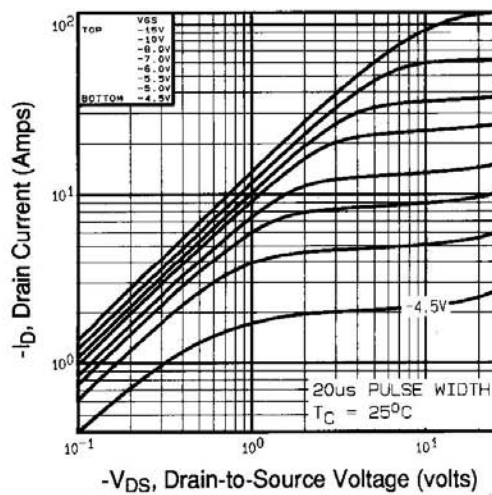
① Repetitive rating; pulse width limited by max. junction temperature (See Figure 11)

③  $I_{SD} \leq -12A$ ,  $di/dt \leq 170A/\mu s$ ,  $V_{DD} \leq V_{(BR)DSS}$ ,  $T_J \leq 175^\circ\text{C}$

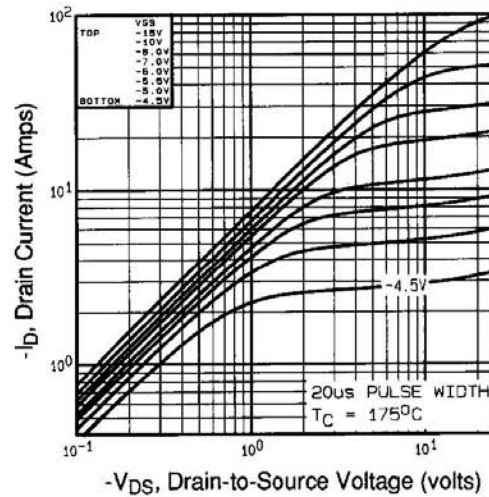
⑤  $t=60s$ ,  $f=60Hz$

②  $V_{DD}=-25V$ , starting  $T_J=25^\circ\text{C}$ ,  $L=3.0mH$ ,  $R_G=25\Omega$ ,  $I_{AS}=-12A$  (See Figure 12)

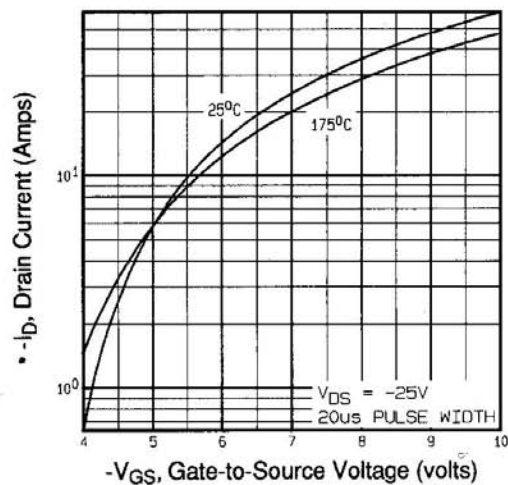
④ Pulse width  $\leq 300\mu s$ ; duty cycle  $\leq 2\%$ .



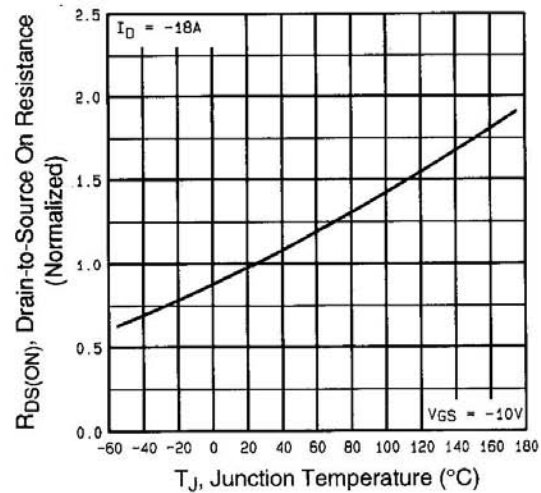
**Fig 1.** Typical Output Characteristics,  
 $T_C = 25^\circ\text{C}$



**Fig 2.** Typical Output Characteristics,  
 $T_C = 175^\circ\text{C}$



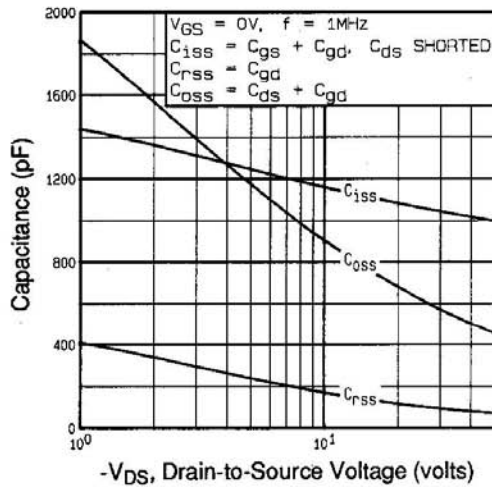
**Fig 3.** Typical Transfer Characteristics



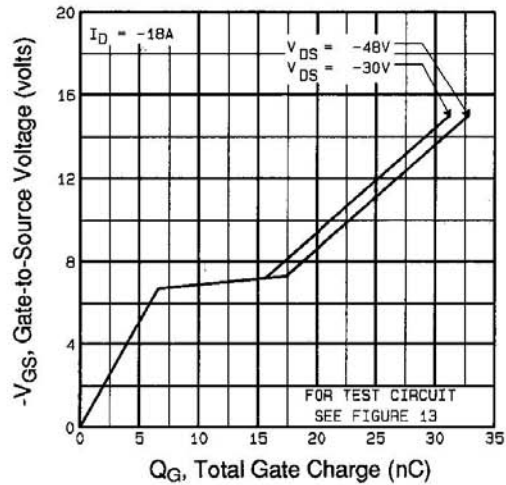
**Fig 4.** Normalized On-Resistance  
Vs. Temperature

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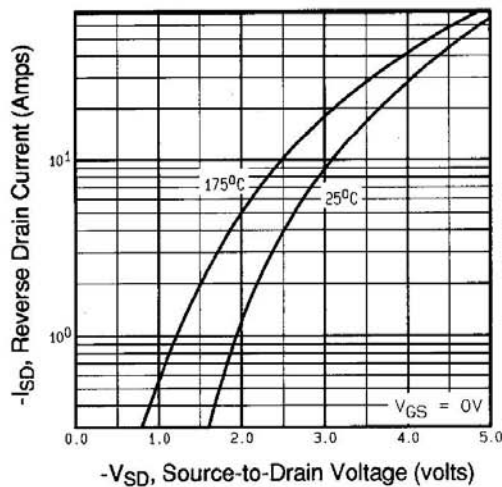
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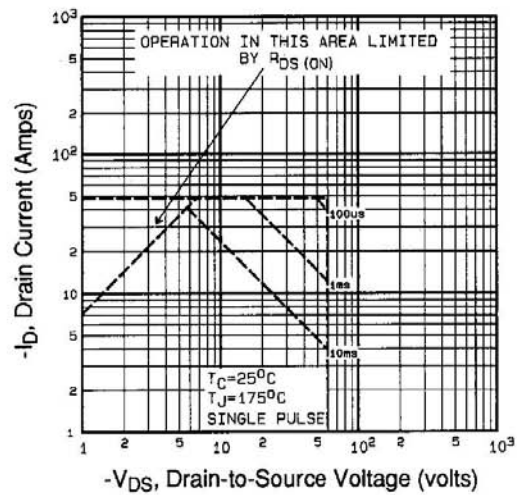
**Fig 5.** Typical Capacitance Vs. Drain-to-Source Voltage



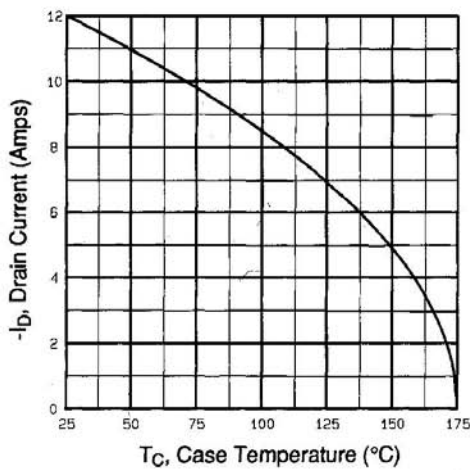
**Fig 6.** Typical Gate Charge Vs. Gate-to-Source Voltage



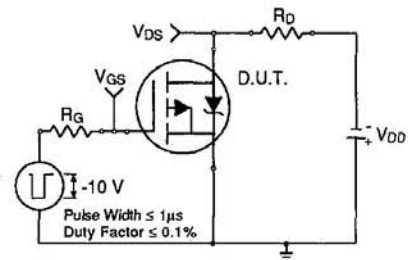
**Fig 7.** Typical Source-Drain Diode Forward Voltage



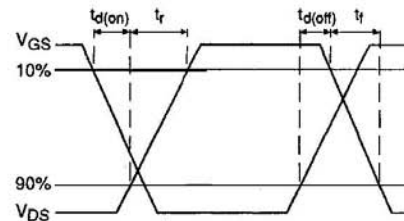
**Fig 8.** Maximum Safe Operating Area



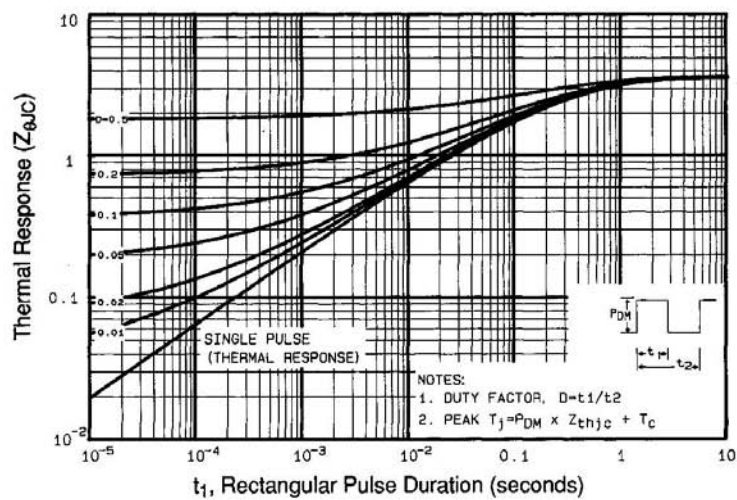
**Fig 9.** Maximum Drain Current Vs. Case Temperature



**Fig 10a.** Switching Time Test Circuit



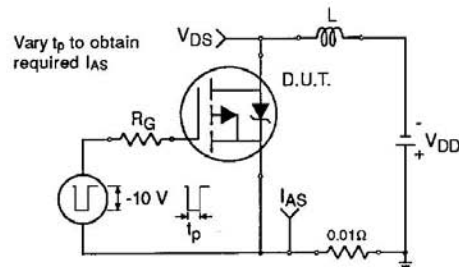
**Fig 10b.** Switching Time Waveforms



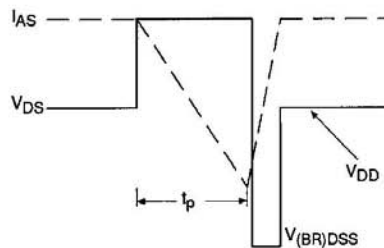
**Fig 11.** Maximum Effective Transient Thermal Impedance, Junction-to-Case

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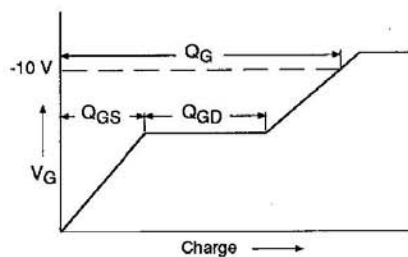
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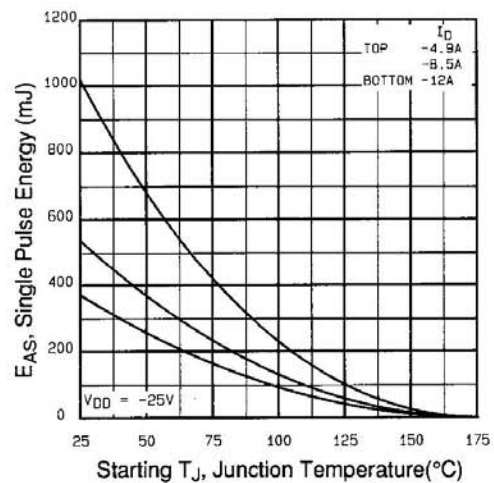
**Fig 12a.** Unclamped Inductive Test Circuit



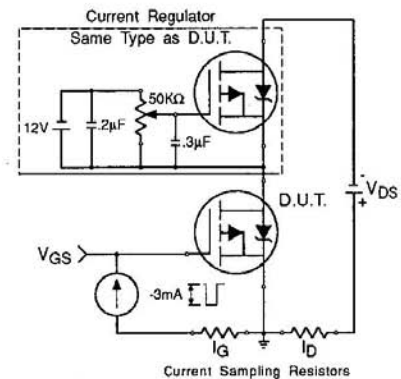
**Fig 12b.** Unclamped Inductive Waveforms



**Fig 13a.** Basic Gate Charge Waveform



**Fig 12c.** Maximum Avalanche Energy Vs. Drain Current



**Fig 13b.** Gate Charge Test Circuit

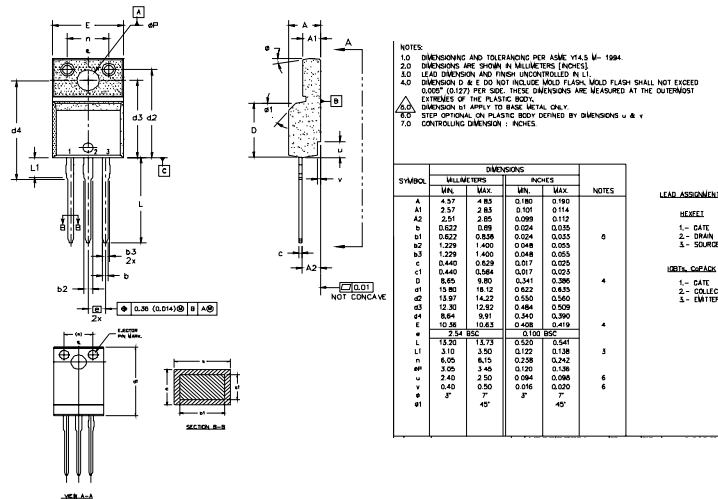
**Appendix A:** Figure 14, Peak Diode Recovery  $dv/dt$  Test Circuit – See page 1506

**Appendix B:** Package Outline Mechanical Drawing – See page 1510

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## TO-220 Full-Pak Package Outline

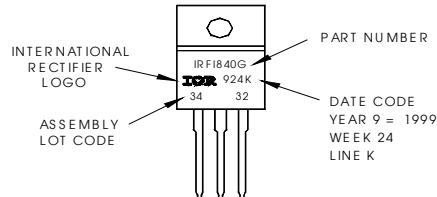
Dimensions are shown in millimeters (inches)



## TO-220 Full-Pak Part Marking Information

EXAMPLE: THIS IS AN IRFI840G  
 WITH ASSEMBLY  
 LOT CODE 3432  
 ASSEMBLED ON WW 24 1999  
 IN THE ASSEMBLY LINE "K"

**Note:** "P" in assembly line position indicates "Lead-Free"



Data and specifications subject to change without notice.

Note: For the most current drawings please refer to the IR website at:  
<http://www.irf.com/package/>