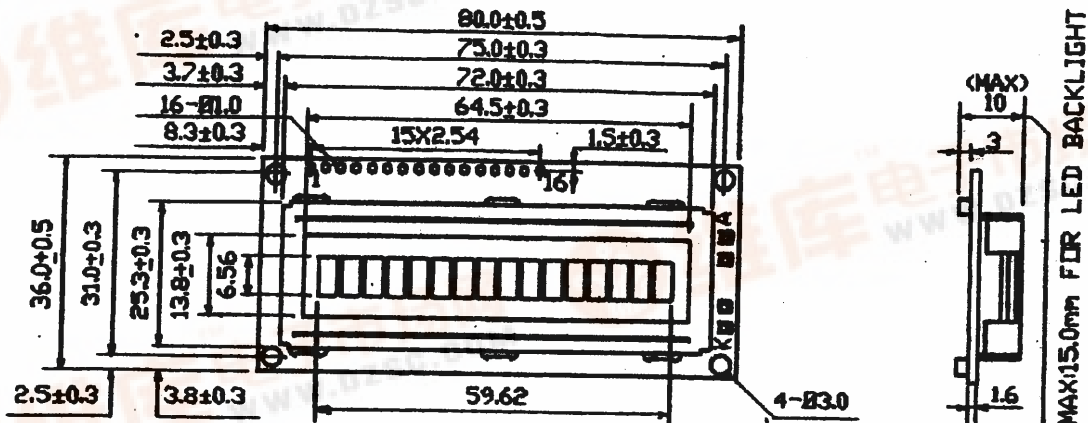
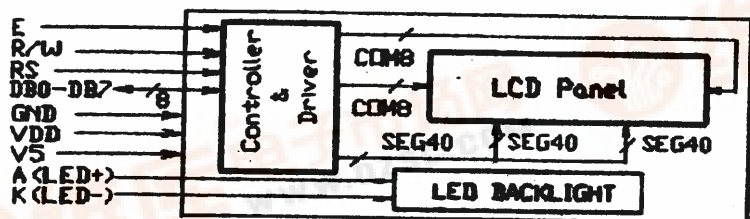


TRI MODS-1531-TN NON B/L

16 X 1 CHARACTER MODULE



Block Diagram & DD RAM Address

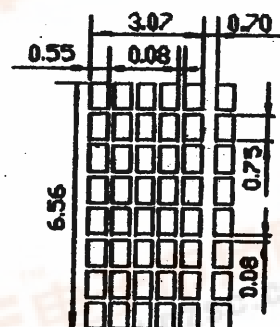


1	2	3	4	5	6	7	8	9	10	11	12	13	14	15	16
00	01	02	03	04	05	06	07	40	41	42	43	44	45	46	47

RAM area : 00H — 27H and 40H — 67H

Note : Please select 2 line display

Display Pattern



Interface Pin Function

Pin No.	Symbol	Level	Function
1	Vss	—	Power Supply
2	Vdd	—	
3	Vee	—	
4	Rs	H/L	Register select H>Data Input L/Instruction Input
5	R/W	H/L	Read/Write select H>Data Read(Module-MPU) L>Data Write(Module-MPU)
6	E	H/L	Operation start signal for data read/write
7	DB0	H/L	Lower order 4 line data bus with bidirectional three--state. Used for data transfer between the MPU and the module. These four are not used during 4-bit operation.
8	DB1	H/L	
9	DB2	H/L	
10	DB3	H/L	
11	DB4	H/L	Higher order 4 lines data bus with bidirectional three--state. Used for data transfer between the MPU and the module. DB7 can be used as a BUSY flag
12	DB5	H/L	
13	DB6	H/L	
14	DB7	H/L	
* 15	AK(LED+)	—	Supply Voltage for LED Backlighting 0V(GND)
* 16	KK(LED-)	—	

Note : * marked pin are only for LED backlighting type