

An ISO/TS 16949, ISO 9001 and ISO 14001 Certified Company





SOT-23 Formed SMD Package

CMBT4403

SILICON PLANAR EPITAXIAL TRANSISTOR

P-N-P transistor

Marking CMBT4403 = 2T

PACKAGE OUTLINE DETAILS ALL DIMENSIONS IN mm

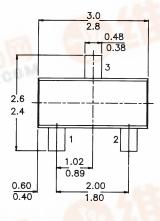


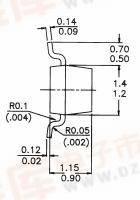
1 = BASE

2 = EMITTER

3 = COLLECTOR







ABSOLUTE MAXIMUM RATINGS

Collector-emitter voltage	$-V_{CEO}$	max.	40	V
Collector current (DC)	$-I_C$	max.	600	mA
DC current gain			100	
$I_C = 150 \text{ mA}; V_{CE} = 2 \text{ V}$	hee	min.	100	
IC = 150 mA, VCE = 2 V	h_{FE}	max.	300	
Total power dissipation up to $T_{amb} = 25 ^{\circ}C$	P_{tot}	max	250	mW
RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified)				

RATINGS (at $T_A = 25^{\circ}C$ unless otherwise specified) Limiting values

Limiting values				
Collector-emitter voltage	$-V_{CEO}$	max.	40	V
Collector-base voltage	$-V_{CBO}$	max.	40	V
Emitter-base voltage	$-V_{EBO}$	max.	5	V
Collector current (DC)	$-I_C$	max.	600	mA
Total power dissipation up to $T_{amb} = 25 ^{\circ}C$	P_{tot}	max	<i>250</i>	mW
Storage temperature range	T_{Stg}	−55 to	+150	$^{\circ}$ C
Junction temperature	T_j	max.	<i>150</i>	$^{\circ}$ C
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THERMAL RESISTANCE				
From junction to ambient	$R_{th\ j-a}$	= 3	500	K/W
CHARACTERISTICS				
$T_{amb} = 25$ °C unless otherwise specified				
Collector–emitter breakdown voltage				
$-I_C = 1.0 \text{ mA}; I_B = 0$	-V _{(BR)CEO}	>	40	V
Collector-base breakdown voltage	(DR)CLO			
$-I_C = 100 \ \mu A; I_E = 0$	-V _(BR) CBO	>	40	V
Emitter-base breakdown voltage	(==,,==			
$-I_E = 100 \ \mu A; I_C = 0$	$-V_{(BR)EBO}$	>	5	V
Base cut-off current				
$-V_{CE} = 35 \ V; \ -V_{EB} = 0.4 \ V$	$-I_{BEX}$	<	0.1	μA
Collector cut-off current				
$-V_{CE} = 35 V; -V_{EB} = 0.4 V$	-I _{CEX}	<	0.1	μA
D.C. current gain				
$-I_C = 0.1 \text{ mA}; -V_{CE} = 1 \text{ V}$	h_{FE}	>	<i>30</i>	
$-I_C = 1.0 \text{ mA}; -V_{CE} = 1 \text{ V}$	h_{FE}	>	<i>60</i>	
$-I_C = 10 \text{ mA; } -V_{CE} = 1 \text{ V}$	h_{FE}	> 1	100	
$-I_C = 150 \text{ mA; } -V_{CE} = 2 \text{ V}$	h_{FE}	100 to 3	300	
$-I_C = 500 \text{ mA; } -V_{CE} = 2 \text{ V}$	h_{FE}	>	20	
Saturation voltage				
$-I_C = 150 \text{ mA}; -I_B = 15 \text{ mA}$	-V _{CEsat}	<	0.4	V
	-V _{BEsat}	0.75 to 0	0.95	V
$-I_C = 500 \text{ mA}; -I_B = 50 \text{ mA}$	-V _{CEsat}	< 0	. 75	V
	-V _{BEsat}	<	1.3	V
Transition frequency				
$f = 100 \text{ MHz; } -I_C = 20 \text{ mA; } -V_{CE} = 10 \text{ V}$	f_T	> 2	200	MHz
Collector-base capacitance				
$I_E = 0$; $-V_{CB} = 10 \text{ V}$; $f = 100 \text{ kHz}$	C_{cb}	<	8.5	рF
Emitter-base capacitance				
$I_C = 0$; $-V_{BE} = 0.5 V$; $f = 100 \text{ kHz}$	C_{eb}	<	35	рF
Input impedance at $f = 1$ kHz;				
$-I_C = 1 \text{ mA; } -V_{CE} = 10 \text{ V}$	h_{ie}		1.5	
		max.	15	$k\Omega$
Voltage feed-back ratio at $f = 1$ kHz;				
$-I_C = 1 \text{ mA; } -V_{CE} = 10 \text{ V}$	h_{re}	min.0.1 × 10	0^{-4}	
C , CE	16	max. 8 × 10		
Small-signal curent gain at $f = 1 \text{ kHz}$				
$-I_C = 1 \text{ mA; } -V_{CE} = 10 \text{ V}$	h_{fe}	min.	60	
		max.	500	

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Output admittance at $f = 1$ kHz; $-I_C = 1$ mA; $-V_{CE} = 10$ V	$h_{O\!e}$	min. max.	1 μS 100 μS
Switching times (resistive load)			
Turn-on time			
$-I_C = 150 \text{ mA}; -I_{B1} = 15 \text{ mA};$			
$-V_{CC} = 30 \ V; \ -V_{EB} = 2 \ V$			
delay time	t_d	max.	15 ns
rise time	$t_{arGamma}$	max.	20 ns
Turn-off time			
$-I_C = 150 \text{ mA}; -V_{CC} = 30 \text{ V};$			
$-I_{B1} = +I_{B2} = 15 \text{ mA}$			
storage time	t_{S}	max.	225 ns
fall time	t_f	max.	30 ns

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