



DDTA (R1-ONLY SERIES) CA

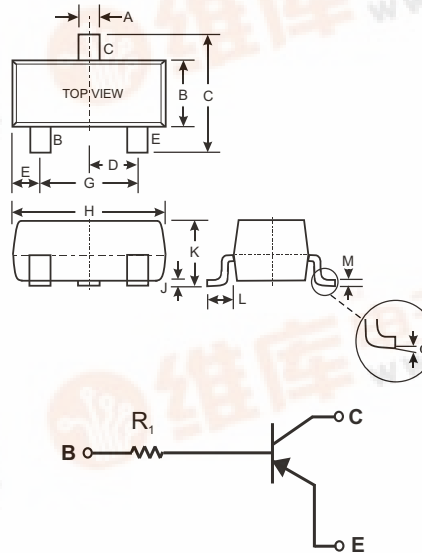
PNP PRE-BIASED SMALL SIGNAL SOT-23 SURFACE MOUNT TRANSISTOR

Features

- Epitaxial Planar Die Construction
- Complementary NPN Types Available (DDTC)
- Built-In Biasing Resistor, R1 only
- Lead Free/RoHS Compliant (Note 2)

Mechanical Data

- Case: SOT-23 Molded Plastic
- Case Material: Molded Plastic. UL Flammability Classification Rating 94V-0
- Moisture Sensitivity: Level 1 per J-STD-020C
- Terminal Connections: See Diagram
- Terminals: Solderable per MIL-STD-202, Method 208
- Lead Free Plating (Matte Tin Finish annealed over Alloy 42 leadframe).
- Marking: Date Code and Type Code (See Table Below & Page 2)
- Ordering Information (See Page 2)
- Weight: 0.008 grams (approximate)



SCHEMATIC DIAGRAM

SOT-23		
Dim	Min	Max
A	0.37	0.51
B	1.20	1.40
C	2.30	2.50
D	0.89	1.03
E	0.45	0.60
G	1.78	2.05
H	2.80	3.00
J	0.013	0.10
K	0.903	1.10
L	0.45	0.61
M	0.085	0.180
	0	8
All Dimensions in mm		

P/N	R1 (NOM)	Type Code
DDTA113TCA	1K	P01
DDTA123TCA	2.2K	P03
DDTA143TCA	4.7K	P07
DDTA114TCA	10K	P12
DDTA124TCA	22K	P16
DDTA144TCA	47K	P19
DDTA115TCA	100K	P23
DDTA125TCA	200K	P25

Maximum Ratings @ T_A = 25 °C unless otherwise specified

Characteristic	Symbol	Value	Unit
Collector-Base Voltage	V _{CB0}	-50	V
Collector-Emitter Voltage	V _{CE0}	-50	V
Emitter-Base Voltage	V _{EB0}	-5	V
Collector Current	I _C (Max)	-100	mA
Power Dissipation	P _d	200	mW
Thermal Resistance, Junction to Ambient Air (Note 1)	R _{JA}	625	°C/W
Operating and Storage Temperature Range	T _j , T _{STG}	-55 to +150	°C

Note: 1. Mounted on FR4 PC Board with recommended pad layout at <http://www.diodes.com/datasheets/ap02001.pdf>.
2. No purposefully added lead.

Electrical Characteristics @ $T_A = 25^\circ\text{C}$ unless otherwise specified

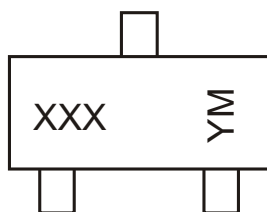
Characteristic	Symbol	Min	Typ	Max	Unit	Test Condition
Collector-Base Breakdown Voltage	BV_{CBO}	-50			V	$I_C = -50\text{ A}$
Collector-Emitter Breakdown Voltage	BV_{CEO}	-50			V	$I_C = -1\text{mA}$
Emitter-Base Breakdown Voltage	BV_{EBO}	-5			V	$I_E = -50\text{ A}$
Collector Cutoff Current	I_{CBO}			-0.5	A	$V_{CB} = -50\text{V}$
Emitter Cutoff Current	I_{EBO}			-0.5	A	$V_{EB} = -4\text{V}$
Collector-Emitter Saturation Voltage	$V_{CE(sat)}$			-0.3	V	$I_C/I_B = -10\text{mA}/-1\text{mA}$ DDTA113TCA $I_C/I_B = -5\text{mA}/-0.5\text{mA}$ DDTA123TCA $I_C/I_B = -2.5\text{mA}/-.25\text{mA}$ DDTA143TCA $I_C/I_B = -1\text{mA}/-.1\text{mA}$ DDTA114TCA $I_C/I_B = -5\text{mA}/-0.5\text{mA}$ DDTA124TCA $I_C/I_B = -2.5\text{mA}/-.25\text{mA}$ DDTA144TCA $I_C/I_B = -1\text{mA}/-0.1\text{mA}$ DDTA115TCA $I_C/I_B = -.5\text{mA}/-.05\text{mA}$ DDTA125TCA
DC Current Transfer Ratio	h_{FE}	100	250	600		$I_C = -1\text{mA}$, $V_{CE} = -5\text{V}$
Input Resistor (R_1) Tolerance	R_1	-30		+30	%	
Gain-Bandwidth Product*	f_T		250		MHz	$V_{CE} = -10\text{V}$, $I_E = 5\text{mA}$, $f = 100\text{MHz}$

* Transistor - For Reference Only

Ordering Information (Note 3)

Device	Packaging	Shipping
DDTA113TCA-7-F	SOT-23	3000/Tape & Reel
DDTA123TCA-7-F	SOT-23	3000/Tape & Reel
DDTA143TCA-7-F	SOT-23	3000/Tape & Reel
DDTA114TCA-7-F	SOT-23	3000/Tape & Reel
DDTA124TCA-7-F	SOT-23	3000/Tape & Reel
DDTA144TCA-7-F	SOT-23	3000/Tape & Reel
DDTA115TCA-7-F	SOT-23	3000/Tape & Reel
DDTA125TCA-7-F	SOT-23	3000/Tape & Reel

Notes: 3. For Packaging Details, go to our website at <http://www.diodes.com/datasheets/ap02007.pdf>.

Marking Information


XXX = Product Type Marking Code, See Table on Page 1
 YM = Date Code Marking
 Y = Year ex: N = 2002
 M = Month ex: 9 = September

Date Code Key

Year	2002	2003	2004	2005	2006	2007	2008	2009
Code	N	P	R	S	T	U	V	W

Month	Jan	Feb	March	Apr	May	Jun	Jul	Aug	Sep	Oct	Nov	Dec
Code	1	2	3	4	5	6	7	8	9	O	N	D

TYPICAL CURVES - DDTA114TCA

NEW PRODUCT

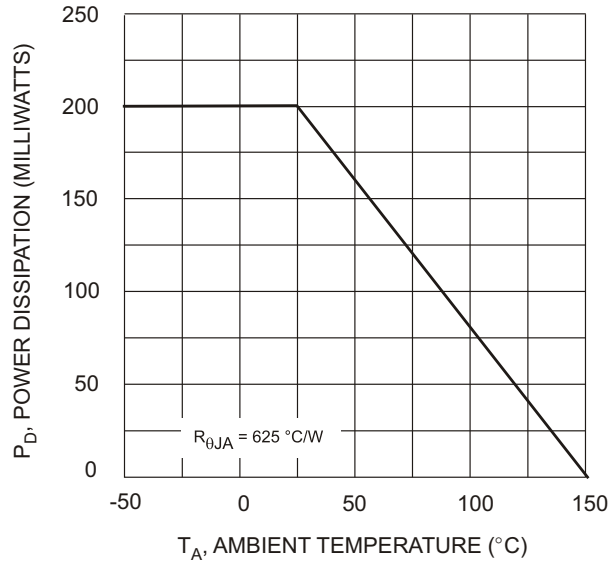


Fig. 1 Derating Curve

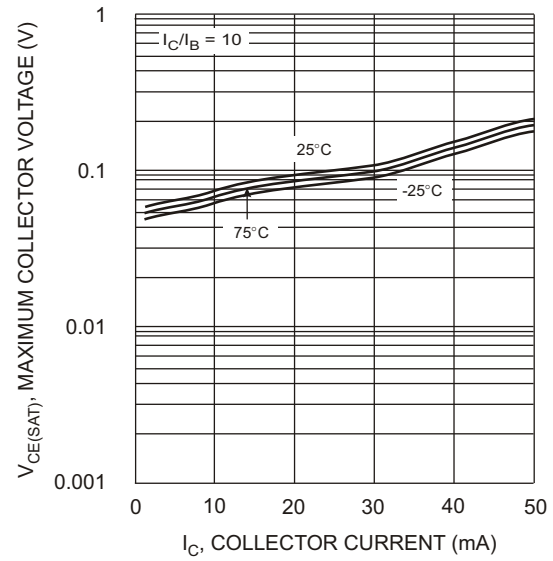


Fig. 2 $V_{CE(SAT)}$ vs. I_C

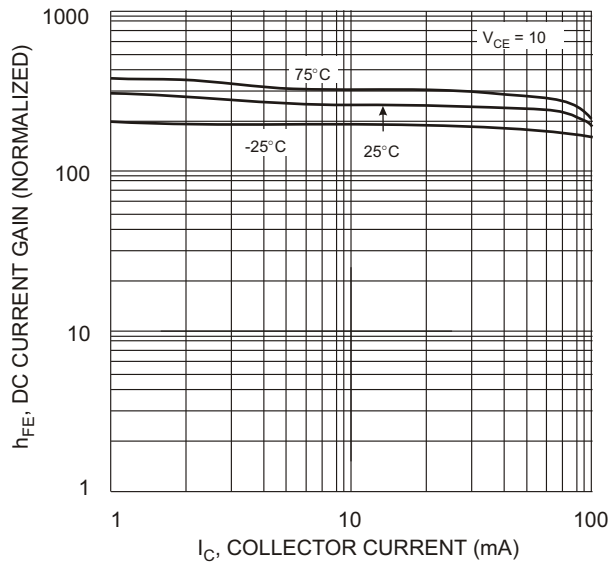


Fig. 3 DC Current Gain

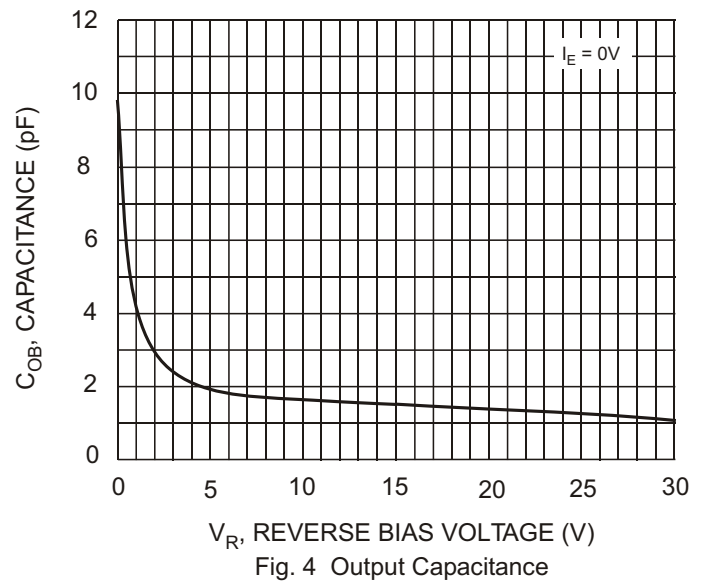


Fig. 4 Output Capacitance

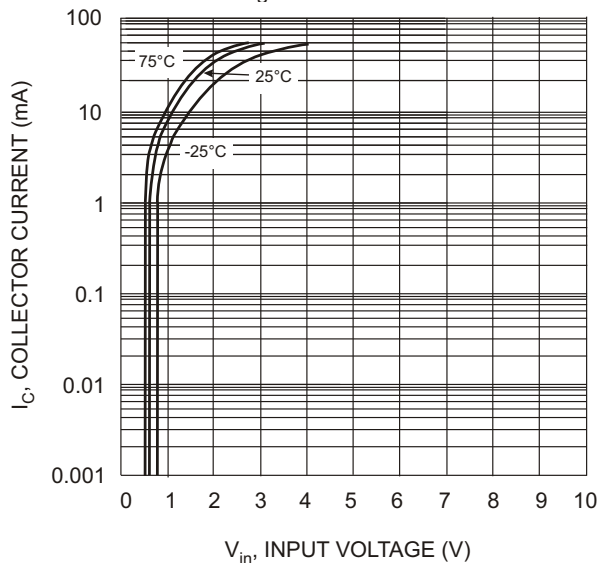


Fig. 5 Collector Current Vs. Input Voltage

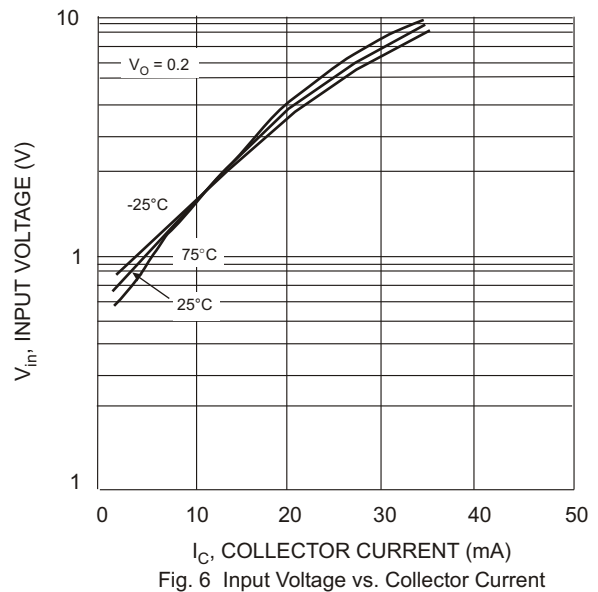


Fig. 6 Input Voltage vs. Collector Current



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