

**FAIRCHILD**  
SEMICONDUCTOR®

July 2007

# PPF2148 Full Function Load Switch

## Features

- 1.8 to 5.5V Input Voltage Range
- Controlled Turn-On
- 200mA Current Limit Options
- Undervoltage Lockout
- Thermal Shutdown
- <math>2\mu\text{A}</math> Shutdown Current
- Fast Current limit Response Time
  - 5 $\mu\text{s}$  to Moderate Over Currents
  - 30ns to Hard Shorts
- Fault Blanking
- Power Good Function
- RoHS Compliant

## Applications

- PDAs
- Cell Phones
- GPS Devices
- MP3 Players
- Digital Cameras
- Peripheral Ports
- Hot Swap Supplies

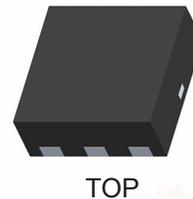
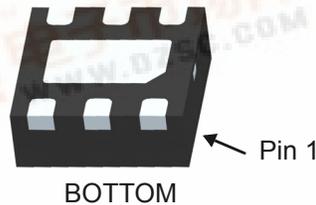


## General Description

The PPF2148 is a load switch which provides full protection to systems and loads which may encounter large current conditions. These devices contain a 0.15 $\Omega$  current-limited P-channel MOSFET which can operate over an input voltage range of 1.8-5.5V. Switch control is by a logic input (ONB) capable of interfacing directly with low voltage control signals. The part contains thermal shutdown protection which shuts off the switch to prevent damage to the part when a continuous over-current condition causes excessive heating.

When the switch current reaches the current limit, the part operates in a constant-current mode to prohibit excessive currents from causing damage. For the PPF2148, a current limit condition will immediately pull the fault signal pin low and the part will remain in the constant-current mode until the switch current falls below the current limit. The minimum current limit is 200mA.

The part is available in a space-saving 6 pin 2X2 MLP package.



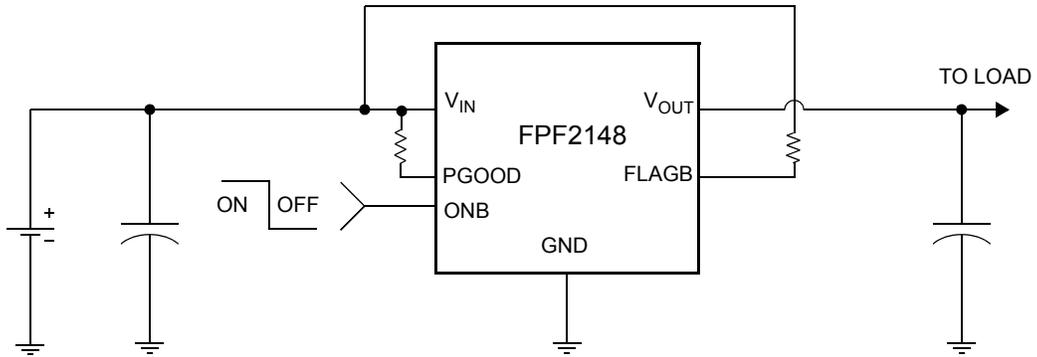
## Ordering Information

Part	Current Limit [mA]	Current Limit Blanking Time [ms]	Auto-Restart Time [ms]	ONB Pin Activity
PPF2148	200/300/400	NA	NA	Active LO

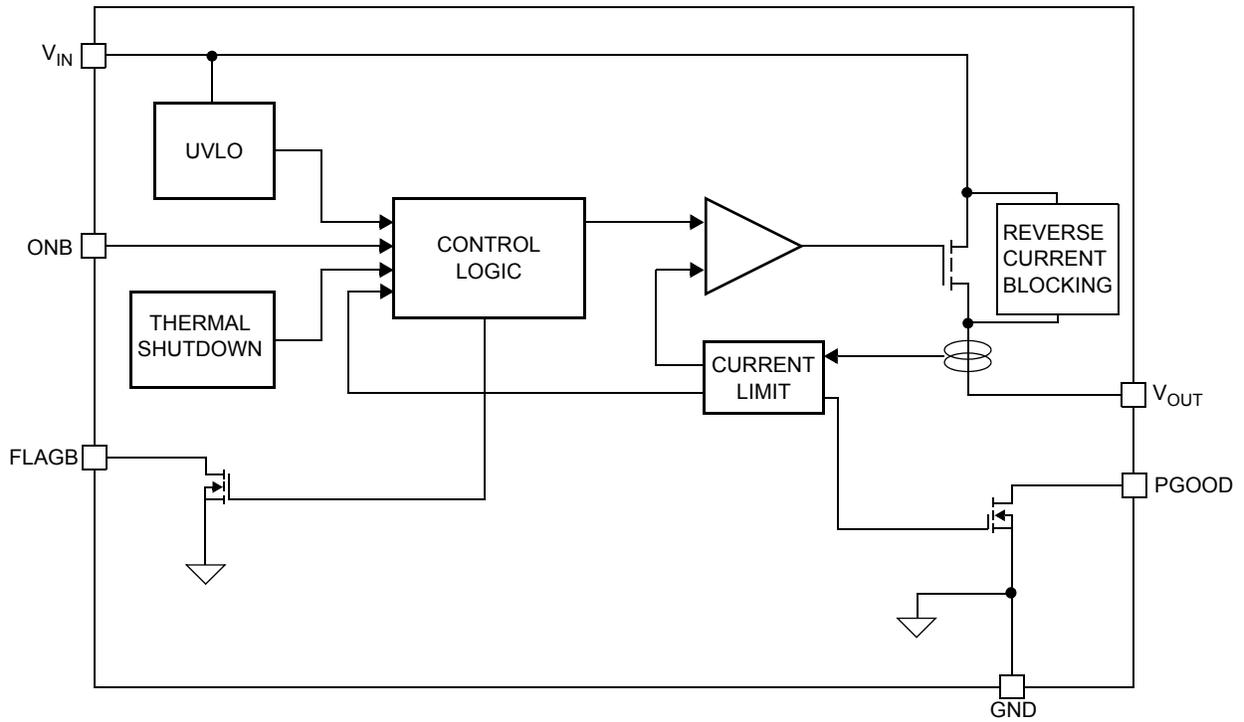
PPF2148 Full Function Load Switch



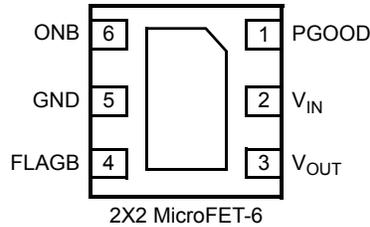
**Typical Application Circuit**



**Functional Block Diagram**



## Pin Configuration



## Pin Description

Pin	Name	Function
1	PGOOD	Power Good output: Open drain output which indicate that output voltage has reached 90% of input voltage
2	$V_{IN}$	Supply Input: Input to the power switch and the supply voltage For the IC
3	$V_{OUT}$	Switch Output: Output of the power switch
4	FLAGB	Fault Output: Active LO, open drain output which indicates an over current supply under voltage or over temperature state.
5	GND	Ground
6	ONB	ON Control Input

## Absolute Maximum Ratings

Parameter	Min.	Max.	Unit
$V_{IN}$ , $V_{OUT}$ , ONB, FLAGB, PGOOD to GND	-0.3	6	V
Power Dissipation		1.2	W
Operating and Storage Junction Temperature	-65	150	°C
Thermal Resistance, Junction to Ambient		86	°C/W
Electrostatic Discharge Protection	HBM	4000	V
	MM	400	V

## Recommended Operating Range

Parameter	Min.	Max.	Unit
$V_{IN}$	1.8	5.5	V
Ambient Operating Temperature, $T_A$	-40	85	°C

## Electrical Characteristics

$V_{IN} = 1.8$  to  $5.5V$ ,  $T_A = -40$  to  $+85^\circ C$  unless otherwise noted. Typical values are at  $V_{IN} = 3.3V$  and  $T_A = 25^\circ C$ .

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units	
<b>Basic Operation</b>							
Operating Voltage	$V_{IN}$		1.8		5.5	V	
Quiescent Current	$I_Q$	$I_{OUT} = 0mA$	$V_{IN} = 1.8V$	40	70	100	$\mu A$
			$V_{IN} = 3.3V$		75	120	
			$V_{IN} = 5.5V$		85	120	
On-Resistance	$R_{ON}$	$T_A = 25^\circ C$ , $I_{OUT} = 200mA$		120	160	m $\Omega$	
		$T_A = -40$ to $+85^\circ C$ , $I_{OUT} = 200mA$		135			

## Electrical Characteristics Cont.

$V_{IN} = 1.8$  to  $5.5V$ ,  $T_A = -40$  to  $+85^\circ C$  unless otherwise noted. Typical values are at  $V_{IN} = 3.3V$  and  $T_A = 25^\circ C$ .

Parameter	Symbol	Conditions	Min.	Typ.	Max.	Units
ONB Input Logic High Voltage (ON)	$V_{IH}$	$V_{IN} = 1.8V$	0.8			V
		$V_{IN} = 5.5V$	1.4			
ONB Input Logic Low Voltage	$V_{IL}$	$V_{IN} = 1.8V$			0.5	V
		$V_{IN} = 5.5V$			1	
ONB Input Leakage		$V_{ONB} = V_{IN}$ or GND	-1		1	$\mu A$
$V_{IN}$ Shutdown Current		$V_{ONB} = 5.5V$ , $V_{OUT} = 5.5V$ , $V_{IN} =$ short to GND	-2		2	$\mu A$
FLAGB Output Logic Low Voltage		$V_{IN} = 5V$ , $I_{SINK} = 10mA$		0.05	0.2	V
		$V_{IN} = 1.8V$ , $I_{SINK} = 10mA$		0.12	0.3	
FLAGB Output High Leakage Current		$V_{IN} = 5V$ , $V_{ONB} = 0V$			1	$\mu A$
PGOOD Threshold Voltage		$V_{IN} = 5.5V$		90		%
PGOOD Threshold Voltage Hysteresis				1		%
PGOOD Output Logic Low Voltage		$V_{IN} = 5V$ , $I_{SINK} = 10mA$		0.05	0.1	V
		$V_{IN} = 1.8V$ , $I_{SINK} = 10mA$		0.12	0.2	V
PGOOD Output High Leakage Current		$V_{IN} = 5V$ , $V_{ONB} = 0V$			1	$\mu A$
<b>Reverse Block</b>						
$V_{OUT}$ Shutdown Current		$V_{ONB} = 5.5V$ , $V_{OUT} = 5.5V$ , $V_{IN} =$ short to GND	-2		2	$\mu A$
<b>Protections</b>						
Current Limit	$I_{LIM}$	$V_{IN} = 3.3V$ , $V_{OUT} = 3.0V$	200	300	400	mA
Thermal Shutdown		Shutdown Threshold $T_J$ increasing		140		$^\circ C$
		Return from Shutdown		130		
		Hysteresis		10		
Under Voltage Lockout	$V_{UVLO}$	$V_{IN}$ Increasing	1.55	1.65	1.75	V
Under Voltage Lockout Hysteresis				50		mV
<b>Dynamic</b>						
Turn on time	$t_{ON}$	$R_L = 500\Omega$ , $C_L = 0.1\mu F$		25		$\mu s$
Turn off time	$t_{OFF}$	$R_L = 500\Omega$ , $C_L = 0.1\mu F$		45		$\mu s$
$V_{OUT}$ Rise Time	$t_R$	$R_L = 500\Omega$ , $C_L = 0.1\mu F$		10		$\mu s$
$V_{OUT}$ Fall Time	$t_F$	$R_L = 500\Omega$ , $C_L = 0.1\mu F$		110		$\mu s$
Short Circuit Response Time		$V_{IN} = 5.5V$ , $V_{ONB} = GND$ . Moderate Over-Current Condition		5		$\mu s$
		$V_{IN} = 5.5V$ , $V_{ONB} = GND$ . Hard Short		30		ns

**Note 1:** Package power dissipation on 1square inch pad, 2 oz. copper board.

Typical Characteristics

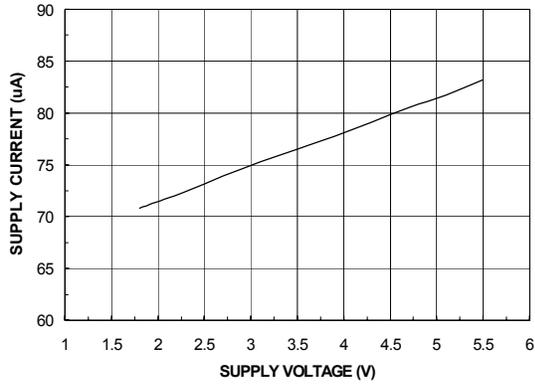


Figure 1. Quiescent Current vs. Input Voltage

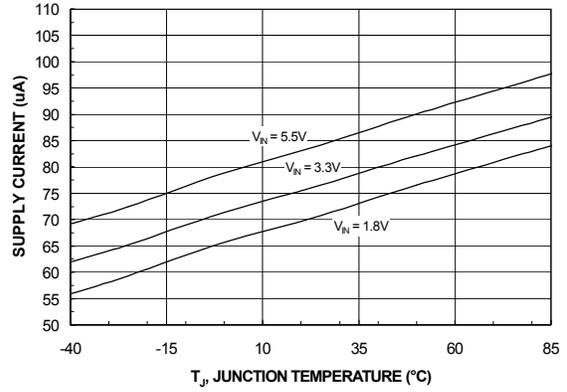


Figure 2. Quiescent Current vs. Temperature

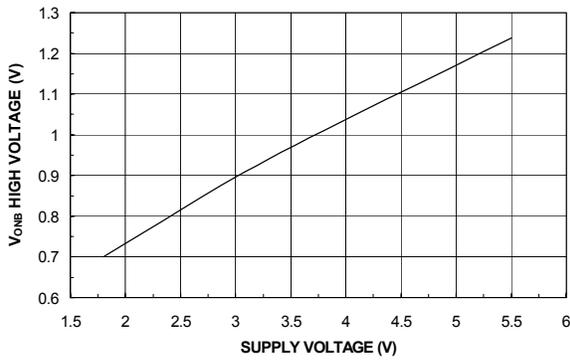


Figure 3. V<sub>ONB</sub> High Voltage vs. Input Voltage

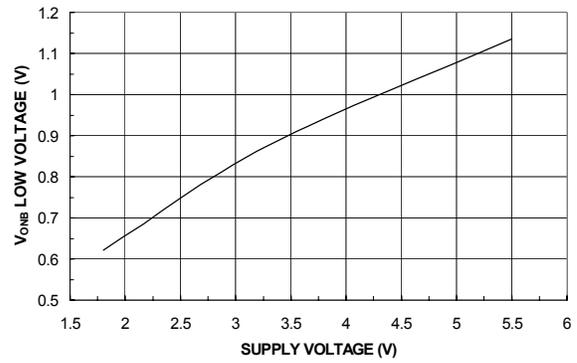


Figure 4. V<sub>ONB</sub> Low Voltage vs. Input Voltage

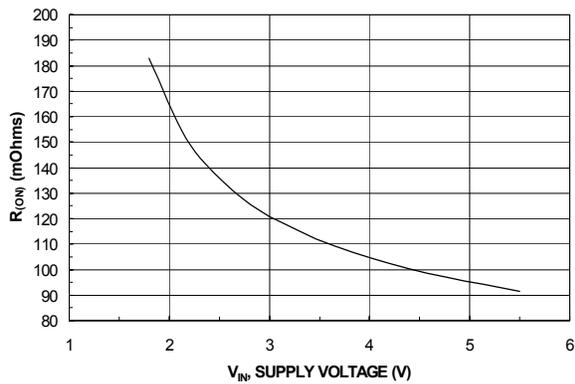


Figure 5. R<sub>(ON)</sub> vs. V<sub>IN</sub>

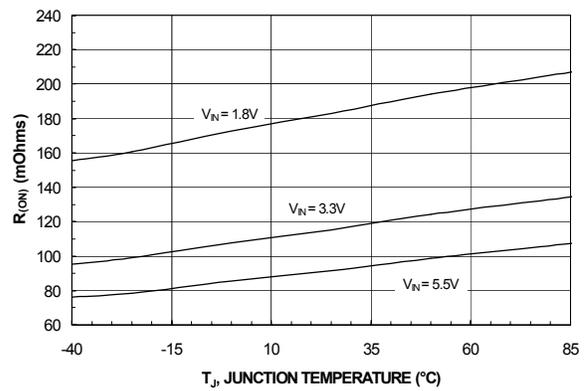


Figure 6. R<sub>(ON)</sub> vs. Temperature

### Typical Characteristics

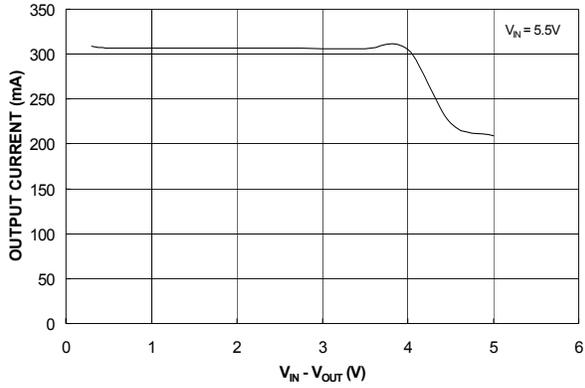


Figure 7. Current Limit vs. Output Voltage

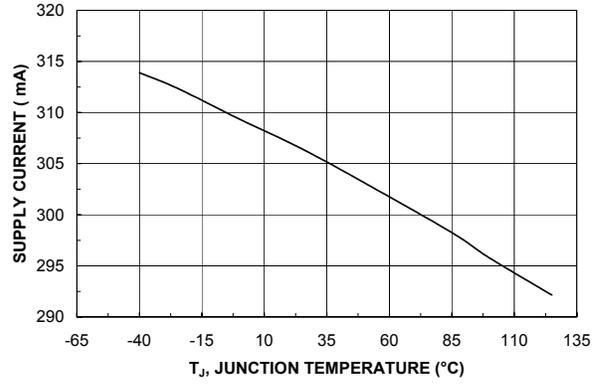


Figure 8. Current Limit vs. Temperature

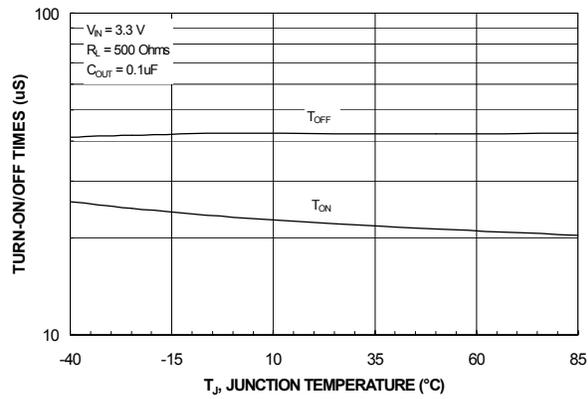


Figure 9.  $T_{ON}$  /  $T_{OFF}$  vs. Temperature

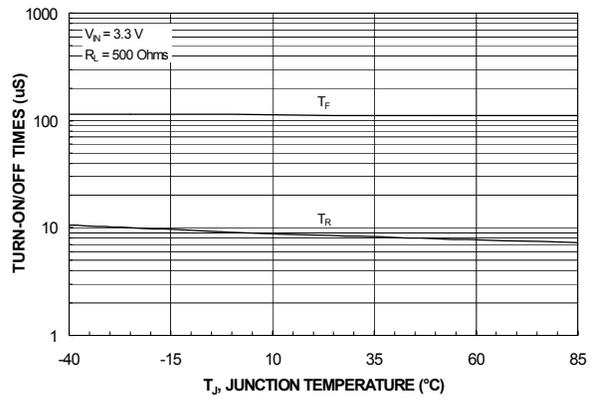


Figure 10.  $T_{RISE}$  /  $T_{FALL}$  vs. Temperature

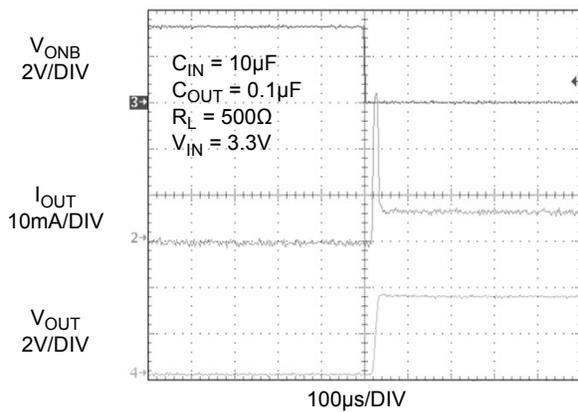


Figure 11.  $T_{ON}$  Response

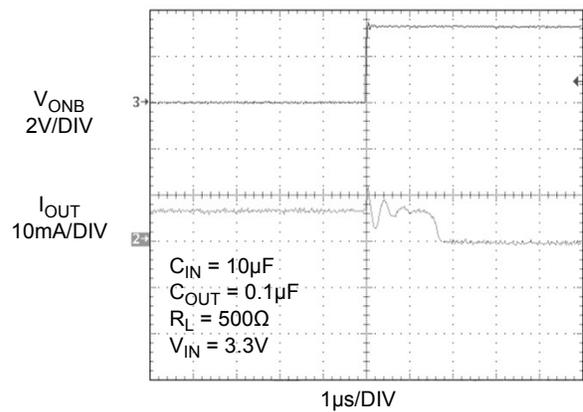


Figure 12.  $T_{OFF}$  Response

Typical Characteristics

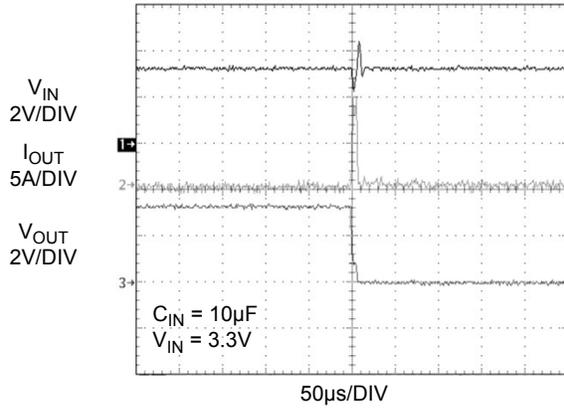


Figure 13. Short Circuit Response Time (Output shorted to GND)

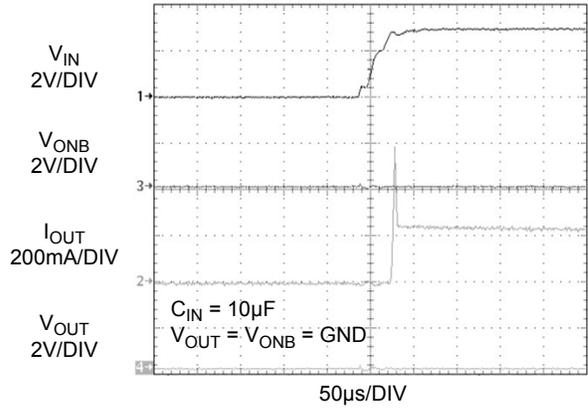


Figure 14. Current Limit Response Time (Switch is powered into a short)

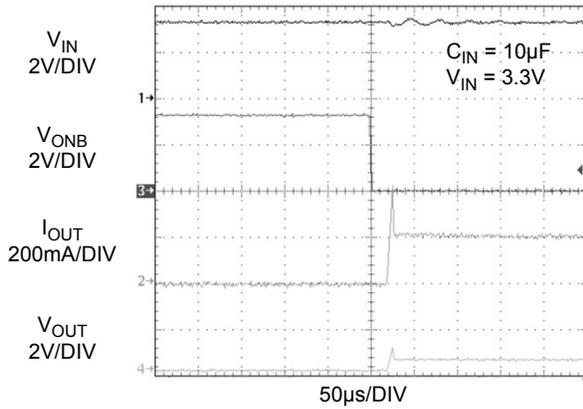


Figure 15. Current Limit Response Time (Output is loaded by 2.2Ω,  $C_{OUT} = 0.1\mu F$ )

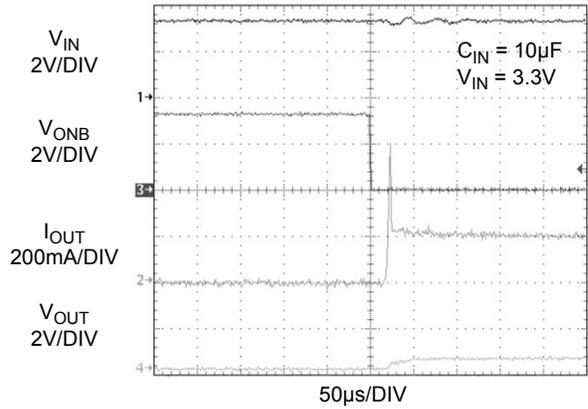


Figure 16. Current Limit Response Time (Output is loaded by 2.2Ω,  $C_{OUT} = 10\mu F$ )

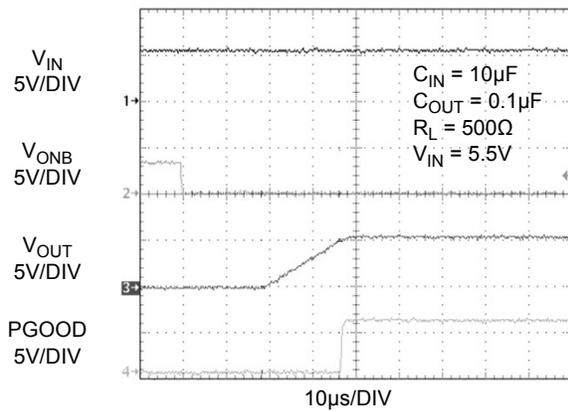


Figure 17. PGOOD Response

## Description of Operation

The FPF2148 is a current limited switch that protects systems and loads which can be damaged or disrupted by the application of high currents. The core of each device is a 0.15Ω P-channel MOSFET and a controller capable of functioning over a wide input operating range of 1.8-5.5V. The controller protects against system malfunctions through current limiting, under-voltage lockout and thermal shutdown and power good features. The current limit is preset for 200mA.

### On/Off Control

The ONB pin controls the state of the switch. Activating ONB continuously (ONB pin low) holds the switch in the on state so long as there is no under-voltage on  $V_{IN}$  or a junction temperature in excess of 140°C. ONB is active LO and has a low threshold making it capable of interfacing with low voltage signals. In addition, excessive currents will cause the switch to turn off due to thermal shutdown. The FPF2148 does not turn off in response to a over current condition but instead remain operating in a constant current mode so long as ONB is active and the thermal shutdown or under-voltage lockout have not activated.

### Fault Reporting

Upon the detection of an over-current, an input under-voltage, or an over-temperature condition, the FLAGB signals the fault mode by activating LO. And the FLAGB goes LO immediately. It will remain LO during the faults and immediately returns HI at the end of the fault condition. FLAGB is an open-drain output which requires a pull-up resistor between  $V_{IN}$  and FLAGB. During shutdown, the pull-down on FLAGB is disabled to reduce current draw from the supply.

### Current Limiting

The current limit ensures that the current through the switch doesn't exceed 400mA while not limiting at less than 200mA. The FPF2148 have no current limit blanking period so immediately upon a current limit condition FLAGB is activated. The part will remain in a constant current state until the ONB pin is deactivated or the thermal shutdown turns-off the switch.

### Under-Voltage Lockout

The under-voltage lockout turns-off the switch if the input voltage drops below the under-voltage lockout threshold. With the ONB pin active the input voltage rising above the under-voltage lockout threshold will cause a controlled turn-on of the switch which limits current over-shoots.

### Thermal Shutdown

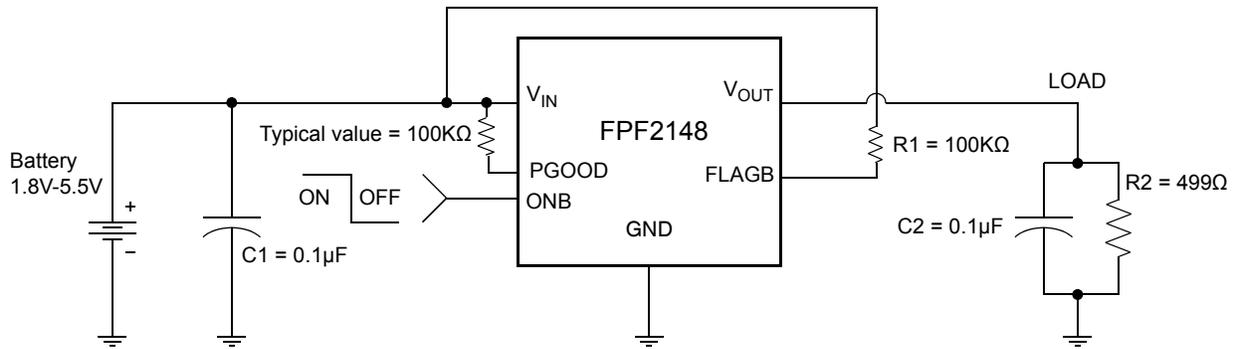
The thermal shutdown protects the die from internally or externally generated excessive temperatures. During an over-temperature condition the FLAGB is activated and the switch is turned-off. The switch automatically turns-on again if temperature of the die drops below the threshold temperature.

### Power Good

The PGOOD monitors the output voltage of the switch. When the ONB pin is active, PGOOD signal goes low as soon as  $V_{OUT}$  falls below 90% of  $V_{IN}$ . PGOOD is an open-drain output which requires a pull-up resistor between  $V_{IN}$  and PGOOD.

## Application Information

### Typical Application



### Input Capacitor

To limit the voltage drop on the input supply caused by transient in-rush currents when the switch turns-on into a discharged load capacitor or a short-circuit, a capacitor needs to be placed between  $V_{IN}$  and GND. A  $1\mu\text{F}$  ceramic capacitor,  $C_{IN}$ , placed close to the pins is usually sufficient. Higher values of  $C_{IN}$  can be used to further reduce the voltage drop.

### Output Capacitor

A  $0.1\mu\text{F}$  capacitor  $C_{OUT}$ , should be placed between  $V_{OUT}$  and GND. This capacitor will prevent parasitic board inductances from forcing  $V_{OUT}$  below GND when the switch turns-off. For the FPF2148, the total output capacitance needs to be kept below a maximum value,  $C_{OUT(max)}$ , to prevent the part from registering an over-current condition and turning-off the switch. The maximum output capacitance can be determined from the following formula,

$$C_{OUT(max)} = \frac{I_{LIM(max)} \times t_R(max)}{V_{IN}} \quad (1)$$

### Power Dissipation

During normal operation as a switch, the power dissipation is small and has little effect on the operating temperature of the part. The parts with the higher current limits will dissipate the most power and that will only be,

$$P = (I_{LIM})^2 \times R_{DS} = (0.4)^2 \times 0.15 = 24\text{mW} \quad (2)$$

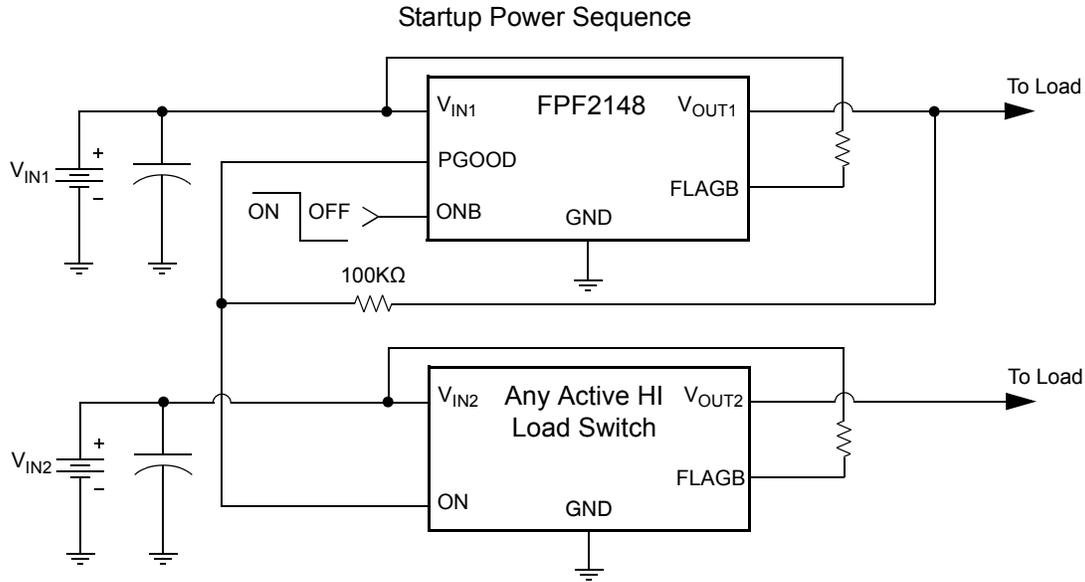
If the part goes into current limit the maximum power dissipation will occur when the output is shorted to ground. For the FPF2148, a short on the output will cause the part to operate in a constant current state dissipating a worst case power as calculated in (3) until the thermal shutdown activates. It will then cycle in and out of thermal shutdown so long as the ONB pin is active and the short is present.

$$\begin{aligned} P(max) &= V_{IN(max)} \times I_{LIM(max)} \\ &= 5.5 \times 0.4 = 275\text{mW} \end{aligned} \quad (3)$$

### Board Layout

For best performance, all traces should be as short as possible. To be most effective, the input and output capacitors should be placed close to the device to minimize the effects that parasitic trace inductances may have on normal and short-circuit operation. Using wide traces for  $V_{IN}$ ,  $V_{OUT}$  and GND will help minimize parasitic electrical effects along with minimizing the case to ambient thermal impedance.

## Application Notes



Power good function in sequential startup. No battery is loaded to the output

### Power Good

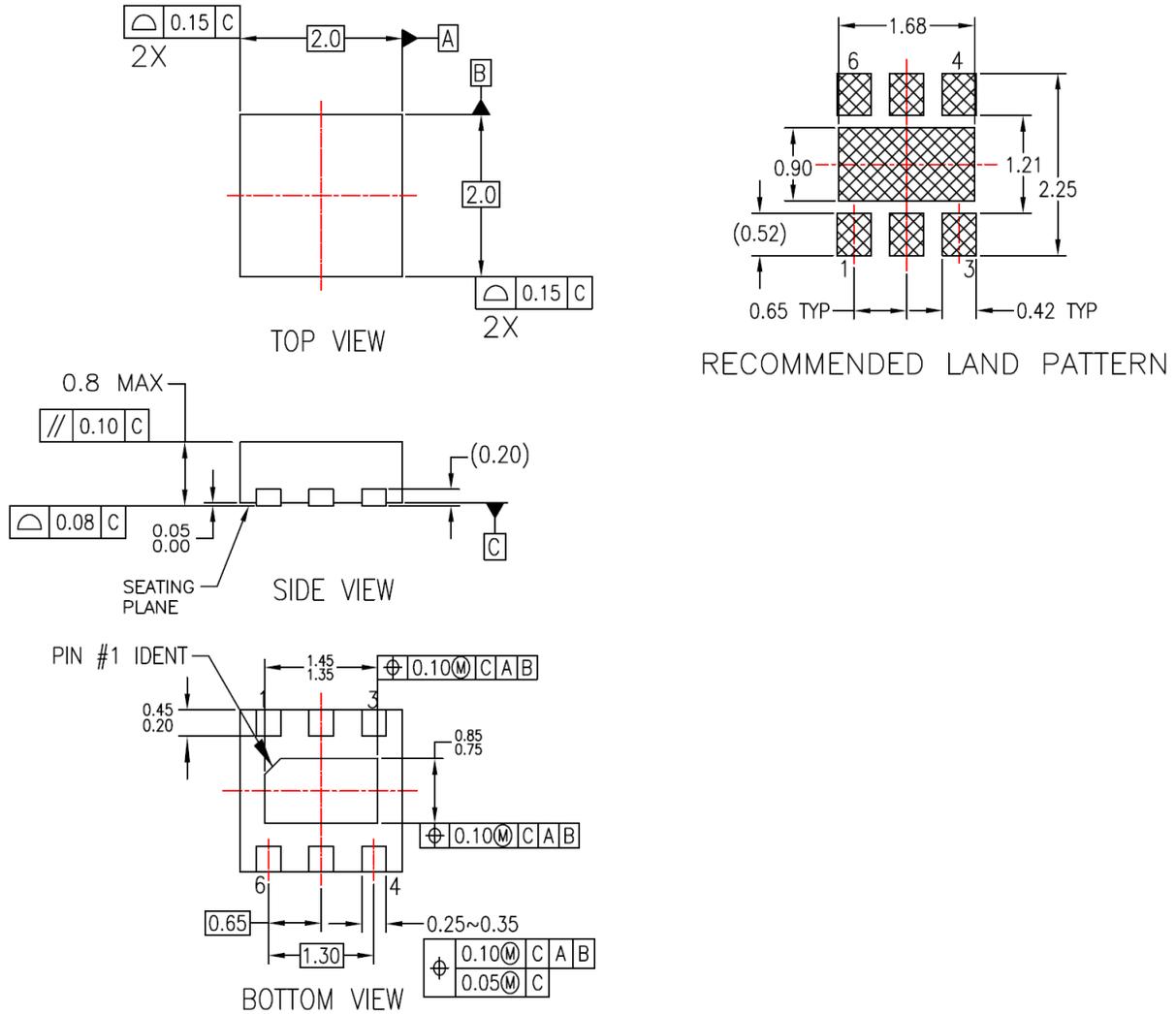
FPF2148 has a "Power Good" feature. PGOOD pin is an open-drain MOSFET which asserts high when the output voltage reaches 90% of the input voltage.

PGOOD pin requires an external pull up resistor that is connected to the output voltage when there is no battery in the load side and the logic level of the subsequent controller permits. This would give logic levels similar to a CMOS output stage for PGOOD, while still keeping the option to tie the pull-up to a different supply voltage. A 100KΩ is recommended to be used as pull up resistor. The PGOOD pin status is independent of the ONB pin position. This means that PGOOD pin stays low when the load switch is OFF. If the Power Good feature is not used in the application the pin can be connected directly to GND.

### Sequential Startup using Power Good

The power of FPF2148 PGOOD pin can be connected to another active HI load switch's enable pin to implement sequential startup. PGOOD pin asserts low when the load switch is OFF. This feature allows driving a subsequent circuit. The above diagram illustrates power good function in sequential startup. As the  $V_{OUT1}$  of the first load switch starts to ramp to the 90% of its input voltage the second switch remains in OFF state since the ON pin of the second switch stays low. Whereas the  $V_{OUT1}$  passes the 90% threshold, power good signal becomes active and asserts high. This signal will turn on the second load switch and  $V_{OUT2}$  will start to increase. The total startup time may vary according to the difference between supply voltages that are used in the application.

**Dimensional Outline and Pad Layout**



**NOTES:**

- A. NON-CONFORMS TO JEDEC REGISTRATION,
- B. DIMENSIONS ARE IN MILLIMETERS.
- C. DIMENSIONS AND TOLERANCES PER ASME Y14.5M, 1994

MLP06KrevA



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Fairchild Semiconductor <sup>®</sup>	MicroPak <sup>™</sup>	RapidConfigure <sup>™</sup>	TinyPWM <sup>™</sup>
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FAST <sup>®</sup>	OPTOPLANAR <sup>®</sup>	STEALTH <sup>™</sup>	UHC <sup>®</sup>
FastvCore <sup>™</sup>	 <sup>®</sup>	SuperFET <sup>™</sup>	UniFET <sup>™</sup>
FPS <sup>™</sup>	PDP-SPM <sup>™</sup>	SuperSOT <sup>™</sup> -3	VCX <sup>™</sup>
FRFET <sup>®</sup>	Power220 <sup>®</sup>	SuperSOT <sup>™</sup> -6	
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- A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

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Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.
Preliminary	First Production	This datasheet contains preliminary data; supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice to improve design.
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