

International Rectifier

RADIATION HARDENED POWER MOSFET THRU-HOLE (Low-Ohmic TO-257AA)

PD-96925B

IRHYS67230CM 200V, N-CHANNEL R₆ TECHNOLOGY

Product Summary

Part Number	Radiation Level	R _{Ds(on)}	I _D
IRHYS67230CM	100K Rads (Si)	0.13Ω	16A
IRHYS63230CM	300K Rads (Si)	0.13Ω	16A

International Rectifier's R₆™ technology provides superior power MOSFETs for space applications. These devices have improved immunity to Single Event Effect (SEE) and have been characterized for useful performance with Linear Energy Transfer (LET) up to 90MeV/(mg/cm²). Their combination of very low R_{Ds(on)} and faster switching times reduces power loss and increases power density in today's high speed switching applications such as DC-DC converters and motor controllers. These devices retain all of the well established advantages of MOSFETs such as voltage control, ease of paralleling and temperature stability of electrical parameters.



Features:

- Low R_{Ds(on)}
- Fast Switching
- Single Event Effect (SEE) Hardened
- Low Total Gate Charge
- Simple Drive Requirements
- Ease of Paralleling
- Hermetically Sealed
- Ceramic Eyelets
- Electrically Isolated
- Light Weight

Absolute Maximum Ratings

Pre-Irradiation

	Parameter	Units	
I _D @ V _{GS} = 12V, T _C = 25°C	Continuous Drain Current	A	16
I _D @ V _{GS} = 12V, T _C = 100°C	Continuous Drain Current		10
I _{DM}	Pulsed Drain Current ①		64
P _D @ T _C = 25°C	Max. Power Dissipation	W	75
	Linear Derating Factor	W/C	0.6
V _{GS}	Gate-to-Source Voltage	V	±20
E _{AS}	Single Pulse Avalanche Energy ②	mJ	83
I _{AR}	Avalanche Current ①	A	16
E _{AR}	Repetitive Avalanche Energy ①	mJ	7.5
dV/dt	Peak Diode Recovery dV/dt ③	V/ns	9.0
T _J	Operating Junction	°C	-55 to 150
T _{STG}	Storage Temperature Range		
	Lead Temperature		300 (0.063 in. /1.6 mm from case for 10s)
	Weight	g	4.3 (Typical)

For footnotes refer to the last page

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IRHYS67230CM

Pre-Irradiation

Electrical Characteristics @ $T_J = 25^\circ\text{C}$ (Unless Otherwise Specified)

	Parameter	Min	Typ	Max	Units	Test Conditions
BV_{DSS}	Drain-to-Source Breakdown Voltage	200	—	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\Delta \text{BV}_{\text{DSS}}/\Delta T_J$	Temperature Coefficient of Breakdown Voltage	—	0.19	—	$\text{V}/^\circ\text{C}$	Reference to 25°C , $\text{I}_D = 1.0\text{mA}$
$R_{\text{DS}(\text{on})}$	Static Drain-to-Source On-State Resistance	—	—	0.13	Ω	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 10\text{A}$ ④
$\text{V}_{\text{GS}(\text{th})}$	Gate Threshold Voltage	2.0	—	4.0	V	$\text{V}_{\text{DS}} = \text{V}_{\text{GS}}, \text{I}_D = 1.0\text{mA}$
g_{fs}	Forward Transconductance	11	—	—	$\text{S} (\text{f})$	$\text{V}_{\text{DS}} = 15\text{V}, \text{I}_{\text{DS}} = 10\text{A}$ ④
I_{DSS}	Zero Gate Voltage Drain Current	—	—	10	μA	$\text{V}_{\text{DS}} = 160\text{V}, \text{V}_{\text{GS}}=0\text{V}$
		—	—	25		$\text{V}_{\text{DS}} = 160\text{V}, \text{V}_{\text{GS}} = 0\text{V}, T_J = 125^\circ\text{C}$
I_{GSS}	Gate-to-Source Leakage Forward	—	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	—	-100		$\text{V}_{\text{GS}} = -20\text{V}$
Q_g	Total Gate Charge	—	—	42	nC	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 16\text{A}$
Q_{gs}	Gate-to-Source Charge	—	—	10		$\text{V}_{\text{DS}} = 100\text{V}$
Q_{gd}	Gate-to-Drain ('Miller') Charge	—	—	20		
$t_{\text{d}(\text{on})}$	Turn-On Delay Time	—	—	15	ns	$\text{V}_{\text{DD}} = 100\text{V}, \text{I}_D = 16\text{A}$ $\text{V}_{\text{GS}} = 12\text{V}, \text{R}_G = 7.5\Omega$
t_r	Rise Time	—	—	40		
$t_{\text{d}(\text{off})}$	Turn-Off Delay Time	—	—	35		
t_f	Fall Time	—	—	15		
$L_{\text{S}} + L_{\text{D}}$	Total Inductance	—	6.8	—	nH	Measured from Drain lead (6mm / 0.25in. from package) to Source lead (6mm / 0.25in. from package)
C_{iss}	Input Capacitance	—	1660	—	pF	$\text{V}_{\text{GS}} = 0\text{V}, \text{V}_{\text{DS}} = 25\text{V}$ $f = 1.0\text{MHz}$
C_{oss}	Output Capacitance	—	206	—		
C_{rss}	Reverse Transfer Capacitance	—	2.6	—		
R_g	Internal Gate Resistance	—	1.75	—	Ω	$f = 1.0\text{MHz}$, open drain

Source-Drain Diode Ratings and Characteristics

	Parameter	Min	Typ	Max	Units	Test Conditions
I_S	Continuous Source Current (Body Diode)	—	—	16	A	
I_{SM}	Pulse Source Current (Body Diode) ①	—	—	64		
V_{SD}	Diode Forward Voltage	—	—	1.2	V	$T_J = 25^\circ\text{C}, I_S = 16\text{A}, \text{V}_{\text{GS}} = 0\text{V}$ ④
t_{rr}	Reverse Recovery Time	—	—	300	ns	$T_J = 25^\circ\text{C}, I_F = 16\text{A}, dI/dt \leq 100\text{A}/\mu\text{s}$ $\text{V}_{\text{DD}} \leq 25\text{V}$ ④
Q_{RR}	Reverse Recovery Charge	—	—	3.2	μC	
t_{on}	Forward Turn-On Time	Intrinsic turn-on time is negligible. Turn-on speed is substantially controlled by $L_{\text{S}} + L_{\text{D}}$.				

Thermal Resistance

	Parameter	Min	Typ	Max	Units	Test Conditions
R_{thJC}	Junction-to-Case	—	—	1.67	$^\circ\text{C/W}$	
R_{thJA}	Junction-to-Ambient	—	—	80		Typical Socket Mount

Note: Corresponding Spice and Saber models are available on International Rectifier Web site.

For footnotes refer to the last page

Radiation Characteristics

IRHYS67230CM

International Rectifier Radiation Hardened MOSFETs are tested to verify their radiation hardness capability. The hardness assurance program at International Rectifier is comprised of two radiation environments. Every manufacturing lot is tested for total ionizing dose (per notes 5 and 6) using the TO-3 package. Both pre- and post-irradiation performance are tested and specified using the same drive circuitry and test conditions in order to provide a direct comparison.

Table 1. Electrical Characteristics @ $T_j = 25^\circ\text{C}$, Post Total Dose Irradiation ⁽⁵⁾⁽⁶⁾

	Parameter	Up to 300K Rads (Si) ¹		Units	Test Conditions
		Min	Max		
BV_{DSS}	Drain-to-Source Breakdown Voltage	200	—	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 1.0\text{mA}$
$\text{V}_{\text{GS(th)}}$	Gate Threshold Voltage	2.0	4.0		$\text{V}_{\text{GS}} = \text{V}_{\text{DS}}, \text{I}_D = 1.0\text{mA}$
I_{GSS}	Gate-to-Source Leakage Forward	—	100	nA	$\text{V}_{\text{GS}} = 20\text{V}$
I_{GSS}	Gate-to-Source Leakage Reverse	—	-100		$\text{V}_{\text{GS}} = -20\text{V}$
I_{DSS}	Zero Gate Voltage Drain Current	—	10	μA	$\text{V}_{\text{DS}}=160\text{V}, \text{V}_{\text{GS}}=0\text{V}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source ⁽⁴⁾ On-State Resistance (TO-3)	—	0.134	Ω	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 10\text{A}$
$\text{R}_{\text{DS(on)}}$	Static Drain-to-Source On-State ⁽⁴⁾ Resistance (Low Ohmic TO-257)	—	0.13	Ω	$\text{V}_{\text{GS}} = 12\text{V}, \text{I}_D = 10\text{A}$
V_{SD}	Diode Forward Voltage ⁽⁴⁾	—	1.2	V	$\text{V}_{\text{GS}} = 0\text{V}, \text{I}_D = 16\text{A}$

1. Part numbers IRHYS67230CM and IRHYS63230CM

International Rectifier radiation hardened MOSFETs have been characterized in heavy ion environment for Single Event Effects (SEE). Single Event Effects characterization is illustrated in Fig. a and Table 2.

Table 2. Single Event Effect Safe Operating Area

Ion	LET (MeV/(mg/cm ²))	Energy (MeV)	Range (μm)	VDS (V)			
				@ $\text{VGS} = 0\text{V}$	@ $\text{VGS} = -5\text{V}$	@ $\text{VGS} = -10\text{V}$	@ $\text{VGS} = -15\text{V}$
Xe	43	2441	205	200	200	200	190
Xe	59	825	66	200	200	200	190
Au	90	1480	80	170	170	--	--

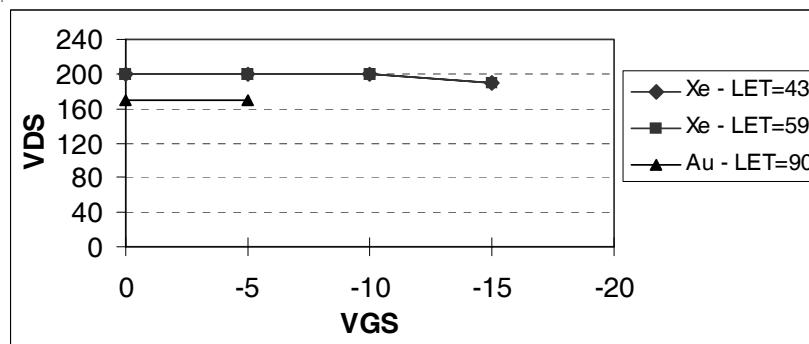


Fig a. Single Event Effect, Safe Operating Area

For footnotes refer to the last page

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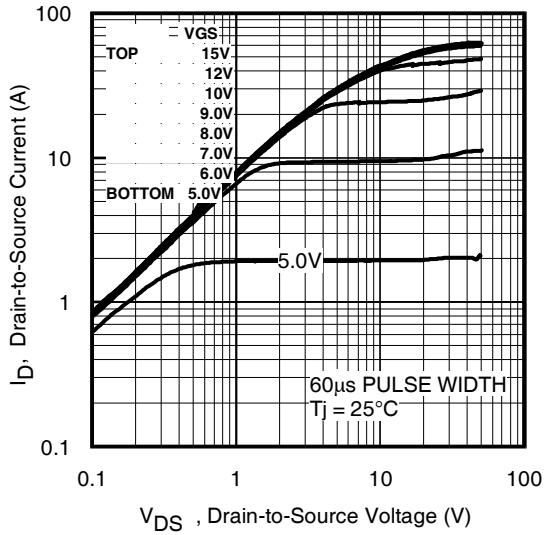


Fig 1. Typical Output Characteristics

Pre-Irradiation

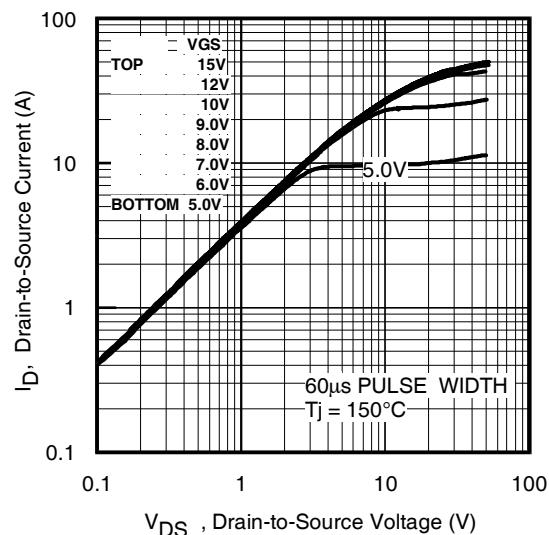


Fig 2. Typical Output Characteristics

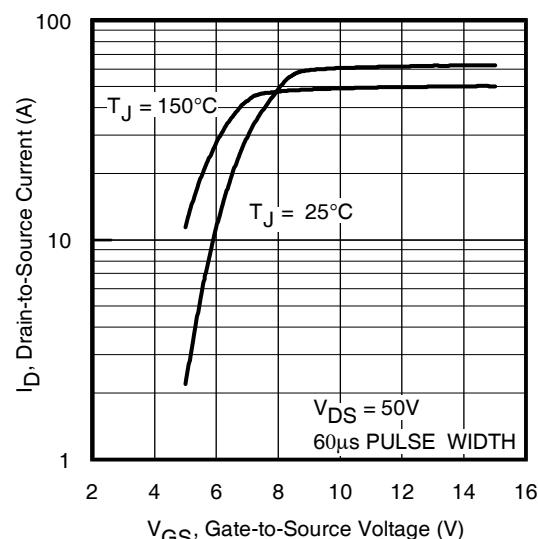


Fig 3. Typical Transfer Characteristics

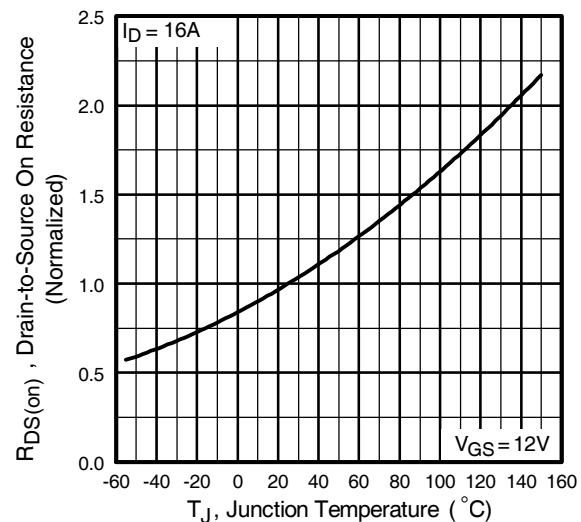


Fig 4. Normalized On-Resistance Vs. Temperature

Pre-Irradiation

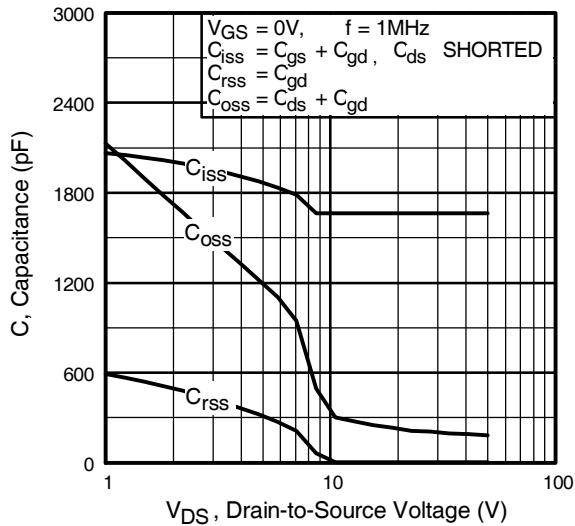


Fig 5. Typical Capacitance Vs.
Drain-to-Source Voltage

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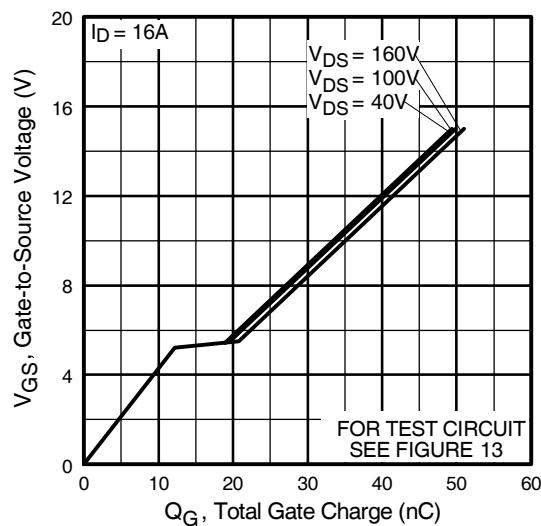


Fig 6. Typical Gate Charge Vs.
Gate-to-Source Voltage

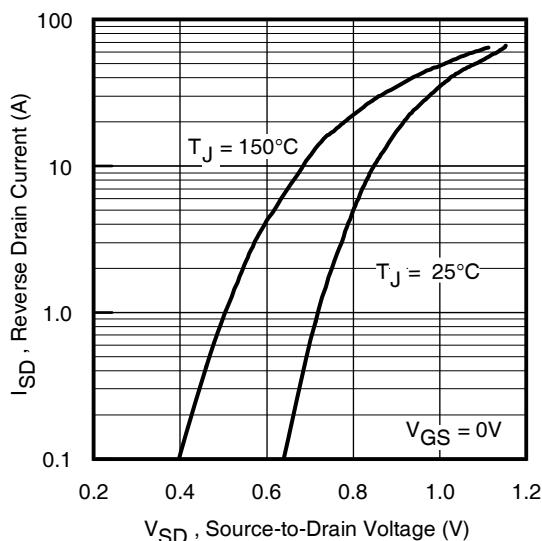


Fig 7. Typical Source-Drain Diode
Forward Voltage

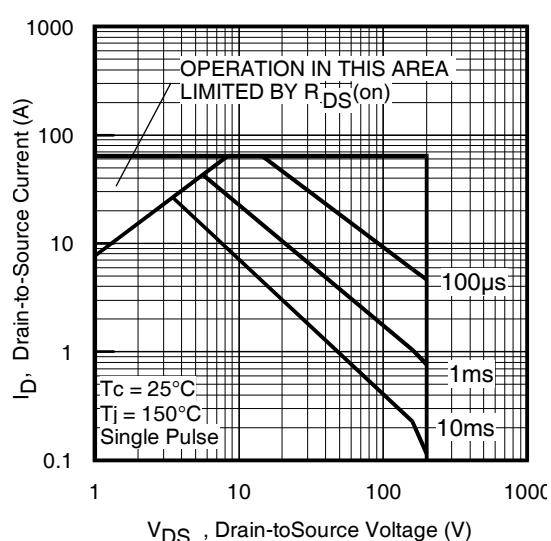


Fig 8. Maximum Safe Operating Area

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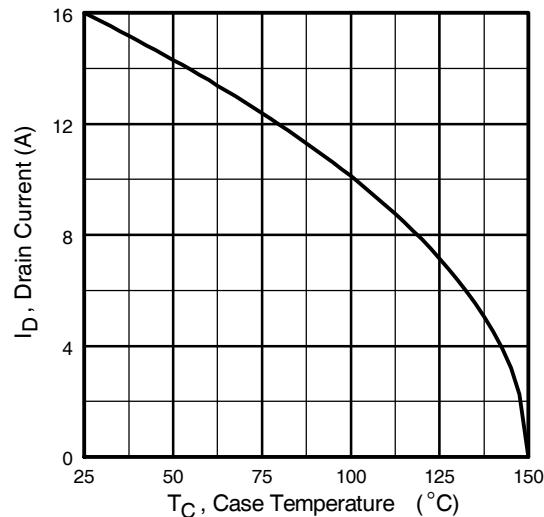


Fig 9. Maximum Drain Current Vs.
Case Temperature

Pre-Irradiation

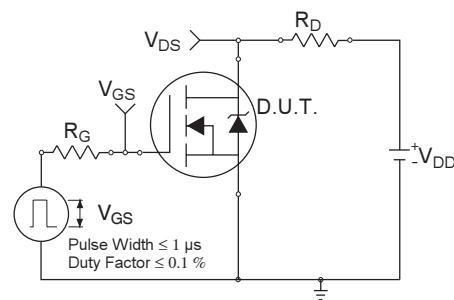


Fig 10a. Switching Time Test Circuit

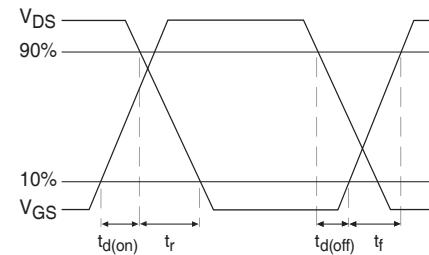


Fig 10b. Switching Time Waveforms

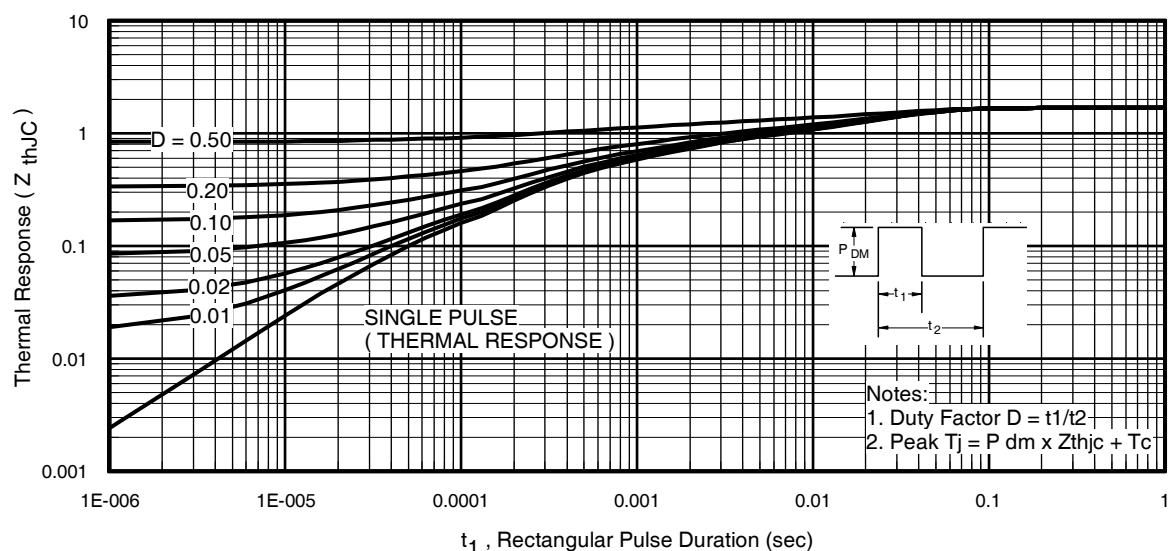


Fig 11. Maximum Effective Transient Thermal Impedance, Junction-to-Case

Pre-Irradiation

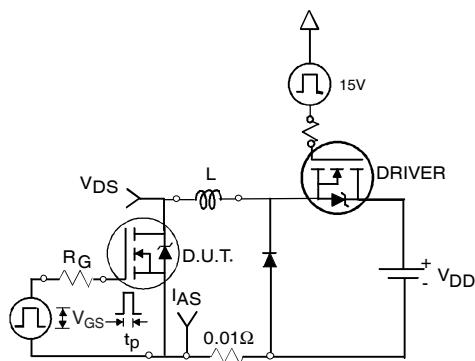


Fig 12a. Unclamped Inductive Test Circuit

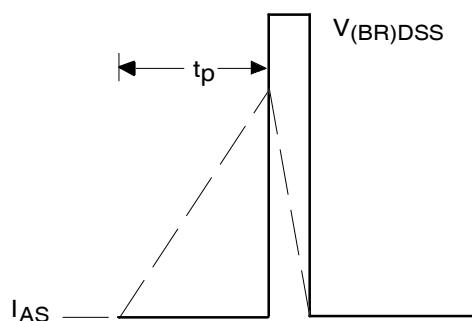


Fig 12b. Unclamped Inductive Waveforms

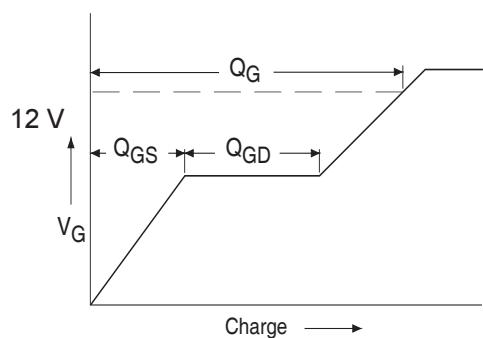


Fig 13a. Basic Gate Charge Waveform

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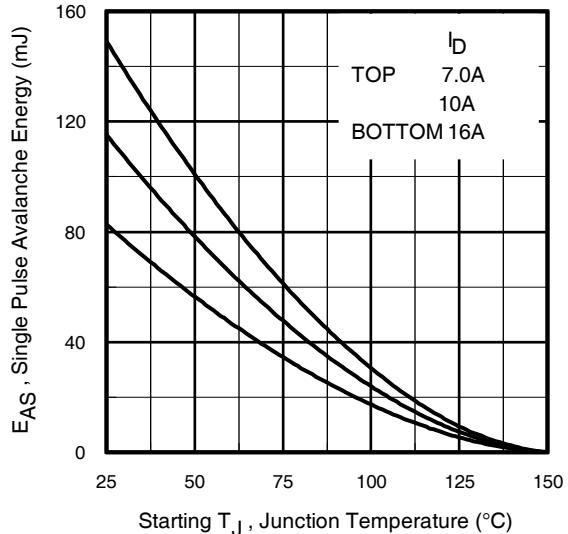


Fig 12c. Maximum Avalanche Energy Vs. Drain Current

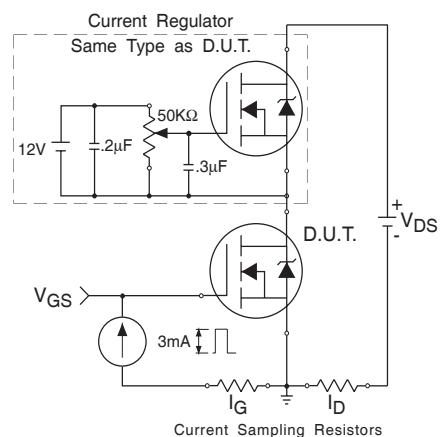


Fig 13b. Gate Charge Test Circuit

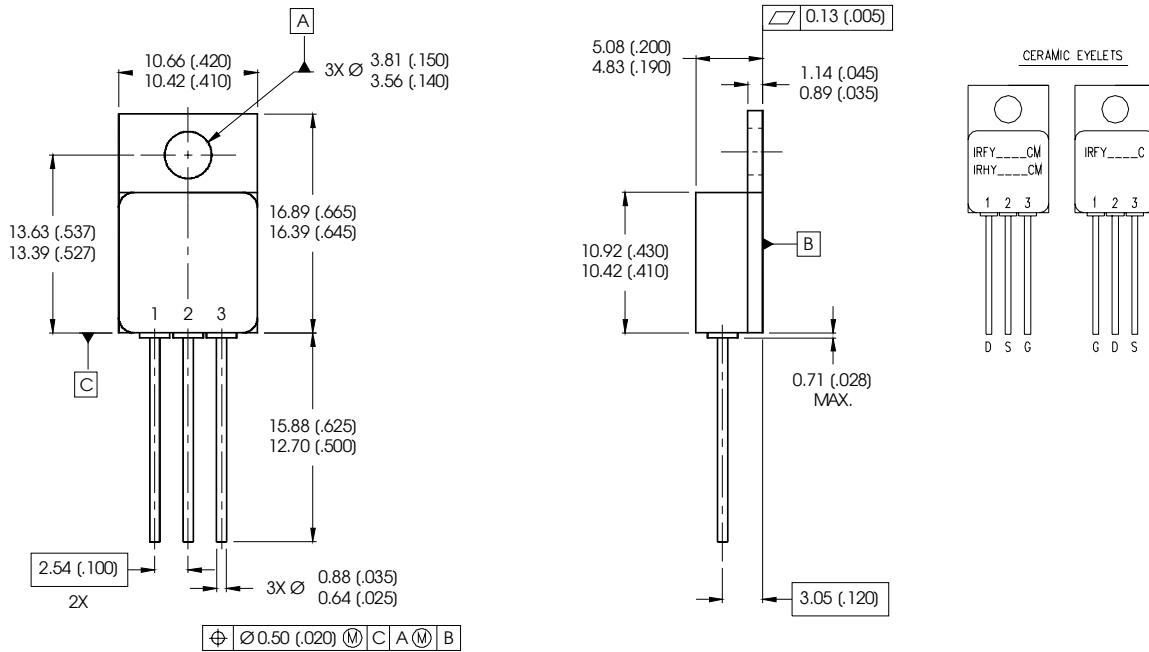
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Pre-Irradiation

Footnotes:

- ① Repetitive Rating; Pulse width limited by maximum junction temperature.
- ② $V_{DD} = 25V$, starting $T_J = 25^\circ C$, $L = 0.65mH$
Peak $I_L = 16A$, $V_{GS} = 12V$
- ③ $ISD \leq 16A$, $dI/dt \leq 750A/\mu s$,
 $V_{DD} \leq 200V$, $T_J \leq 150^\circ C$
- ④ Pulse width $\leq 300 \mu s$; Duty Cycle $\leq 2\%$
- ⑤ **Total Dose Irradiation with V_{GS} Bias.**
12 volt V_{GS} applied and $V_{DS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.
- ⑥ **Total Dose Irradiation with V_{DS} Bias.**
160 volt V_{DS} applied and $V_{GS} = 0$ during irradiation per MIL-STD-750, method 1019, condition A.

Case Outline and Dimensions — Low-Ohmic TO-257AA



NOTES:

1. DIMENSIONING & TOLERANCING PER ANSI Y14.5M-1994.
2. CONTROLLING DIMENSION: INCH.
3. DIMENSIONS ARE SHOWN IN MILLIMETERS (INCHES).
4. OUTLINE CONFORMS TO JEDEC OUTLINE TO-257AA.

LEAD ASSIGNMENTS

- 1 = DRAIN
- 2 = SOURCE
- 3 = GATE

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