



TYN606
TYN1006

STANDARD

6A SCRs

Table 1: Main Features

Symbol	Value	Unit
$I_{T(RMS)}$	6	A
V_{DRM}/V_{RRM}	600 and 1000	V
I_{GT}	15	mA

DESCRIPTION

The **TYN606** and **TYN1006** family of Silicon Controlled Rectifiers are high performance glass passivated technology.

This general purpose Family of Silicon Controlled Rectifiers is designed for power supply up to 400Hz on resistive or inductive load.

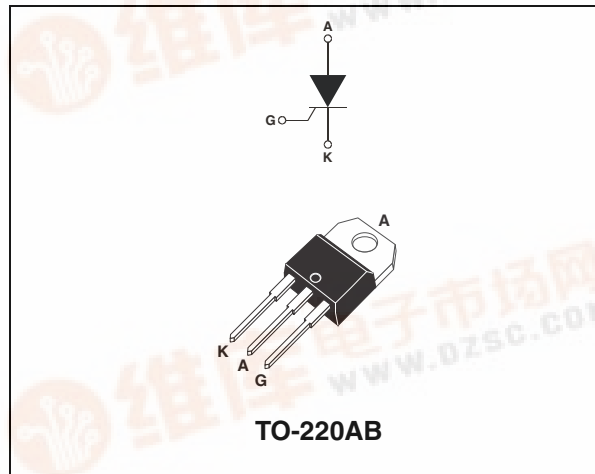


Table 2: Order Codes

Part Numbers	Marking
TYN606RG	TYN606
TYN1006RG	TYN1006

Table 3: Absolute Ratings (limiting values)

Symbol	Parameter		Value	Unit
$I_{T(RMS)}$	RMS on-state current (180° conduction angle)		$T_c = 110^\circ\text{C}$	A
$I_{T(AV)}$	Average on-state current (180° conduction angle)		$T_c = 110^\circ\text{C}$	A
I_{TSM}	Non repetitive surge peak on-state current	$t_p = 8.3 \text{ ms}$	$T_j = 25^\circ\text{C}$	A
		$t_p = 10 \text{ ms}$		
I^2t	I^2t Value for fusing		$T_j = 25^\circ\text{C}$	A^2s
di/dt	Critical rate of rise of on-state current $I_G = 100 \text{ mA}$, $dI_G/dt = 0.1 \text{ A}/\mu\text{s}$		$T_j = 125^\circ\text{C}$	$A/\mu\text{s}$
I_{GM}	Peak gate current	$t_p = 20 \mu\text{s}$	$T_j = 125^\circ\text{C}$	A
$P_{G(AV)}$	Average gate power dissipation		$T_j = 125^\circ\text{C}$	W
P_{GM}	Maximum gate power	$t_p = 20 \mu\text{s}$	$T_j = 125^\circ\text{C}$	W
V_{DRM} V_{RRM}	Repetitive peak off-state voltage	TYN606	$T_j = 125^\circ\text{C}$	600
		TYN1006		1000
T_{stg} T_j	Storage junction temperature range Operating junction temperature range		- 40 to + 150 - 40 to + 125	$^\circ\text{C}$
T_L	Maximum lead temperature for soldering during 10s at 2mm from case		260	$^\circ\text{C}$



Tables 4: Electrical Characteristics ($T_j = 25^\circ\text{C}$, unless otherwise specified)

Symbol	Test Conditions			Value	Unit	
I _{GT}	V _D = 12 V (D.C.) R _L = 33 Ω		MAX.	15	mA	
V _{GT}			MAX.	1.5	V	
V _{GD}	V _D = V _{DRM} R _L = 3.3 kΩ	T _j = 110°C	MIN.	0.2	V	
t _{gt}	V _D = V _{DRM} I _G = 40 mA dl _G /dt = 0.5 A/μs		TYP.	2	μs	
I _H	I _T = 100 mA Gate open		MAX.	30	mA	
I _L	I _G = 1.2 x I _{GT}		TYP.	50	mA	
dV/dt	Linear slope up to: V _D = 67 % V _{DRM} Gate open	T _j = 110°C	MIN.	200	V/μs	
V _{TM}	I _{TM} = 12 A tp = 380 μs		MAX.	1.6	V	
I _{DRM} I _{RRM}	V _{DRM} = V _{RRM}	T _j = 25°C	MAX.	10	μA	
		T _j = 110°C		2	mA	
t _q	V _D = 67 % V _{DRM} I _{TM} = 12 A V _R = 25 V dl _{TM} /dt = 30 A/μs dV _D /dt = 50 V/μs		T _j = 110°C	TYP.	70	μs

Table 5: Thermal Resistance

Symbol	Parameter	Value	Unit
$R_{th(j-c)}$	Junction to case (D.C.)	2.5	$^\circ\text{C}/\text{W}$
$R_{th(j-a)}$	Junction to ambient	60	$^\circ\text{C}/\text{W}$

Figure 1: Maximum average power dissipation versus average on-state current

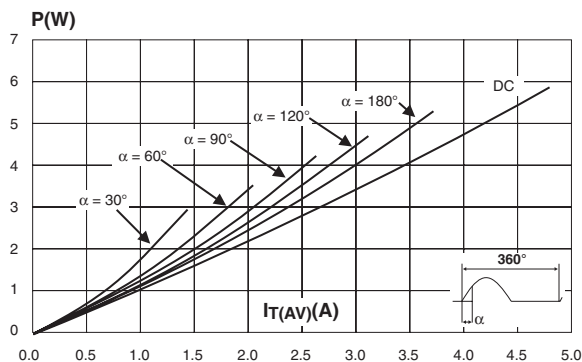


Figure 2: Correlation between maximum average power dissipation and maximum allowable temperature (T_{amb} and T_{lead})

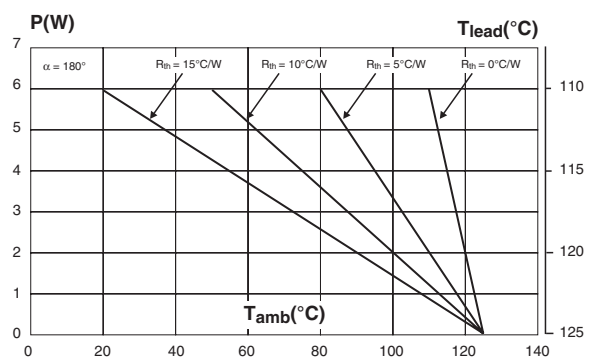


Figure 3: Average on-state current versus case temperature

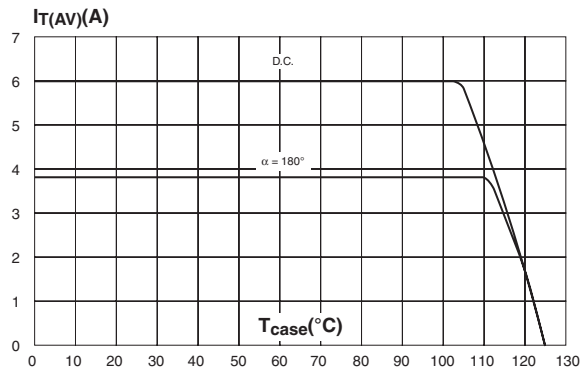


Figure 4: Relative variation of thermal impedance versus pulse duration

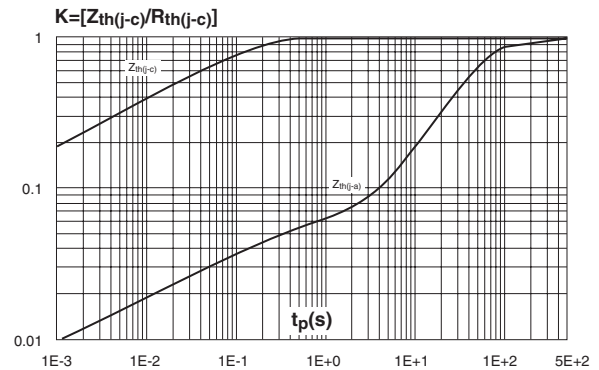


Figure 5: Relative variation of gate trigger current versus junction temperature

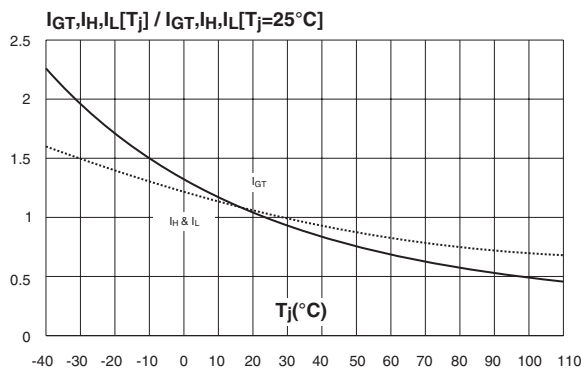


Figure 6: Surge peak on-state current versus number of cycles

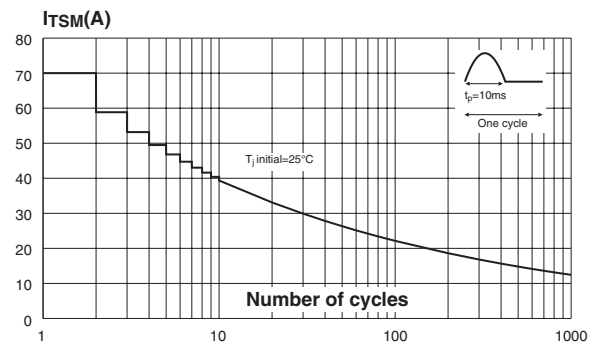


Figure 7: Non-repetitive surge peak on-state current for a sinusoidal pulse with width $t_p < 10$ ms, and corresponding values of I^2t

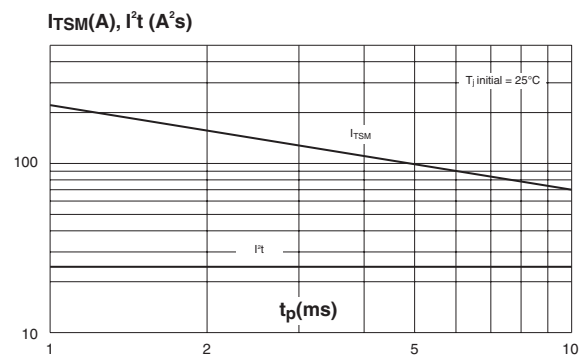
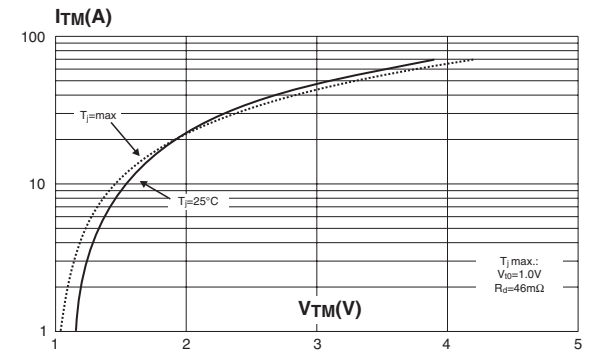


Figure 8: On-state characteristics (maximum values)



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Figure 9: Ordering Information Scheme

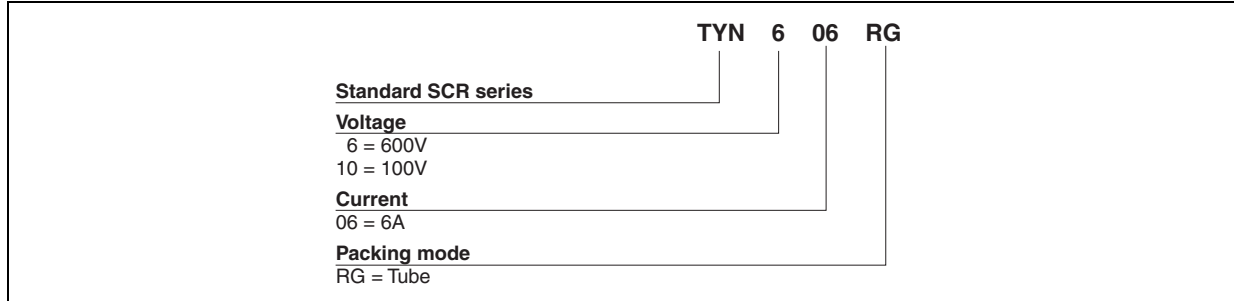


Table 6: Product Selector

Part Numbers	Voltage (xx)		Sensitivity	Package
	600 V	1000 V		
TYN606RG	X		15 mA	TO-220AB
TYN1006RG		X	15 mA	TO-220AB

Figure 10: TO-220AB Package Mechanical Data

REF.	DIMENSIONS					
	Millimeters			Inches		
	Min.	Typ.	Max.	Min.	Typ.	Max.
A	15.20		15.90	0.598		0.625
a1		3.75			0.147	
a2	13.00		14.00	0.511		0.551
B	10.00		10.40	0.393		0.409
b1	0.61		0.88	0.024		0.034
b2	1.23		1.32	0.048		0.051
C	4.40		4.60	0.173		0.181
c1	0.49		0.70	0.019		0.027
c2	2.40		2.72	0.094		0.107
e	2.40		2.70	0.094		0.106
F	6.20		6.60	0.244		0.259
ØI	3.75		3.85	0.147		0.151
I4	15.80	16.40	16.80	0.622	0.646	0.661
L	2.65		2.95	0.104		0.116
I2	1.14		1.70	0.044		0.066
I3	1.14		1.70	0.044		0.066
M		2.60			0.102	

In order to meet environmental requirements, ST offers these devices in ECOPACK® packages. These packages have a Lead-free second level interconnect. The category of second level interconnect is marked on the package and on the inner box label, in compliance with JEDEC Standard JESD97. The maximum ratings related to soldering conditions are also marked on the inner box label. ECOPACK is an ST trademark. ECOPACK specifications are available at: www.st.com.

Table 7: Ordering Information

Ordering type	Marking	Package	Weight	Base qty	Delivery mode
TYN606RG	TYN606	TO-220AB	2.3 g	50	Tube
TYN1006RG	TYN1006				

Table 8: Revision History

Date	Revision	Description of Changes
Sep-2001	1A	First issue.
13-Feb-2006	2	TO-220AB delivery mode changed from bulk to tube. ECOPACK statement added.

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