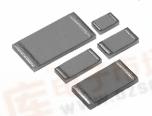


Vishay Foil Resistors

Ultra High Precision Z-Foil Flip Chip Resistor with TCR of ± 0.05 ppm/°C, 35 % Space Saving versus Wraparound Design and PCR of 5 ppm at Rated Power



Bottom View

Any value at tolerance available within resistance range

INTRODUCTION

One of the most important parameters influencing stability is the Temperature Coefficient of Resistance (TCR). Although the TCR of Bulk Metal® Foil resistors is considered extremely low, this characteristic has been further refined over the years.

The VFCP Series utilizes ultra precision Bulk Metal® Z-Foil.

The Z-Foil technology provides a significant reduction to the resistive element's sensitivity to ambient temperature variations (TCR) and to self heating when power is applied (power coefficient).

Along with the inherently low PCR and TCR, Z-Foil technology also provides remarkably improved load life stablility, low noise and availability of tight tolerance.

The Flip Chip configuration provides a substantial PCB space saving of more than 35 % vs. a surface mount chip with wraparound terminations. The VFCP is available in any value within the specified resistance range.

Our Application Engineering Department is available to advise and make recommendations. For non-standard technical requirements and special applications, please contact us.

	TABLE 1 - TOLERANCE AND TCR VS RESISTANCE VALUE					
	RESISTANCE VALUE (Ω)	TOLERANCE (%)	TYPICAL TCR AND MAX. SPREAD (- 55 °C to + 125 °C, + 25 °C Ref.)			
	250 to 150K	± 0.01	± 0.2 ± 1.6			
	100 to < 250	± 0.02	± 0.2 ± 1.6			
	50 to < 100	± 0.05	± 0.2 ± 1.8			
	25 to < 50	± 0.1	± 0.2 ± 2.8			
4	P10 to < 25	+ 0.25	+ 0.2 + 2.8			

FEATURES Temperature Coefficient of Resistance (TCR):

 \pm 0.05 ppm/°C typical (0 °C to + 60 °C) ± 0.2 ppm/°C typical (- 55 °C to + 125 °C, 25 °C Ref.)



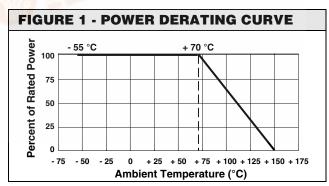
Tolerance: to ± 0.01 % (100 ppm)

- Power Coefficient "AR due to self heating" 5 ppm at Rated Power
- Load Life Stability (70 °C for 2000 hours): ± 0.005 % (50 ppm)
- Power Rating to: 600 mW at + 70 °C
- Electrostatic Discharge (ESD) above 25 000 V
- Resistance Range: 10Ω to $150 \text{ k}\Omega$ (for lower and higher values, please contact us)
- Non Inductive, Non Capacitive Design
- Short time overload ≤ 0.005 % (50 ppm)
- Non hot spot design
- Rise time: 1ns without ringing
- Current Noise: 40 dB
- Voltage Coefficient < 0.1 ppm/V
- Non Inductive: < 0.08 μH
- Terminal Finishes available: Lead (Pb)-free Tin/Lead Alloy
- Matched sets are available per request
- Any value available within resistance range (e.g. 1K2345)
- Prototype samples available from 48 hours. For more information, please contact foil@vishav.com
- For better performances please contact us

APPLICATIONS

- Automatic Test Equipment (ATE)
- High Precision Instrumentation
- · Laboratory, Industrial and Medical
- EB Applications (electron beam scanning and recording) equipment, electron microscopes)

- Down Hole instrumentation
 Communication



Po containing terminations are not RoHS compliant, exemptions may apply

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TABLE 2 - DSCC SPECIFICATIONS (Vishay resistors are listed on the following DSCC specifications)						
MODEL	DSCC	MIL SPEC				
VFCP1206	2009	MIL-PRF-55342				

Note

[•] For VFCP0805, VFCP1506, VFCP2010 and VFCP2512 DSCC Drawings, please contact foil@vishay.com

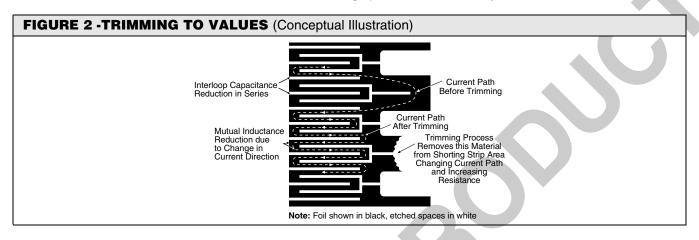


TABLE 3 - SPECIFICATIONS					
CHIP SIZE	RATED POWER (mW) at + 70 °C	$\begin{array}{c} \text{MAXIMUM} \\ \text{VOLTAGE} \\ \text{RATING} \\ (\leq \sqrt{P \times R} \) \end{array}$	RESISTANCE RANGE (Ω)	MAXIMUM WEIGHT (mg)	
0805	100 mW	34 V	10 to 12K	5.2	
1206	250 mW	86 V	10 to 30K	10.3	
1506	300 mW	109 V	10 to 40K	12	
2010	400 mW	200 V	10 to 100K	25	
2512	600 mW	220 V	10 to 150K	35	

TABLE 4 - LOAD LIFE STABILITY (+ 70 °C FOR 2000 HOURS)		
CHIP SIZE	MAXIMUM AR LIMITS	
0805	± 0.005 % at 50 mW ± 0.01 % at 100 mW	
1206	± 0.005 % at 150 mW ± 0.01 % at 250 mW	
1506	± 0.005 % at 150 mW ± 0.01 % at 300 mW	
2010	± 0.005 % at 200mW ± 0.01 % at 400 mW	
2512	± 0.005 % at 500 mW ± 0.01 % at 600 mW	

TABLE 5 - PERFORMANCES				
TEST OR CONDITION	MIL-PRF-55342 CHARACTERISTIC E AR LIMITS	TYPICAL AR LIMITS	MAXIMUM ∆R LIMITS ¹⁾	
Thermal Shock	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)	
Low Temperature Operation	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)	
Short Time Overload	± 0.1 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)	
High Temperature Exposure	± 0.1 %	± 0.01 % (100 ppm)	± 0.02 % (200 ppm)	
Resistance to Soldering Heat	± 0.2 %	± 0.005 % (50 ppm)	± 0.015 % (150 ppm)	
Moisture Resistance	± 0.2 %	± 0.005 % (50 ppm)	± 0.02 % (200 ppm)	
Load Life Stability + 70 °C for 2000 hours at Rated Power	± 0.5 %	± 0.005 % (50 ppm)	± 0.01 % (100 ppm)	

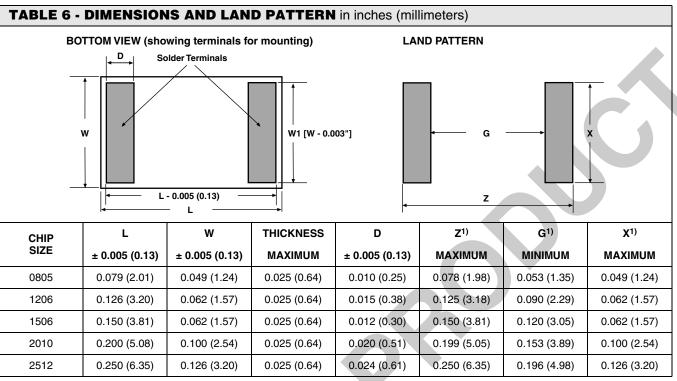
Note

1. As shown + 0.01 W to allow for measurement errors at low values.

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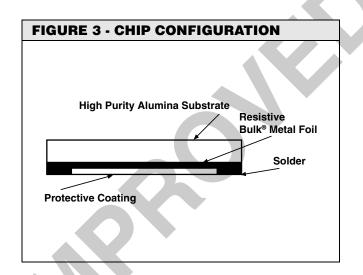


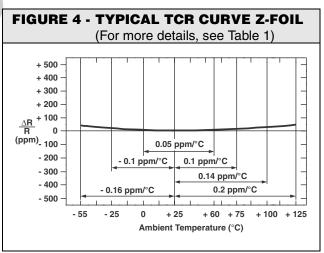
Ultra High Precision Z-Foil Flip Chip Resistor with TCR Vishay Foil Resistors of ± 0.05 ppm/°C, 35 % Space Saving versus
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Note

1. Land Pattern Dimensions are per IPC-782





Note

• The TCR values for < 100Ω are influenced by the termination composition and result in deviation from this curve

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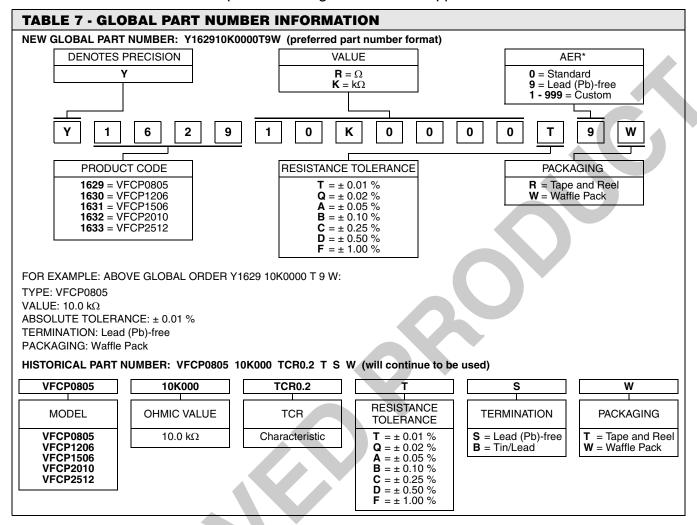


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Note

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^{*} For non-standard requests, please contact Application Engineering.



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