



V-INFINITY
a division of CUI INC

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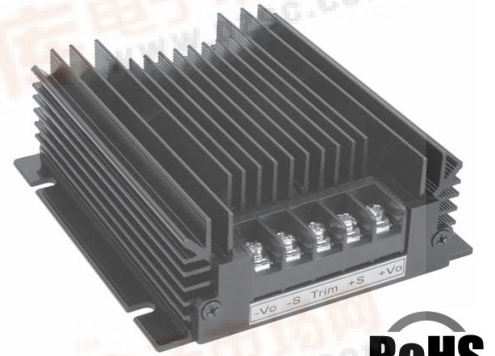
date 04/2008

PART NUMBER: VHK75W**DESCRIPTION:** chassis mount dc-dc converter**description**

The VHK75W series of rugged DC-DC converters are designed for high reliability applications, featuring integral heat sink, over-temperature protection, over-current protection and input transient voltage protection. With an external input fuse, the converter can protect accidental input polarity reversal. The wide 4:1 input range (9-36V or 18-75V) is very useful to stabilize an input source like batteries in various discharging and charging conditions. Additionally, high efficiency, fast response, tight regulations, remote sense and remote On/Off control make these converters very useful in many industrial and communications applications.

features

- 50-75W isolated output
- Efficiency to 85%
- RoHS Compliant
- 4:1 input range
- Regulated output
- Continuous short circuit protection
- Metal enclosure with integrated heatsink
- Rugged design
- Over-temperature shutdown

**MODEL**

	input voltage		output voltage (V dc)	output current ¹ (A)	input current		efficiency typ. (%)
	nominal (V dc)	range (V dc)			no load (mA)	full load (mA)	
VHK75W-Q24-S3R3	24	9.0~36.0	3.3	15	50	2611	79
VHK75W-Q24-S5	24	9.0~36.0	5	15	50	3811	82
VHK75W-Q24-S12	24	9.0~36.0	12	6.25	50	3765	83
VHK75W-Q24-S15	24	9.0~36.0	15	5	50	3720	84
VHK75W-Q24-S24	24	9.0~36.0	24	3.12	50	3720	84
VHK75W-Q48-S3R3	48	18.0~75.0	3.3	15	50	1289	80
VHK75W-Q48-S5	48	18.0~75.0	5	15	50	1883	83
VHK75W-Q48-S12	48	18.0~75.0	12	6.25	50	1860	84
VHK75W-Q48-S15	48	18.0~75.0	15	5	50	1838	85
VHK75W-Q48-S24	48	18.0~75.0	24	3.12	50	1835	85

notes: 1. see output derating curve (page 4)

INPUT

parameter	conditions/description	min	nom	max	units
input voltage range		9	24	36	V dc
		18	48	75	V dc
under voltage lockout	24 Vin power up		8.8		V dc
	24 Vin power down		8		V dc
	48 Vin power up		17		V dc
	48 Vin power down		16		V dc
remote on/off control ²	section 15 in the application notes				
input filter	PI type				

notes: 2. add suffix "N" to the model number for negative logic on/off control



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OUTPUT

parameter	conditions/description	min	nom	max	units
voltage accuracy				±1	%
transient response	25% step load change			500	μ sec
external trim adj. range				±10	%
ripple & noise (20MHz BW)	3.3V, 5V			40	mV RMS
				100	mV pk-pk
	12V& 15V			60	mV RMS
				150	mV pk-pk
	24V			100	mV RMS
				240	mV pk-pk
temperature coefficient			±0.03		%/°C
short circuit protection	continuous				
line regulation ³				±0.2	%
load regulation ⁴				±0.2	%
over voltage protection trip range, % Vo nom.		115		140	%
over current protection	nominal output	110		160	%

GENERAL SPECIFICATIONS

parameter	conditions/description	min	nom	max	units
switching frequency			300		KHz
operating ambient temp. ⁵		-40		85	°C
storage temperature		-55		105	°C
thermal shutdown case temp.			85		°C
case material	aluminum / steel				

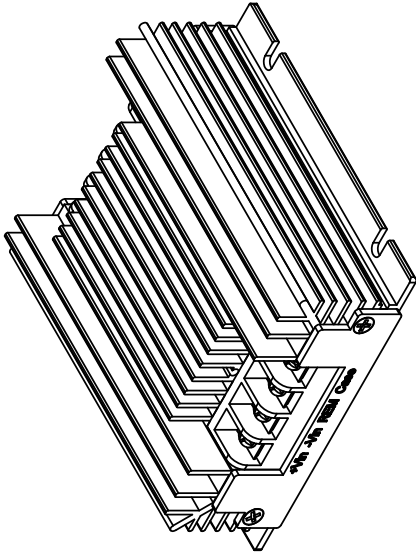
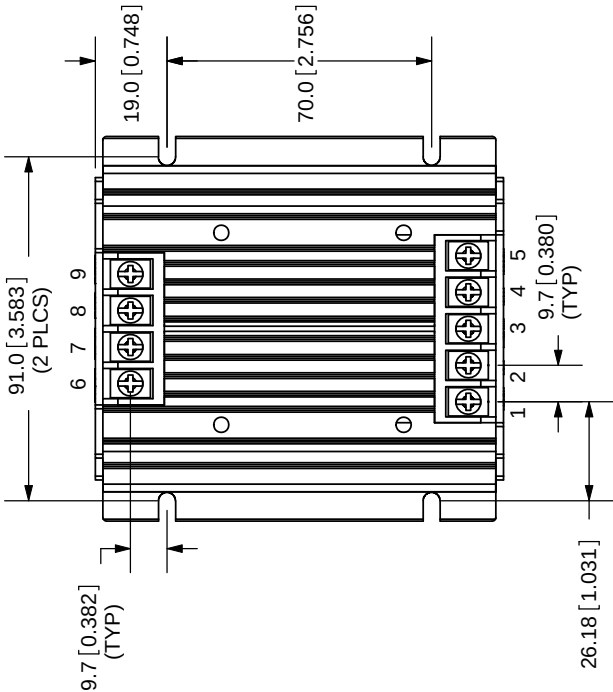
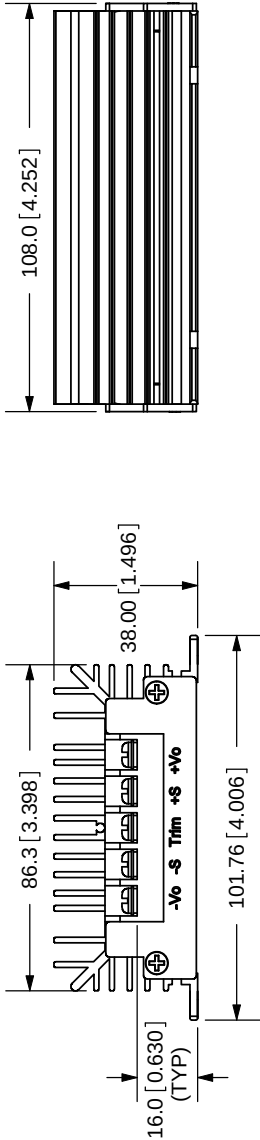
ISOLATION SPECIFICATIONS

parameter	conditions/description	min	nom	max	units
isolation voltage	input/output	1500			V dc
	input/case	1500			V dc
	output/case	1500			V dc
isolation resistance		100			MΩ

notes:

3. measured from high line to low line at full load
4. measured from full load to zero load at nominal input
5. see output derating curve (page 4)

REV.	DESCRIPTION	DATE
A	NEW DRAWING	4/22/2008



TOLERANCE:
±0.3mm UNLESS OTHERWISE
SPECIFIED

RoHS



20050 SW 112th Ave.
Tualatin, OR 97062
Phone: 503-612-2300
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TITLE: VHK	REV: A
PART NO. VHKXXW Series	UNITS: MM [INCHES]
DRAWN BY: ZRJ	APPROVED BY:
	SCALE: 1:2

Terminal #	Description
1	- V out
2	- S
3	Trim
4	+ S
5	+ V out
6	Case
7	Remote
8	- V in
9	+ V in

PART NUMBER: VHK75W

DESCRIPTION: chassis mount dc-dc converter

APPLICATION NOTES

1. OUTPUT DE-RATING

The operating ambient temperature range for VHK75W is $-40 \sim +85^{\circ}\text{C}$, with the actual output power subject to the following de-rating curves at free-air convection and at forced air conditions. To ensure long-term reliability, it is important to ensure proper cooling at the worst operating conditions.

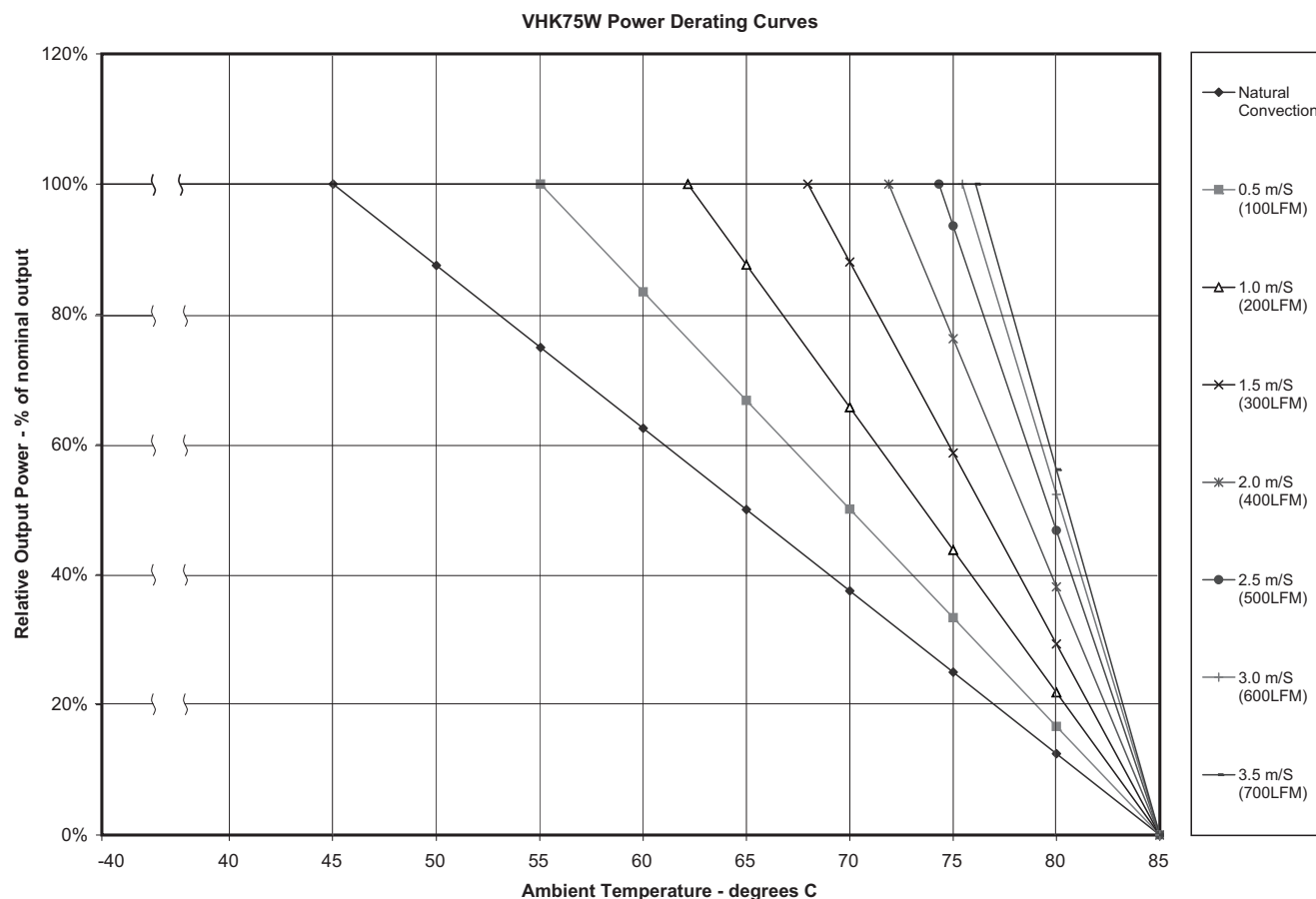


FIGURE 1. OUTPUT DERATING

2. INPUT VOLTAGE RANGE

It is important to ensure the input voltage measured at the converter input pins is within the range for that converter. Make sure wire losses and voltage ripples are accounted for. One possible problem is driving the converter with a linear unregulated power supply. For example, if the average voltage measured by a DMM is 9V, with a voltage ripple of 3Vpp, the actual input can swing from 7.5V to 10.5V. This will be outside the specified input range of 9-36V and the converter may not function properly. On the other end, make sure the actual input voltage does not exceed the highest voltage of 36V or 75V.

3. LEAD WIRES

Make sure the input and output wires are of adequate AWG size to minimize voltage drop, and ensure the voltage across the input terminals is above the converter's rated minimum voltage at all times. It is recommended to have the wire pairs twisted, respectively for the input pair and the output pair, so as to minimize noise pickup.

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4. INPUT CURRENT

The input voltage source must be able to provide enough current to the converter, otherwise it may not start up or operate properly. A typical symptom is not starting or unusually low output voltage. In general, it is recommended to be able to provide at least:

$I_{peak} = 150\% \cdot P_{out} / (\eta \cdot V_{min})$ where P_{out} is the maximum output power, V_{min} is the minimum input voltage and η is the converter's efficiency. As an example, for VHK75W-Q24-S5 to operate with 9~36V input, 75W output and an efficiency of 82%, the minimum source current is recommended to be: **$I_{peak} = 150\% \cdot 75 / (82\% \cdot 9) = 15.24A$** .

5. INPUT FUSE

To limit the input current and to facilitate input reversal protection and input OVP protection, a fast-acting input fuse is recommended for the input line. The fuse rating will depend on the input range and should allow for the maximum current at the lowest input voltage, as shown in this equation: **$I_{peak} = 150\% \cdot P_{out} / (\eta \cdot V_{min})$** .

In the previous example of VHK75W-Q24-S5, the peak input current at 9V was calculated to be 15.24A. A 10A or a 15A fuse may be suitable for this application. Make sure the fuse voltage rating is higher than the maximum input voltage.

6. INPUT OVP

It is important to ensure the input voltage does not exceed the maximum rated input voltage for that model. To suppress voltage transients of short durations, the converter includes a transient voltage suppression device (TVS) at the input. The built-in TVS has a rated breakdown voltage of 39V for the VHK75W-Q24-XX models and 82V for the VHK75W-Q48 models. With small transients of short-durations, the TVS will limit the input voltage without interrupting the converters operations. For large transients, the TVS may conduct a large amount of current that may trip the input fuse. Without the input fuse, the converter may suffer permanent damages. When the fuse is open, replace it with one of same type and ratings.

7. INPUT REVERSAL PROTECTION

If the input voltage is reversed for any reason, the built-in protection circuits in the converter will limit the reverse voltage to one diode drop which is no more than 1V. With an external input fuse connected, the fuse will open and thus remove power from the converter. Check the wiring and make corrections as needed. The input fuse will need to be replaced. Make sure the new fuse is of the same type and rating. Without the input fuse, the converter may suffer permanent damages.

8. REMOTE SENSE

The converter provides regulated outputs at the output terminals. When there is a large current and/or the output cable is of some length, the voltage at the end of the output cable may be noticeably lower than at the terminals. The converter can compensate up to 0.5V of voltage drop through remote sense terminals. To ensure accurate regulation, run two separate wires (twisted) from the desired regulation points to the remote sense terminals, as shown below.

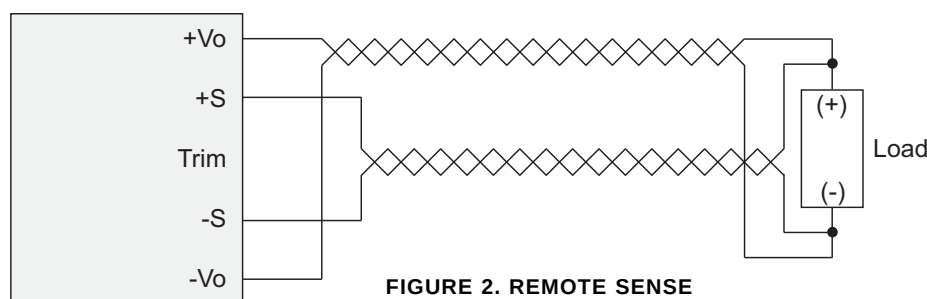


FIGURE 2. REMOTE SENSE

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DESCRIPTION: chassis mount dc-dc converter

9. OUTPUT TRIMMING (OPTIONAL)

The output voltages are preset to nominal values as indicated by the models table at the factory. If desired, the output voltage may optionally be trimmed to a different value (+/- 10%) with external resistors and/or potentiometer as shown below.

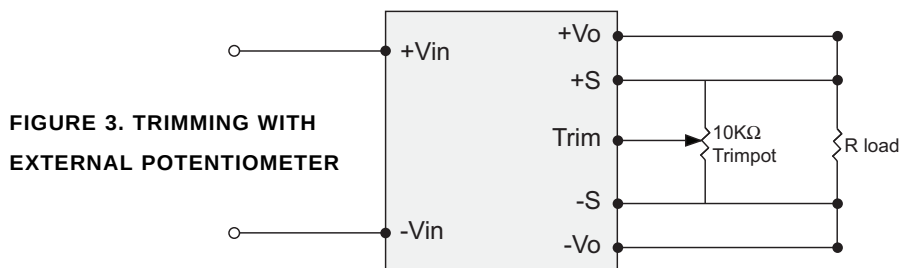


FIGURE 3. TRIMMING WITH EXTERNAL POTENTIOMETER

To trim the output voltage with fixed resistors, the output voltage can be calculated as follows.

Trim-Up

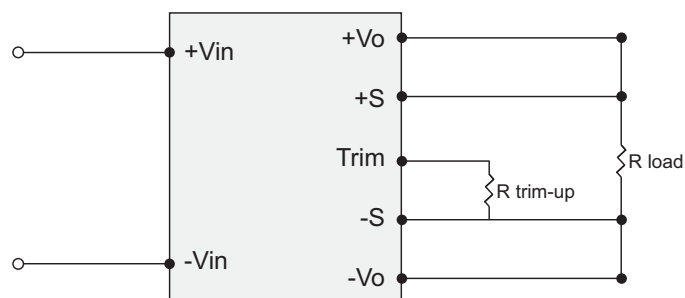


FIGURE 4: TRIM-UP VOLTAGE SETUP

The value of $R_{trim-up}$ is defined as:

$$R_{trim-up} = \frac{(R_1 - R_2 \times (V_o - V_{o,nom}))}{(V_o - V_{o,nom})} (K\Omega)$$

Where: $R_{trim-up}$ is the external resistor in $K\Omega$. $V_{o,nom}$ is the nominal output voltage. V_o is the desired output voltage.

R_1 and R_2 are internal to the unit and are defined in Table 1.

For example, to trim-up the output voltage of 5.0V module (VHK75W-Q48-S5) by 8% to 5.4V, $R_{trim-up}$ is calculated as follows:

$$V_o - V_{o,nom} = 5.4 - 5.0 = 0.4V$$

$$R_1 = 5.8 K\Omega \quad / \quad R_2 = 8.2 K\Omega$$

$$R_{trim-up} = \frac{5.8 - 8.2 \times 0.4}{0.4} = 6.3 (K\Omega)$$

Table 1

Output Voltage(V)	R_1 (KΩ)	R_2 (KΩ)
3.3V	3.168	7.2
5V	5.8	8.2
12V	19.656	13.304
15V	25.474	14.76
24V	42.33	16.67

Trim-Down

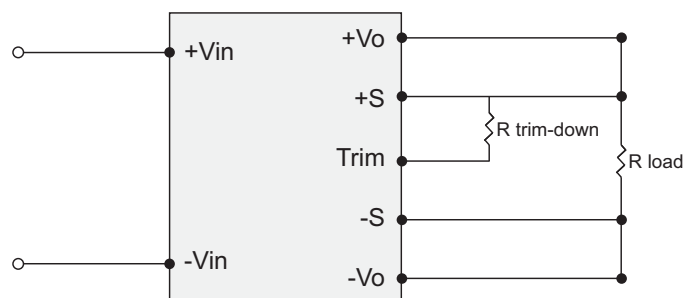


FIGURE 5: TRIM-DOWN VOLTAGE SETUP

The value of $R_{trim-down}$ is defined as:

$$R_{trim-down} = \frac{(R_1 - R_2 \times (V_{o,nom} - V_o))}{(V_{o,nom} - V_o)} (K\Omega)$$

Where: $R_{trim-down}$ is the external resistor in $K\Omega$. $V_{o,nom}$ is the nominal output voltage. V_o is the desired output voltage.

R_1 and R_2 are internal to the unit and are defined in Table 2.

For example, to trim-down the output voltage of 5.0V module (VHK75W-Q48-S5) by 8% to 4.6V, $R_{trim-down}$ is calculated as follows:

$$V_{o,nom} - V_o = 5.0 - 4.6 = 0.4 V$$

$$R_1 = 5.8 K\Omega \quad / \quad R_2 = 10.52 K\Omega$$

$$R_{trim-down} = \frac{5.8 - 10.52 \times 0.4}{0.4} = 3.98 (K\Omega)$$

Table 2

Output Voltage(V)	R_1 (KΩ)	R_2 (KΩ)
3.3V	6.18	15
5V	5.8	10.52
12V	86.45	60.1
15V	150	94
24V	430	120

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10. OUTPUT OCP AND SHORT-CIRCUIT PROTECTION

Output overload and short circuit conditional will cause the output voltage to decline or shutdown altogether. If the case temperature is not over 85°C, the output recovers automatically when the short or OCP conditions are removed. In the case of slight overloading, the output voltage may not shut down, but the converter may build up heat over time, causing over-temperature shutdown.

11. OVER-TEMPERATURE PROTECTION

When the case temperature reaches about 85°C, the converter's built-in protection circuit will shut down the output. When the temperature is reduced enough to a safe operating level, the converter will recover to normal operations automatically.

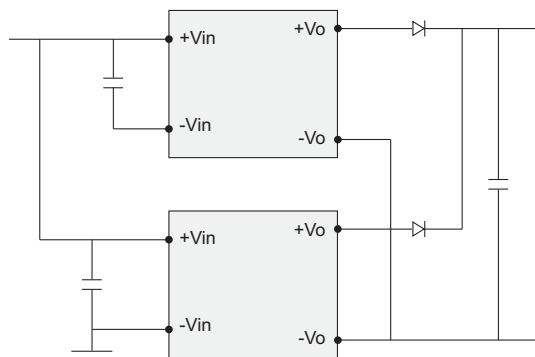
12. OUTPUT OVP

In case the output voltage exceeds the OVP threshold, the converter shuts down.

13. OUTPUT PARALLEL CONNECTIONS

The converter is not designed for load share on the output. However, for redundancy, the outputs of two or more converters can be connected in parallel through O-Ring diodes, as shown below.

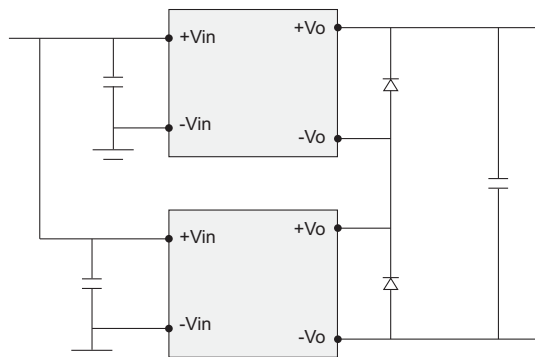
**FIGURE 6. OUTPUT
PARALLEL CONNECTIONS**



14. OUTPUT SERIES CONNECTIONS

Two or more converters can be connected in series to obtain a higher output voltage. To prevent output reverse biasing each other in case of a short, add a Schottky diode on each output in reverse polarity, as shown in the diagram. In the event of a short, the converters will forward-bias the diodes and the output reversal will be limited to one diode drop (about 0.5V) only, so as not to damage the converters. The forward current will cause each converter to go into short-circuit protection. For proper diode selection, make sure that: 1) the diodes voltage rating is higher than each converter output; 2) the rated diode current can carry the short-circuit current; 3) the diodes do not overheat before the short is removed.

**FIGURE 7. OUTPUT
SERIES CONNECTIONS**

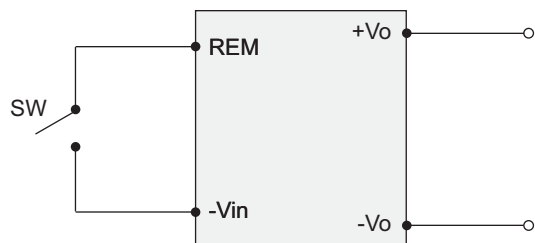
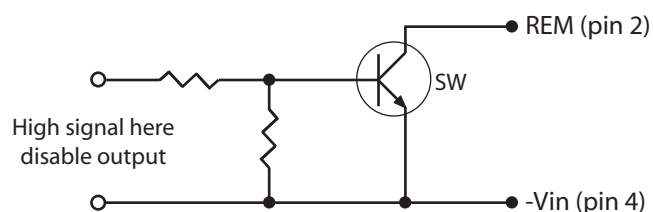


PART NUMBER: VHK75W

DESCRIPTION: chassis mount dc-dc converter

15. REMOTE OUTPUT ON/OFF CONTROL

The converter output can be enabled or disabled through the On/Off pin. The control logic is shown in this table. A common control circuit is shown below.


FIGURE 8. REMOTE ON/OFF CONTROL

**FIGURE 9. REMOTE ON/OFF CONTROL
WITH TRANSISTOR SWITCH**

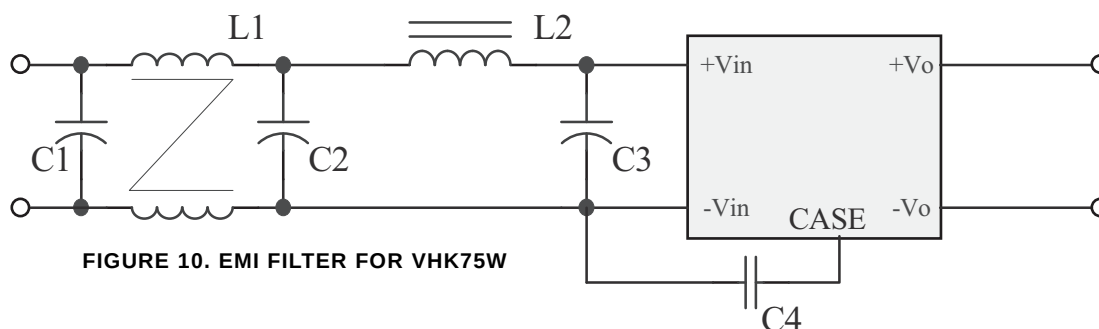
Logic Table	Negative logic	Positive logic
SW Closed ($V_{REM} < 0.8\text{ V}$)	Output on	Output off
SW Open ($V_{REM} > 2.4\text{ V}$)	Output off	Output on

16. ISOLATION

The input and output of the converter are electrically isolated. If needed, an output terminal can be connected to an input terminal, resulting in the converter non-isolated.

17. INPUT FILTERING AND EMI INTERFERENCE

The VHK75W converters have input capacitors that control input current ripple and the associated EMI interference. However, it has not been tested to a formal standard for conducted emissions. Additional filtering may be needed to ensure compliance to an EMI standard. Refer to FIGURE 10 for a reference circuit.


FIGURE 10. EMI FILTER FOR VHK75W

VHK75W Series	C1	C2	C3	C4	L1	L2
EMI to meet class A	NC	100μF/50V ESR<0.330	100μF/50V ESR<0.330	1000pF/2KV	0.65mH	1.5μH
EMI to meet class B	100μF/50V ESR<0.330	100μF/50V ESR<0.330	100μF/50V ESR<0.330	2200pF/2KV	1.3mH	1.5μH

NOTE: The recommended components are a starting point but not guaranteed for meeting these EMI requirements. More or less filtering may be required for a specific application or to meet other EMI standards, e.g. MIL-STD-461.

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18. OUTPUT FILTERING

The converter has built-in capacitors at the output to control ripple and noise. If desired, additional output capacitance can be introduced at the input to the powered equipment. Recommended starting point: one 10 μ F tantalum and one 1 μ F ceramic capacitor in parallel at the output.

19. OUTPUT NOISE MEASUREMENT SCHEMATIC

For proper output ripple and noise measurement, connect a 10 μ F tantalum and a 1 μ F ceramic capacitor across the output. Set the scope bandwidth to 20MHz. Probe directly off of one of the capacitors, using a small ground clip to minimize measurement error.

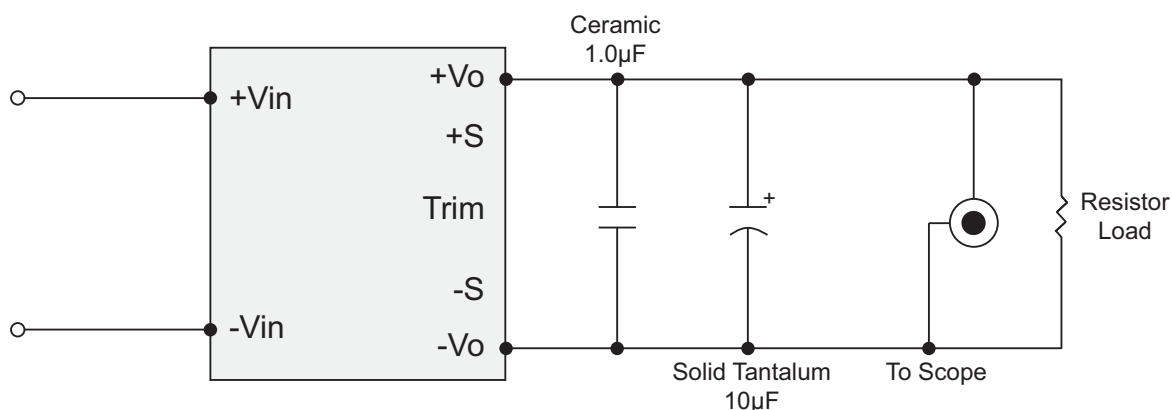


FIGURE 11. OUTPUT NOISE MEASUREMENT CIRCUIT