

### VSKCS203/100P

### Vishay High Power Products

### Schottky Rectifier, 200 A



PRODUCT SUMMARY				
I <sub>F(AV)</sub>	200 A			

#### **MECHANICAL DESCRIPTION**

The Generation 5 of ADD-A-PAK module combine the excellent thermal performance obtained by the usage of direct bonded copper substrate with superior mechanical ruggedness, thanks to the insertion of a solid copper baseplate at the bottom side of the device.

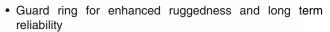
The Cu baseplate allow an easier mounting on the majority of heatsink with increased tolerance of surface roughness and improved thermal spread.

The Generation 5 of ADD-A-PAK module is manufactured without hard mold, eliminating in this way any possible direct stress on the leads.

The electrical terminals are secured against axial pull-out: they are fixed to the module housing via a click-stop feature already tested and proved as reliable on other Vishay HPP modules.

#### **FEATURES**

- 175 °C T<sub>J</sub> operation
- Low forward voltage drop
- High frequency operation



- UL pending
- Totally lead (Pb)-free, RoHS compliant
- Designed and qualified for industrial level

#### **DESCRIPTION**

The VSKCS203.. Schottky rectifier common cathode has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I <sub>F(AV)</sub>	Rectangular waveform	200	Α		
V <sub>RRM</sub>	138	100	V		
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	12 800	A		
V <sub>F</sub>	100 Apk, T <sub>J</sub> = 125 °C	0.76	V		
TJ	Range	- 55 to 175	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VSKCS203/100P	UNITS		
Maximum DC reverse voltage	V <sub>R</sub>	100	V		
Maximum working peak reverse voltage	V <sub>RWM</sub>	100	V		
Maximum working peak reverse voltage		100	V		

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ABSOLUTE MAXIMUM RATINGS						
PARAMETER		SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average	per module		50 % duty cycle at T <sub>C</sub> = 119 °C, rectangular waveform		200	
forward current	per leg	I <sub>F(AV)</sub>			100	
Maximum peak one cycle		5 μs sine or 3 μs rect. pulse	Following any rated load condition and with rated	12 800	Α	
non-repetitive surge current		I <sub>FSM</sub>	10 ms sine or 6 ms rect. pulse		1700	
Non-repetitive avalanche energ	ay .	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 5.5 A, L = 1 mH		15	mJ
Repetitive avalanche current		I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical		1	Α

ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum forward voltage drop V	(1)	100 A	T <sub>J</sub> = 25 °C	0.9	V
		200 A		1.19	
	V <sub>FM</sub> <sup>(1)</sup>	100 A	T <sub>J</sub> = 125 °C	0.76	
		200 A		0.98	
Maximum vayava laaka aa ayvant	m reverse leakage curent $I_{RM}^{~(1)}$	T <sub>J</sub> = 25 °C	V <sub>R</sub> = Rated V <sub>R</sub>	3	mA
Maximum reverse leakage curent		T <sub>J</sub> = 125 °C		40	MA
Maximum junction capacitance	C <sub>T</sub>	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		2750	pF
Typical series inductance	L <sub>S</sub>	From top of terminal hole to mounting plane		7.0	nH
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs	
RMS insulation voltage	V <sub>INS</sub>	50 Hz, circuit to base, all terminals shorted (1 s) 3500		V	

#### Note

 $<sup>^{(1)}</sup>$  Pulse width < 500  $\mu s$ 

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T <sub>J</sub> , T <sub>Stg</sub>		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		R <sub>thJC</sub>	DC operation	0.6	°C/W
Maximum thermal resistance, case to heatsink		R <sub>thCS</sub>	Mounting surface, smooth and greased	0.1	C/VV
Approximate weight				110	g
Approximate weight				4	OZ.
Mounting torque ± 10 %	to heatsink			5	Nm
	busbar			4	INITI
Case style			JEDEC	TO-24	IOAA

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For technical questions, contact: ind-modules@vishay.com

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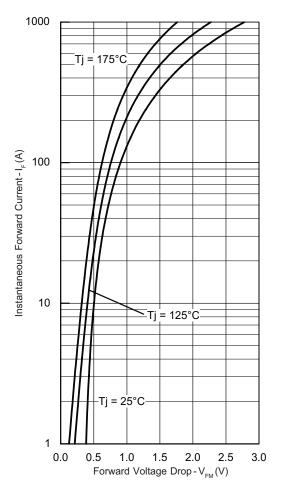


Fig. 1 - Maximum Forward Voltage Drop Characteristics

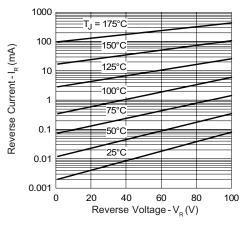


Fig. 2 - Typical Values of Reverse Current vs.
Reverse Voltage

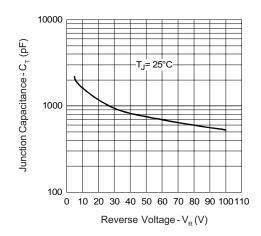


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

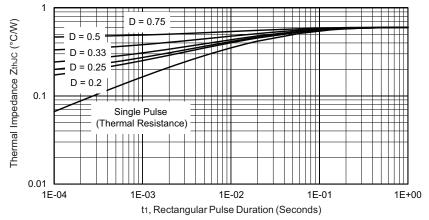


Fig. 4 - Maximum Thermal Impedance  $Z_{thJC}$  Characteristics

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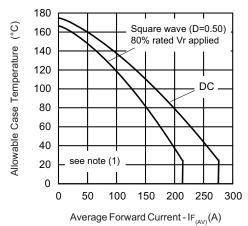


Fig. 5 - Maximum Allowable Case Temperature vs. **Average Forward Current** 

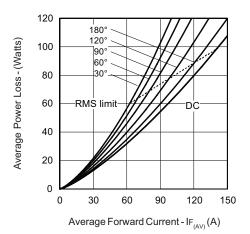


Fig. 6 - Forward Power Loss Characteristics

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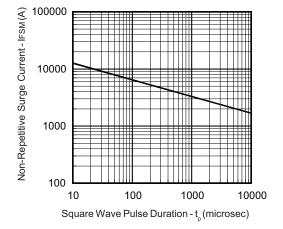


Fig. 7 - Maximum Non-Repetitive Surge Current

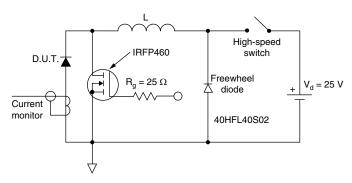


Fig. 8 - Unclamped Inductive Test Circuit

#### Note

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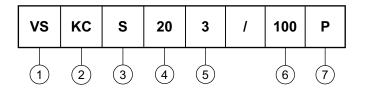
 $^{(1)}$  Formula used:  $T_{C}$  =  $T_{J}$  - (Pd + Pd\_{REV}) x  $R_{thJC};$ Pd = Forward power loss =  $I_{F(AV)} \times V_{FM}$  at  $(I_{F(AV)}/D)$  (see fig. 6); Pd<sub>REV</sub> = Inverse power loss =  $V_{R1} \times I_{R}$  (1 - D);  $I_{R}$  at  $V_{R1}$  = 80 % rated  $V_{R}$ 



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#### **ORDERING INFORMATION TABLE**

**Device code** 



1 - Vishay HPP

2 - Circuit configuration:

KC = ADD-A-PAK - 2 diodes/common cathode

3 - S = Schottky diode

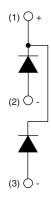
4 - Average rating (x 10)

5 - Product silicon identification

6 - Voltage rating (100 = 100 V)

7 - Lead (Pb)-free

#### **CIRCUIT CONFIGURATION**



	LINKS TO RELATED DOCUMENTS					
Dimensions http://www.vishay.com/doc?95174						



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