

VSKDS403/100P

Vishay High Power Products

Schottky Rectifier, 200 A



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PRODUCT SUMMARY			
I _{F(AV)}	200 A		

MECHANICAL DESCRIPTION

The Generation 5 of ADD-A-PAK module combine the excellent thermal performance obtained by the usage of direct bonded copper substrate with superior mechanical ruggedness, thanks to the insertion of a solid copper baseplate at the bottom side of the device.

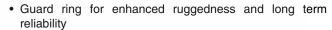
The Cu baseplate allow an easier mounting on the majority of heatsink with increased tolerance of surface roughness and improved thermal spread.

The Generation 5 of ADD-A-PAK module is manufactured without hard mold, eliminating in this way any possible direct stress on the leads.

The electrical terminals are secured against axial pull-out: they are fixed to the module housing via a click-stop feature already tested and proved as reliable on other Vishay HPP modules.

FEATURES

- 175 °C T_J operation
- Low forward voltage drop
- High frequency operation



- · UL pending
- · Totally lead (Pb)-free, RoHS compliant
- Designed and qualified for industrial level

DESCRIPTION

The VSKDS403.. Schottky rectifier doubler module has been optimized for low reverse leakage at high temperature. The proprietary barrier technology allows for reliable operation up to 175 °C junction temperature.

Typical applications are in high current switching power supplies, plating power supplies, UPS systems, converters, freewheeling diodes, welding, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	VALUES	UNITS		
I _{F(AV)}	Rectangular waveform	200	A A		
V _{RRM}		100	V		
I _{FSM}	t _p = 5 μs sine	25 500	А		
V _F	200 Apk, T _J = 125 °C	0.8	V		
T _J	Range	- 55 to 175	°C		

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VSKDS403/100P	UNITS			
Maximum DC reverse voltage	V_{R}	100	V			
Maximum working peak reverse voltage	V_{RWM}	100	V			

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ABSOLUTE MAXIMUM RATINGS							
PARAMETER SYMBOL		TEST CONDITIONS		VALUES	UNITS		
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T _C = 107 °C, rectangular waveform		200			
Maximum peak one cycle	1	5 μs sine or 3 μs rect. pulse Following any rated load condition and with rated	25 500	А			
non-repetitive surge current	IFSM	10 ms sine or 6 ms rect. pulse		3300			
Non-repetitive avalanche energy	E _{AS}	$T_J = 25$ °C, $I_{AS} = 5.5$ Amps, $L = 1$ mH		15	mJ		
Repetitive avalanche current	I _{AR}	Current decaying linearly to zero in 1 μ s Frequency limited by T_J maximum $V_A = 1.5 \times V_R$ typical		1	Α		

ELECTRICAL SPECIFICATIONS						
PARAMETER	METER SYMBOL TEST CONDITIONS		VALUES	UNITS		
	V _{FM} ⁽¹⁾	200 A	T _J = 25 °C	0.93	V	
Maximum forward voltage drop		400 A		1.24		
Maximum forward voltage drop		200 A	T _J = 125 °C	0.8		
		400 A		1.05		
Maximum reverse leakage current	I _{RM} ⁽¹⁾	T _J = 25 °C	V _R = Rated V _R	6	mA	
Maximum reverse leakage current		T _J = 125 °C		80		
Maximum junction capacitance	C _T	$V_R = 5 V_{DC}$ (test signal range 100 kHz to 1 MHz) 25 °C		5500	pF	
Typical series inductance	L _S	From top of terminal hole to mounting plane		5.0	nΗ	
Maximum voltage rate of change	dV/dt	Rated V _R		10 000	V/µs	
RMS insulation voltage	V _{INS}	50 Hz, circuit to base, all terminals shorted (1 s)		3500	V	

Note

 $^{^{(1)}\,}$ Pulse width < 500 μs

THERMAL - MECHANICAL SPECIFICATIONS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range		T _J , T _{Stg}		- 55 to 175	°C
Maximum thermal resistance, junction to case per leg		R _{thJC}	DC operation	0.30	°C/W
Maximum thermal resistance, case to heatsink		R _{thCS}	Mounting surface, smooth and greased	0.1	C/VV
Approximate weight				110	g
				4	OZ.
Mounting torque + 10.9/	to heatsink			5	Nime
Mounting torque ± 10 %	busbar			4	Nm
Case style			JEDEC	TO-2	40AA



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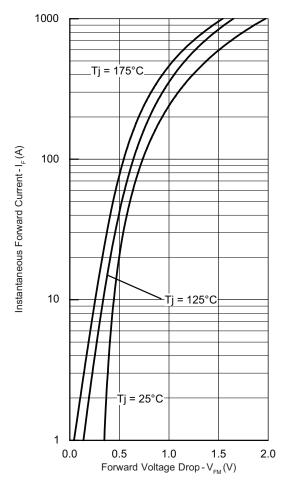


Fig. 1 - Maximum Forward Voltage Drop Characteristics

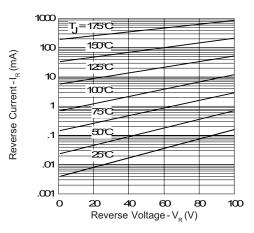


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

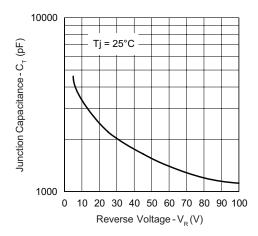


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

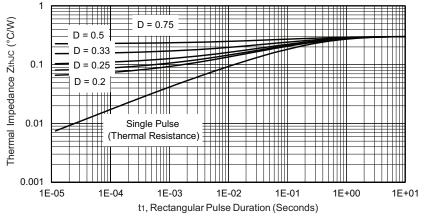


Fig. 4 - Maximum Thermal Impedance Z_{thJC} Characteristics

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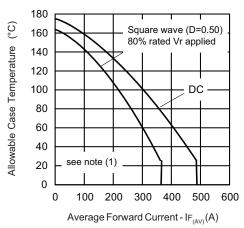


Fig. 5 - Maximum Allowable Case Temperature vs.
Average Forward Current

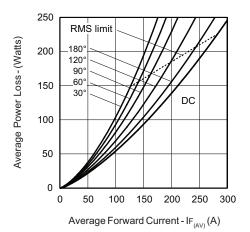


Fig. 6 - Forward Power Loss Characteristics

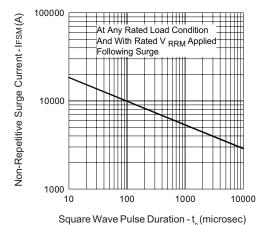


Fig. 7 - Maximum Non-Repetitive Surge Current

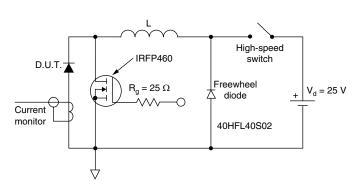


Fig. 8 - Unclamped Inductive Test Circuit

Note

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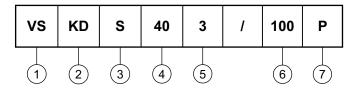
 $\begin{array}{l} \mbox{(1)} \ \ \mbox{Formula used:} \ T_{C} = T_{J} \mbox{-} (\mbox{Pd} + \mbox{Pd}_{REV}) \ x \ R_{thJC}; \\ \mbox{Pd} = \mbox{Forward power loss} = I_{F(AV)} \ x \ V_{FM} \ at \ (I_{F(AV)}/D) \ (\mbox{see fig. 6}); \\ \mbox{Pd}_{REV} = \mbox{Inverse power loss} = V_{R1} \ x \ I_{R} \ (1 \mbox{-} D); \ I_{R} \ at \ V_{R1} = 80 \ \% \ rated \ V_{R} \\ \end{array}$



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ORDERING INFORMATION TABLE

Device code



1 - Vishay HPP

2 - Circuit configuration:

KD = ADD-A-PAK - 2 diodes in series

3 - S = Schottky diode

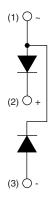
4 - Average rating (x 10)

5 - Product silicon identification

6 - Voltage rating (100 = 100 V)

7 - Lead (Pb)-free

CIRCUIT CONFIGURATION



LINKS TO RELATED DOCUMENTS				
Dimensions	http://www.vishay.com/doc?95174			



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