

### **Advanced Information**

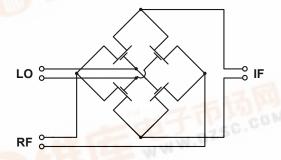
## PE84140

### **Product Description**

The PE84140 is an ultra-high linearity, passive broadband Quad MOSFET array with high dynamic range performance capable of operation beyond 6.0 GHz. This quad array operates with differential signals at all ports (RF, LO, IF), allowing mixers to be built that use LO powers from -7 dBm to +20 dBm. Typical applications range from frequency up/down-conversion to phase detection for Cellular/PCS Base Stations, Wireless Broadband Communications and STB/Cable modems.

The PE84140 is optimized for stringent military applications. Fabricated in Peregrine's patented UTSi® (Ultra Thin Silicon) CMOS technology, the PE84140offers excellent RF performance with the economy and integration of conventional CMOS.

Figure 1. Functional Schematic Diagram



## Ultra-High Linearity Broadband Quad MOSFET Array

### **Features**

- Ultimate Quad MOSFET array
- Ultra-high linearity, broadband performance beyond 6.0 GHz
- Ideal for mixer applications
- Up/down conversion
- Low conversion loss
- High LO Isolation
- Optimized for stringent military applications

Figure 2. Package Type

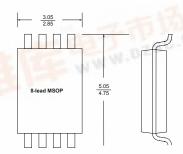


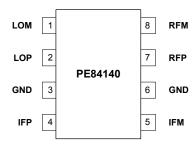
Table 1. AC and DC Electrical Specifications @ +25 °C

| Symbol                | Characteristics                        | Min | Тур  | Max  | Units | Test Conditions                           |
|-----------------------|--|-----|------|------|-------|---|
| F <sub>TYP</sub>      | Operating Frequency Range <sup>1</sup> | DC  | 6.0  | N 47 | GHz   | - W.c.                                    |
| V <sub>DS</sub>       | Drain-Source Voltage                   | 100 | 330  | % I1 | mV    | $V_{GS} = +3V$ , $I_{DS} = 40 \text{ mA}$ |
| V <sub>DS</sub> Match | Drain-Source Voltage Match             | 764 | 20   | 1    | mV    |   |
| V <sub>T</sub>        | Threshold Voltage                      | COM | -100 |      | mV    | V <sub>DS</sub> = 0.1V; per ASTM F617-00  |
| R <sub>DS</sub>       | Drain-Source 'ON' Resistance           |     | 8.25 |      | Ω     | $V_{GS} = +3V$ , $I_{DS} = 40 \text{ mA}$ |

Note 1: Typical untested operating frequency range of Quad MOSFET transistors.



Figure 3. Pin Configuration



**Table 2. Pin Descriptions** 

| Pin<br>No. | Pin<br>Name | Description                  |
|------------|-------------|------------------------------|
| 1          | LOM         | LO Input Connection (Gate)   |
| 2          | LOP         | LO Input Connection (Gate)   |
| 3          | GND         | Ground Connection            |
| 4          | IFP         | IF Output Connection (Drain) |
| 5          | IFM         | IF Output Connection (Drain) |
| 6          | GND         | Ground Connection            |
| 7          | RFP         | RF Input Connection (Source) |
| 8          | RFM         | RF Input Connection (Source) |

**Table 3. Absolute Maximum Ratings** 

| Symbol               | Parameter/Conditions  | Min | Max  | Units |
|----------------------|---|-----|------|-------|
| T <sub>ST</sub>      | Storage temperature range -6                                      |     | 150  | °C    |
| T <sub>OP</sub>      | Operating temperature range                                       | -55 | 125  | °C    |
| V <sub>DC + AC</sub> | Maximum DC plus peak<br>AC voltage across Drain-<br>Source        |     | ±3.3 | ٧     |
| V <sub>DC+AC</sub>   | Maximum DC plus peak AC voltage across Gate- Drain or Gate-Source |     | ±4.2 | V     |
| V <sub>ESD</sub>     | ESD Sensitive Device  |     | 250  | V     |

## **Electrostatic Discharge (ESD) Precautions**

This MOSFET device has minimally protected inputs and is highly susceptible to ESD damage. When handling this UTSi device, observe the same precautions that you would use with other ESD-sensitive devices.

### **Latch-Up Avoidance**

Unlike conventional CMOS devices, UTSi CMOS devices are immune to latch-up.

### **Device Description**

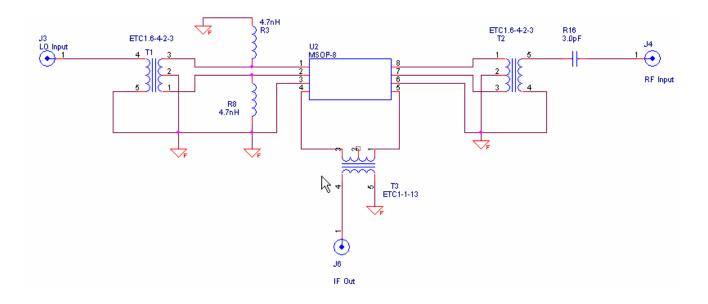
The PE84140 passive broadband Quad MOSFET array is designed for use in up-conversion and down-conversion applications for high performance systems.

The PE84140 is an ideal mixer core for a wide range of mixer products, including module level solutions that incorporate baluns or other single-ended matching structures enabling three-port operation.

The performance level of this passive mixer is made possible by the very high linearity afforded by Peregrine's UTSi CMOS process.



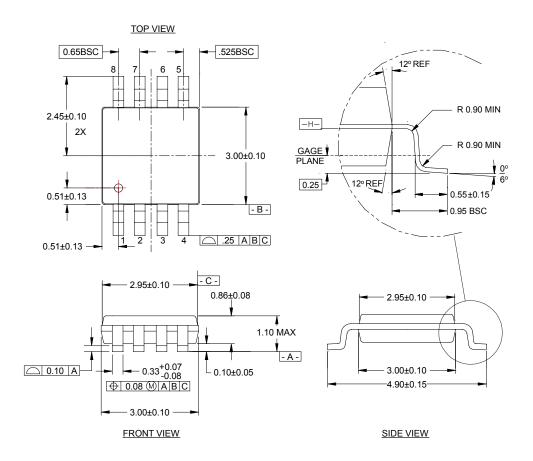
## Figure 4. Typical Schematic





## Figure 5. Package Drawing

8-lead MSOP



**Table 4. Ordering Information** 

| Order<br>Code | Part Marking | Description          | Package        | Shipping<br>Method |
|---------------|--------------|----------------------|----------------|--------------------|
| 84140-01      | 84140        | PE84140-08MSOP-50A   | 8-lead MSOP    | 50 units / Tube    |
| 84140-02      | 84140        | PE84140-08MSOP-2000C | 8-lead MSOP    | 2000 units / T&R   |
| 84140-00      | PE84140-EK   | PE84140-08MSOP-EK    | Evaluation Kit | 1 / Box            |

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### **Data Sheet Identification**

### Advance Information

The product is in a formative or design stage. The data sheet contains design target specifications for product development. Specifications and features may change in any manner without notice.

### **Preliminary Specification**

The data sheet contains preliminary data. Additional data may be added at a later date. Peregrine reserves the right to change specifications at any time without notice in order to supply the best possible product.

### **Product Specification**

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