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COMPLETE DATA SHEET

MAXIM

Improved Low-Power, CMOS Analog Switches with Latches

General Description

Maxim's redesigned DG421/DG423/DG425 monolithic analog switches now feature guaranteed on-resistance matching (3Ω max) between switches and on-resistance flatness over the signal range (4Ω max). These low on-resistance switches (20Ω typ) conduct equally well in both directions. They guarantee a low charge injection of 15pC maximum and an ESD tolerance of 2000V minimum per Method 3015.7. Off leakage current over temperature has also been reduced (less than 5nA at $+85^\circ\text{C}$).

The DG421/DG423/DG425 are precision, dual CMOS switches with latchable logic inputs that simplify interfacing with microprocessors (μPs). The single-pole/single-throw DG421 and double-pole/single-throw DG425 are normally open dual switches. The dual, single-pole/double-throw DG423 has two normally open and two normally closed switches. Fast switching times (175ns for t_{ON} and 145ns for t_{OFF}) and low power consumption ($35\mu\text{W}$ max) make these parts ideal for battery-powered applications requiring μP -compatible switches. Operation is from a single $+10\text{V}$ to $+30\text{V}$ supply, or bipolar $\pm 4.5\text{V}$ to $\pm 20\text{V}$ supplies. Fabricated with the same 44V silicon-gate process, these switches have rail-to-rail signal handling capabilities.

Applications

Sample-and-Hold Circuits	Modems
Fax Machines	Test Equipment
Battery-Operated Systems	PBX, PABX
Guidance and Control Systems	Military Radios
Audio Signal Routing	Communication Systems

New Features

- ♦ Plug-In Upgrades for Industry-Standard DG421/DG423/DG425
- ♦ Improved $r(\text{DS})_{\text{ON}}$ Match Between Channels (3Ω max)
- ♦ Guaranteed $r(\text{FLAT})_{\text{ON}}$ Over Signal Range (4Ω max)
- ♦ Improved Charge Injection (15pC max)
- ♦ Improved Off Leakage Current Over Temperature (<5nA at $+85^\circ\text{C}$)
- ♦ Withstands Electrostatic Discharge (2000V min) per Method 3015.7

Existing Features

- ♦ Low $r(\text{DS})_{\text{ON}}$ (35Ω max)
- ♦ Single-Supply Operation $+10\text{V}$ to $+30\text{V}$
Bipolar-Supply Operation $\pm 4.5\text{V}$ to $\pm 20\text{V}$
- ♦ Low Power Consumption ($35\mu\text{W}$ max)
- ♦ Rail-to-Rail Signal Handling Capability
- ♦ TTL/CMOS-Logic Compatible

Ordering Information

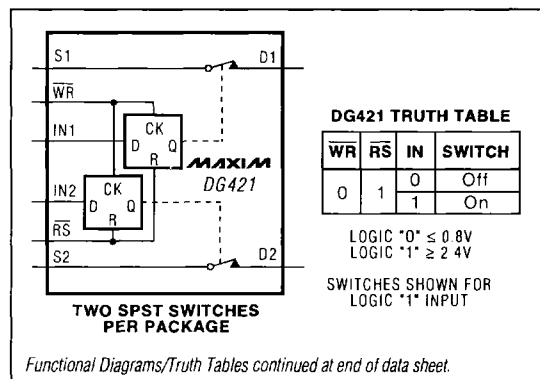
PART	TEMP. RANGE	PIN-PACKAGE
DG421CJ	0°C to $+70^\circ\text{C}$	16 Plastic DIP
DG421CY	0°C to $+70^\circ\text{C}$	16 SO
DG421C/D	0°C to $+70^\circ\text{C}$	Dice*
DG421DJ	-40°C to $+85^\circ\text{C}$	16 Plastic DIP
DG421DY	-40°C to $+85^\circ\text{C}$	16 SO
DG421DK	-40°C to $+85^\circ\text{C}$	16 Cerdip
DG421AK	-55°C to $+125^\circ\text{C}$	16 Cerdip**

Ordering Information continued at end of data sheet.

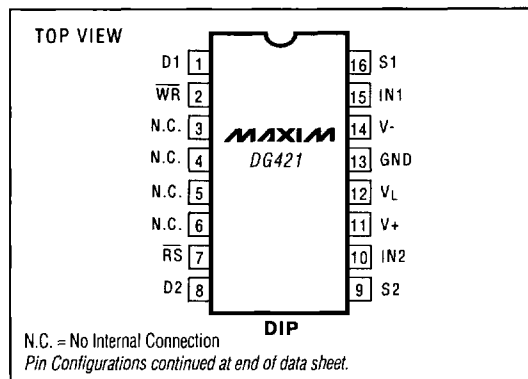
* Contact factory for dice specifications.

** Contact factory for availability and processing to MIL-STD-883B.

Functional Diagrams/Truth Tables



Pin Configurations



DG421/DG423/DG425

MAXIM

Maxim Integrated Products 1-143

Call toll free 1-800-998-8800 for free samples or literature.

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ABSOLUTE MAXIMUM RATINGS

Voltage Referenced to V-
V+ 44V
GND 25V
V_L (GND - 0.3V) to (V+ + 0.3V)
Digital Inputs, V_S, V_D (Note 1) (V- - 2V) to (V+ + 2V)
Current (any terminal, except S or D) 30mA
Continuous Current, S or D 20mA
Peak Current, S or D (pulsed at 1ms, 10% duty cycle max).....100mA

Continuous Power Dissipation (T_A = +70°C)
16-Pin Plastic DIP (derate 10.53mW/°C above +70°C) ... 842mW
20-Pin PLCC (derate 10.00mW/°C above +70°C) 800mW
16-Pin Cerdip (derate 10.00mW/°C above +70°C) ... 800mW
Operating Temperature Ranges
DG42_C_ 0°C to +70°C
DG42_D_ -40°C to +85°C
DG42_A_ -55°C to +125°C
Storage Temperature Ranges
DG42_C_/DG42_D_ -65°C to +125°C
DG42_A_ -65°C to +150°C
Lead Temperature (soldering, 10sec).....+300°C

Note 1: Signals on S, D, or IN exceeding V+ or V- are clamped by internal diodes. Limit forward current to maximum current ratings.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V+ = 15V, V- = -15V, V_L = +5V, GND = 0V, V_{INH} = +2.4V, V_{INL} = +0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		DG42_C, DG42_D MIN TYP MAX (Note 2)			DG42_A MIN TYP MAX (Note 2)			UNITS
SWITCH										
Analog Signal Range	V _{ANALOG}	(Note 3)		-15	15		-15	15		V
Drain-Source On-Resistance	r _{DS(ON)}	V+ = 13.5V, V- = -13.5V, I _S = -10mA, V _D = ±10V	T _A = +25°C	20	45		20	35		Ω
			T _A = T _{MIN} to T _{MAX}	45		45				
On-Resistance Match Between Channels (Note 4)	Δr _{DS(ON)}	V+ = 16.5V, V- = -16.5V, I _S = -10mA, V _D = ±10V	T _A = +25°C	3		3		Ω		
			T _A = T _{MIN} to T _{MAX}	4		4				
On-Resistance Flatness (Note 4)	r _{FLAT(ON)}	V+ = 15V, V- = -15V, I _S = -10mA, V _D = ±5V	T _A = +25°C	4		4		Ω		
			T _A = T _{MIN} to T _{MAX}	5		5				
Source-Off Leakage Current (Note 5)	I _{S(OFF)}	V+ = 16.5V, V- = -16.5V, V _D = ±15.5V, V _S = ±15.5V	T _A = +25°C	-0.50	-0.01	0.50	-0.25	-0.01	0.25	nA
			T _A = T _{MIN} to T _{MAX}	-5	5		-10	10		
Drain-Off Leakage Current (Note 5)	I _{D(OFF)}	V+ = 16.5V, V- = -16.5V, V _D = ±15.5V, V _S = ±15.5V	T _A = +25°C	-0.50	-0.01	0.50	-0.25	-0.01	0.25	nA
			T _A = T _{MIN} to T _{MAX}	-5	5		-10	10		
Drain-On Leakage Current (Note 5)	I _{D(ON)}	V+ = 16.5V, V- = -16.5V, V _D = ±15.5V, V _S = ±15.5V	T _A = +25°C	-1.0	-0.04	1.0	-0.40	-0.04	0.40	nA
			T _A = T _{MIN} to T _{MAX}	-10	10		-20	20		

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ELECTRICAL CHARACTERISTICS (continued)

(V+ = 15V, V- = -15V, VL = +5V, GND = 0V, VINH = +2.4V, VINL = +0.8V, TA = TMIN to TMAX, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS		MIN	TYP (Note 2)	MAX	UNITS
INPUT							
Input Current with Input Voltage High	I _{INH}	I _N = 2.4V, all others = 0.8V			-0.50	0.005 0.50	μA
Input Current with Input Voltage Low	I _{INL}	I _N = 0.8V, all others = 2.4V			-0.50	0.005 0.50	μA
SUPPLY							
Power Supply Range	V+, V-	(Note 3)			±4.5	±20	V
Positive Supply Current	I+	All channels on or off, V+ = 16.5V, V- = -16.5V, V _{IN} = 0V or 5V	T _A = +25°C	-1.0	0.01	1.0	μA
			T _A = T _{MIN} to T _{MAX}	-5.0		5.0	
Negative Supply Current	I-	All channels on or off, V+ = 16.5V, V- = -16.5V, V _{IN} = 0V or 5V	T _A = +25°C	-1.0	-0.01	1.0	μA
			T _A = T _{MIN} to T _{MAX}	-5.0		5.0	
Logic Supply Current	I _L	All channels on or off, V+ = 16.5V, V- = -16.5V, V _{IN} = 0V or 5V	T _A = +25°C	-1.0	-0.01	1.0	μA
			T _A = T _{MIN} to T _{MAX}	-5.0		5.0	
Ground Current	I _{GND}	All channels on or off, V+ = 16.5V, V- = -16.5V, V _{IN} = 0V or 5V	T _A = +25°C	-1.0	-0.01	1.0	μA
			T _A = T _{MIN} to T _{MAX}	-5.0		5.0	
DYNAMIC							
Turn-On Time	t _{ON}	Figure 2	T _A = +25°C	150	250	ns	
			T _A = T _{MIN} to T _{MAX}		300		
Turn-Off Time	t _{OFF}	Figure 2			200	ns	
Latch Timing	t _{WW}	V _S = ±10V, R _L = 300Ω, C _L = 35pF, Figure 3	T _A = +25°C	200		ns	
			T _A = -55°C to +125°C	200			
	t _{DW}		T _A = +25°C	100			
			T _A = -55°C to +125°C	100			
	t _{WD}		T _A = +25°C	60			
			T _A = -55°C to +125°C	100			
Break-Before-Make Interval (Note 3)	t _D	DG423, Figure 4	T _A = +25°C	5	25	ns	
Charge Injection (Note 3)	Q	C _L = 10nF, V _G = 0V, R _G = 0Ω, Figure 5	T _A = +25°C	10	15	pC	
Off-Isolation Rejection Ratio (Note 6)	OIRR	R _L = 100Ω, C _L = 5pF, f = 1MHz, Figure 6	T _A = +25°C	72		dB	
Crosstalk (Note 7)		R _L = 50Ω, C _L = 5pF, f = 1MHz, Figure 7	T _A = +25°C	90		dB	
Drain-Off Capacitance	C _{D(OFF)}	f = 1MHz, Figure 8	T _A = +25°C	12		pF	
Source-Off Capacitance	C _{S(OFF)}	f = 1MHz, Figure 8	T _A = +25°C	12		pF	
Drain-On Capacitance	C _{D(ON)}	f = 1MHz, Figure 9	T _A = +25°C	39		pF	
Source-On Capacitance	C _{S(ON)}	f = 1MHz, Figure 9	T _A = +25°C	39		pF	

Note 2: Typical values are for design aid only, are not guaranteed, and are not subject to production testing. The algebraic convention, where the most negative value is a minimum and the most positive value a maximum, is used in this data sheet.

Note 3: Guaranteed by design.

Note 4: On-resistance match between channels and flatness are guaranteed only with bipolar-supply operation. Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured at the extremes of the specified analog signal range.

Note 5: Leakage parameters IS(OFF), ID(OFF), and ID(ON) are 100% tested at the maximum rated hot temperature and guaranteed by correlation at +25°C.

Note 6: Off-Isolation Rejection Ratio = 20log (VD/VS), VD = output, VS = input to off switch.

Note 7: Between any two switches.

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Typical Operating Characteristics

($T_A = +25^\circ\text{C}$, unless otherwise noted.)

