



Specification of Automotive MLCC (Reference sheet)

• Supplier : Samsung electro-mechanics • Samsung P/N : CL10C100JB81PNC

• Product : Multi-layer Ceramic Capacitor • Description : CAP, 10pF, 50V, ±5%, C0G, 0603

• AEC-Q 200 Specified

A. Samsung Part Number

<u>CL</u> <u>10</u> <u>C</u> <u>100</u> <u>J</u> <u>B</u> <u>8</u> <u>1</u> <u>P</u> <u>N</u> <u>C</u> ① ② ③ ④ ⑤ ⑥ ⑦ ® ⑨ ⑩ ⑪

1	Series	Samsung Multi-layer Ceramic Capacitor							
2	Size	0603 (inch code)	L:	1.6	6 ± 0.1 mm	W:		0.8 ± 0.1	mm
3	Dielectric	C0G		8	Inner electrode		Ni		
4	Capacitance	10 pF			Termination		Cu		
(5)	Capacitance	±5 %			Plating		Sn ′	100%	(Pb Free)
	tolerance			9	Product		Auto	omotive	
6	Rated Voltage	50 V		10	Grade code		Star	ndard	
7	Thickness	0.8 ± 0.1 mm		11)	Packaging		Car	dboard Typ	e, 7" reel

B. Reliability Test and Judgement condition

	Performance	Test condition				
High Temperature	Appearance : No abnormal exterior appearance	Unpowered, 1000hrs@T=125℃				
Exposure	Capacitance Change :	Measurement at 24±2hrs after test conclusion				
	within ±2.5% or ±0.25pF whichever is larger					
	Q: 600 min					
	IR : More than 10,000№ or 500№×μF					
	Whichever is Smaller					
Temperature Cycling	Appearance : No abnormal exterior appearance	1000Cycles				
	Capacitance Change :	Measurement at 24±2hrs after test conclusion				
	within ±2.5% or ±0.25pF whichever is larger	1 cycle condition :				
	Q: 600 min	-55+0/-3 °C (15±3min) -> Room Temp(1min.)				
	IR : More than 10,000№ or 500№×μF	-> 125+3/-0 ℃ (15±3min) -> Room Temp(1min.)				
	Whichever is Smaller					
Destructive Physical	No Defects or abnormalities	Per EIA 469				
Analysis						
Humidity Bias	Appearance : No abnormal exterior appearance	1000hrs 85 ℃/85%RH, Rated Voltage and 1.3~1.5V,				
	Capacitance Change :	Add 100kohm resistor				
	within ±2.5% or ±0.25pF whichever is larger	Measurement at 24±2hrs after test conclusion				
	Q: 133.33 min	The charge/discharge current is less than 50mA.				
	IR : More than 500№ or 25№×μF					
	Whichever is Smaller					
High Temperature	Appearance : No abnormal exterior appearance	1000hrs @ TA=125℃, 200% Rated Voltage,				
Operating Life	Capacitance Change :	Measurement at 24±2hrs after test conclusion				
	within ±3.0% or ±0.3pF whichever is larger	The charge/discharge current is less than 50mA.				
	Q: 300 min					
	IR : More than 10,000№ or 500№×μF					
	Whichever is Smaller					

	Performance	Test condition					
External Visual No abnormal exterior appearance		Visual inspection					
Physical Dimensions	Within the specified dimensions	Using The calipers					
Mechanical Shock	Appearance : No abnormal exterior appearance Capacitance Change : within ±2.5% or ±0.25pF whichever is larger	Three shocks in each direction should be applied along 3 mutually perpendicular axes of the test specimen (18 shocks) Peakvalue Duration Wave Velocity					
	Q, IR: initial spec.	1,500G 0.5ms Half sine 4.7m/sec.					
Vibration	Appearance : No abnormal exterior appearance	5g's for 20min., 12cycles each of 3 orientations,					
	Capacitance Change :	Use 8"x5" PCB 0.031" Thick 7 secure points on one long side					
	within ±2.5% or ±0.25pF whichever is larger	and 2 secure points at corners of opposite sides. Parts mounted					
	Q, IR : initial spec.	within 2" from any secure point. Test from 10~2000Hz.					
Resistance to	Appearance : No abnormal exterior appearance	Solder pot : 260±5 ℃, 10±1sec.					
Solder Heat	Capacitance Change :						
	within ±2.5% or ±0.25pF whichever is larger						
	Q, IR: initial spec.	1.50					
ESD	Appearance : No abnormal exterior appearance	AEC-Q200-002					
	Capacitance Change :						
	within ±2.5% or ±0.25pF whichever is larger						
	Q, IR: initial spec.						
Solderability	95% of the terminations is to be soldered	a) Preheat at 155°C for 4 hours, Immerse in solder for 5s at 245±5°C					
	evenly and continuously	b) Steam aging for 8 hours, Immerse in solder for 5s at 245±5°C					
		c) Steam aging for 8 hours, Immerse in solder for 120s at 260±5 °C					
Electrical	Capacitance : Within specified tolerance	solder: a solution ethanol and rosin					
Characterization	Q: 600 max.	The Capacitance /Q should be measured at 25 °C, 1\mb±10%, 0.5~5Vrms					
Ondi doton Edition	IR(25℃): More than 100,000MΩ or 1,000MΩ×μF	I.R. should be measured with a DC voltage not exceeding					
	IR(125 °C): More than 100,000 Ms or $100 \text{M}\Omega \times \mu\text{F}$	Rated Voltage @25°C, @125°C for 60~120 sec.					
	Whichever is Smaller	Dielectric Strength: 300% of the rated voltage for 1~5 seconds					
	Dielectric Strength						
Board Flex	Appearance : No abnormal exterior appearance	Bending to the limit (3mm) for 60 seconds					
	Capacitance Change :						
	within ±5.0% or ±0.5pF whichever is larger						
Terminal	Appearance : No abnormal exterior appearance	10N, for 60±1 sec.					
Strength(SMD)	Capacitance Change :						
	within ±2.5% or ±0.25pF whichever is larger						
Beam Load	Destruction value should be exceed	Beam speed					
	Chip Length < 2.5mm	0.5±0.05mm/sec					
	a) Chip Thickness > 0.5mm : 20N						
	b) Chip Thickness ≤ 0.5mm : 8N						
Temperature	COG						
Characteristics	(From -55℃ to 125℃, Capacitance change should be within ±30PPM/℃)						

C. Recommended Soldering method:

Reflow (Reflow Peak Temperature : 260+0/-5°C, 10sec. Max)

Meet IPC/JEDEC J-STD-020 D Standard



A Product specifications included in the specifications are effective as of March 1, 2013.

Please be advised that they are standard product specifications for reference only.

We may change, modify or discontinue the product specifications without notice at any time.

So, you need to approve the product specifications before placing an order.

Should you have any question regarding the product specifications,

please contact our sales personnel or application engineers.