



New Product

Si7459DP
 Vishay Siliconix

P-Channel 30-V (D-S) MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
-30	0.0068 @ $V_{GS} = -10$ V	-22

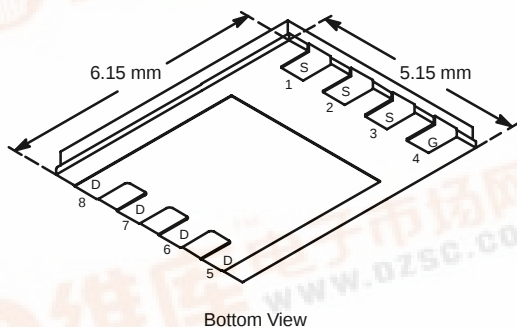
FEATURES

- TrenchFET® Power MOSFETS
- New Low Thermal Resistance PowerPAK® Package with Low 1.07-mm Profile

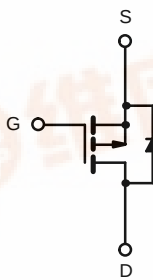
APPLICATIONS

- Battery and Load Switching
 - Notebook Computers
 - Notebook Battery Packs

PowerPAK SO-8



Ordering Information: Si7459DP-T1 - E3



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter		Symbol	10 secs	Steady State	Unit
Drain-Source Voltage		V_{DS}	-30		V
Gate-Source Voltage		V_{GS}	± 25		
Continuous Drain Current ($T_J = 150^{\circ}\text{C}$) ^a	$T_A = 25^{\circ}\text{C}$	I_D	-22	-13	A
	$T_A = 70^{\circ}\text{C}$		-17	-10	
Pulsed Drain Current		I_{DM}	-60		
continuous Source Current (Diode Conduction) ^a		I_S	-4.5	-1.6	
Maximum Power Dissipation ^a	$T_A = 25^{\circ}\text{C}$	P_D	5.4	1.9	W
	$T_A = 70^{\circ}\text{C}$		3.4	1.2	
Operating Junction and Storage Temperature Range		T_J, T_{stg}	-55 to 150		$^{\circ}\text{C}$

THERMAL RESISTANCE RATINGS

Parameter		Symbol	Typical	Maximum	Unit
Maximum Junction-to-Ambient ^a	$t \leq 10$ sec	R_{thJA}	18	23	$^\circ\text{C/W}$
	Steady State		52	65	
Maximum Junction-to-Case (Drain)	Steady State	R_{thJC}	1.0	1.5	

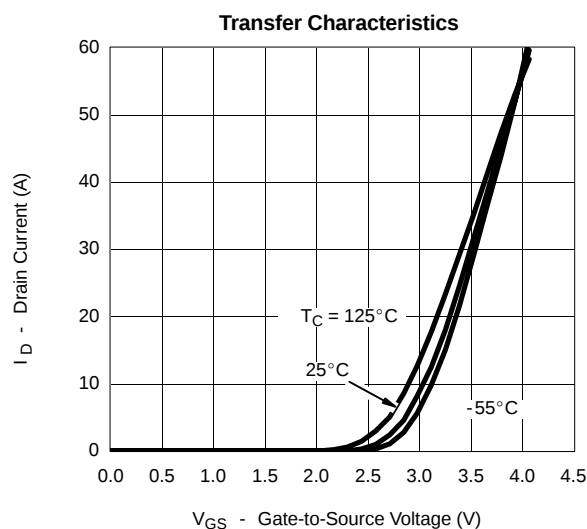
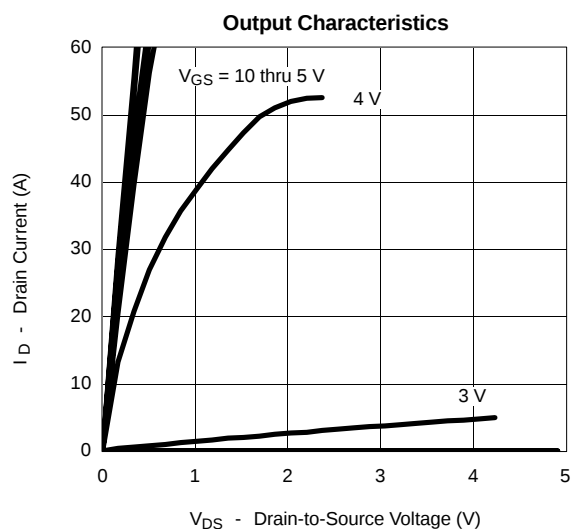
Notes:
^a Surface Mounted on 1" x 1" FR4 Board.

Si7459DP**Vishay Siliconix****New Product****SPECIFICATIONS (T_J = 25 °C UNLESS OTHERWISE NOTED)**

Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
Static						
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = -250 μA	-1.0		-3.0	V
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ±20 V			±100	nA
		V _{DS} = 0 V, V _{GS} = ±25 V			±200	
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = -30 V, V _{GS} = 0 V			-1	μA
		V _{DS} = -30 V, V _{GS} = 0 V, T _J = 70 °C			-10	
On-State Drain Current ^a	I _{D(on)}	V _{DS} = -5 V, V _{GS} = -10 V	-30			A
Drain-Source On-State Resistance ^a	r _{DS(on)}	V _{GS} = -10 V, I _D = -22 A		0.0056	0.0068	Ω
Forward Transconductance ^a	g _{fs}	V _{DS} = -15 V, I _D = -22 A		50		S
Diode Forward Voltage ^a	V _{SD}	I _S = -2.9 A, V _{GS} = 0 V		-0.71	-1.1	V
Dynamic^b						
Total Gate Charge	Q _g	V _{DS} = -15 V, V _{GS} = -10 V, I _D = -22 A		113	170	nC
Gate-Source Charge	Q _{gs}			17		
Gate-Drain Charge	Q _{gd}			32.5		
Turn-On Delay Time	t _{d(on)}	V _{DD} = -15 V, R _L = 15 Ω I _D ≅ -1 A, V _{GEN} = -10 V, R _G = 6 Ω		25	40	ns
Rise Time	t _r			20	30	
Turn-Off Delay Time	t _{d(off)}			180	270	
Fall Time	t _f			130	200	
Gate Resistance	R _g			4		Ω
Source-Drain Reverse Recovery Time	t _{rr}	I _F = -2.9 A, di/dt = 100 A/μs		100	150	ns

Notes

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
b. Guaranteed by design, not subject to production testing.

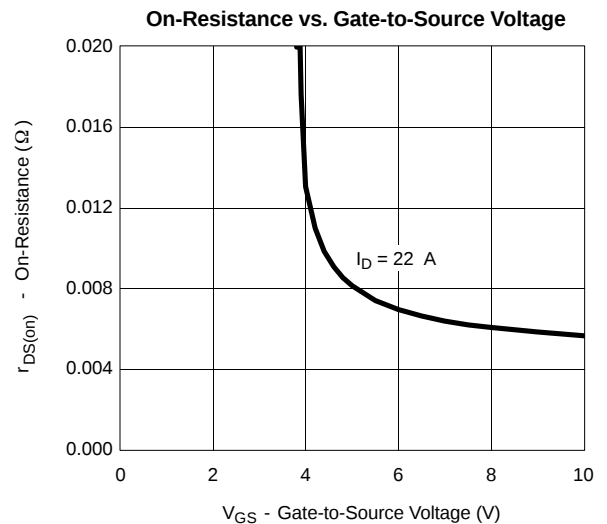
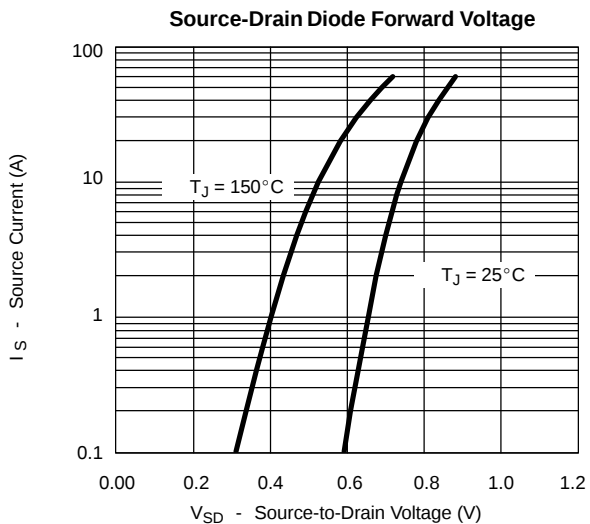
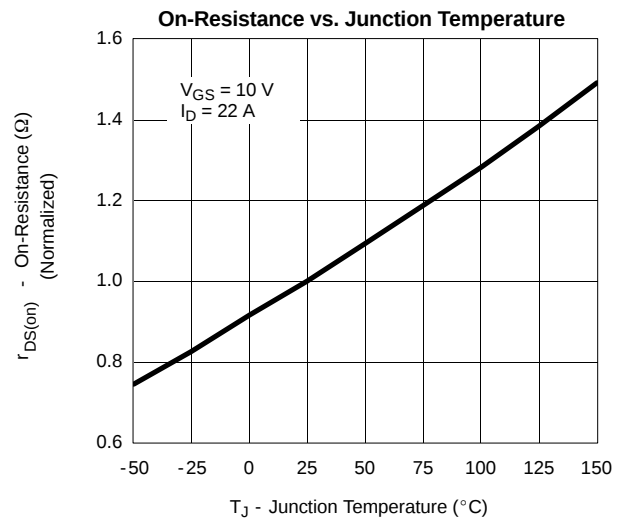
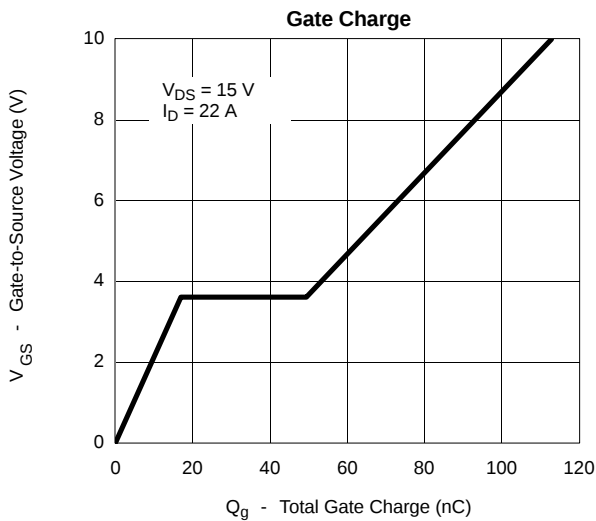
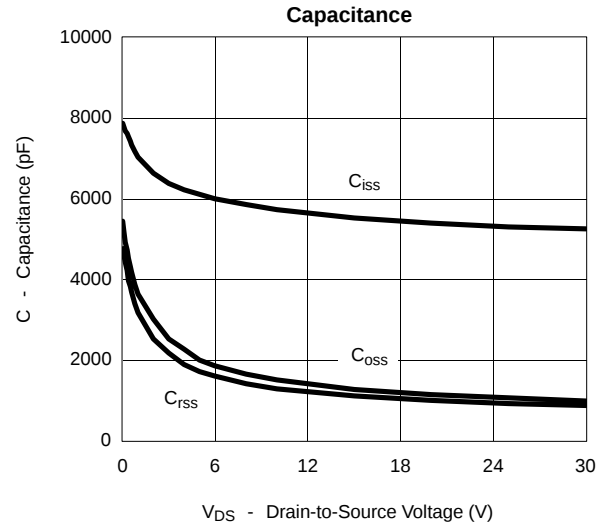
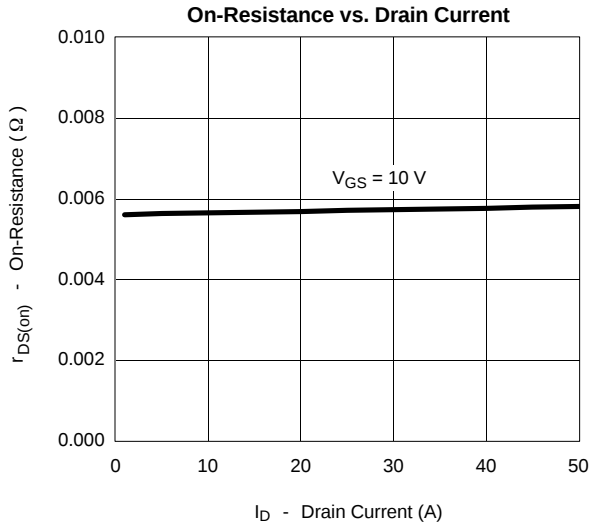
TYPICAL CHARACTERISTICS (25 °C UNLESS NOTED)



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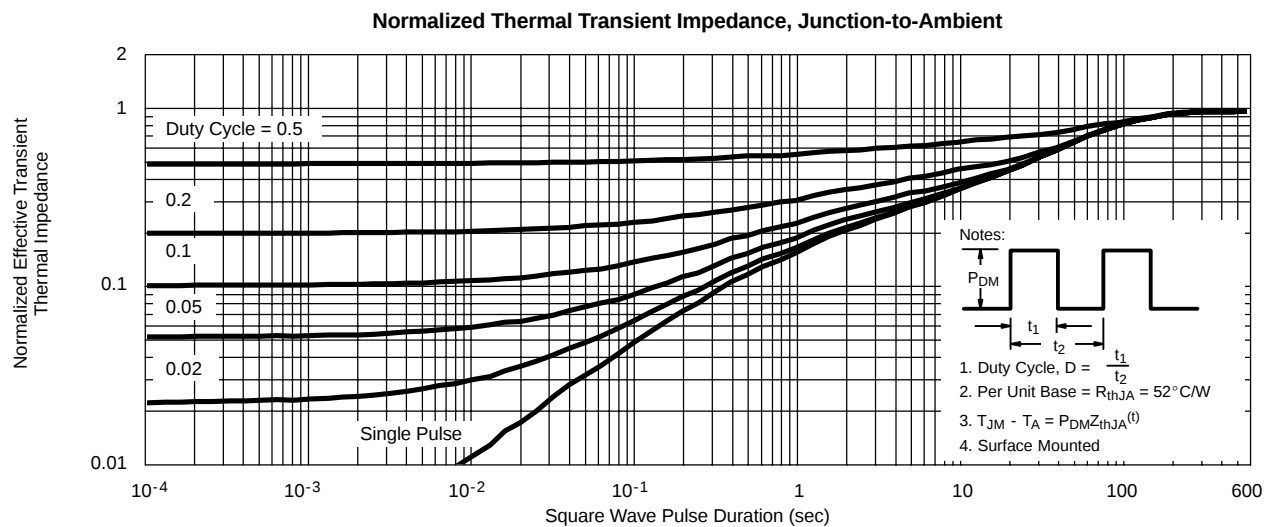
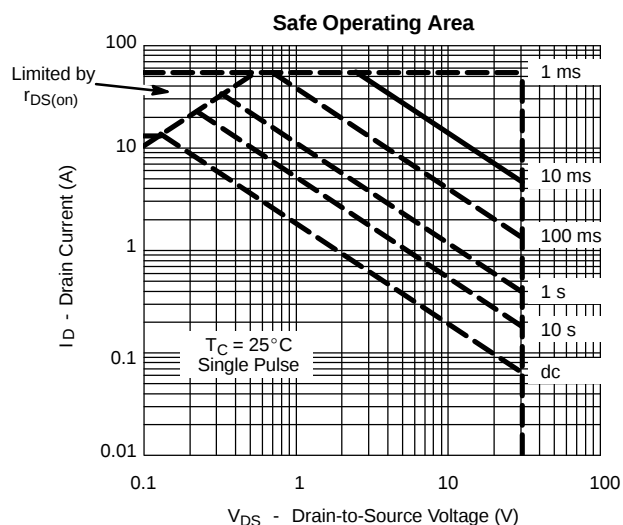
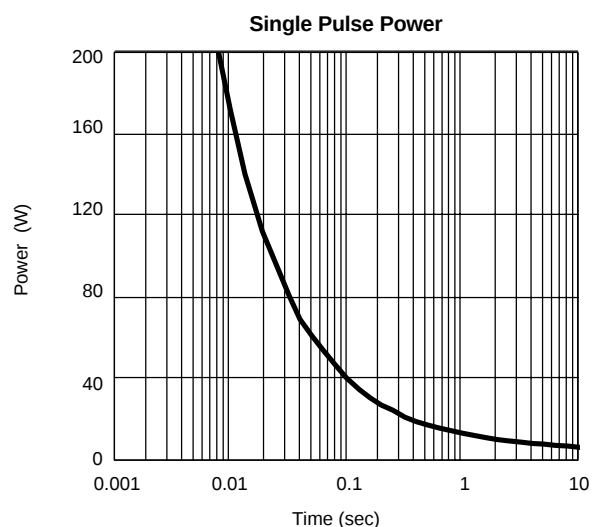
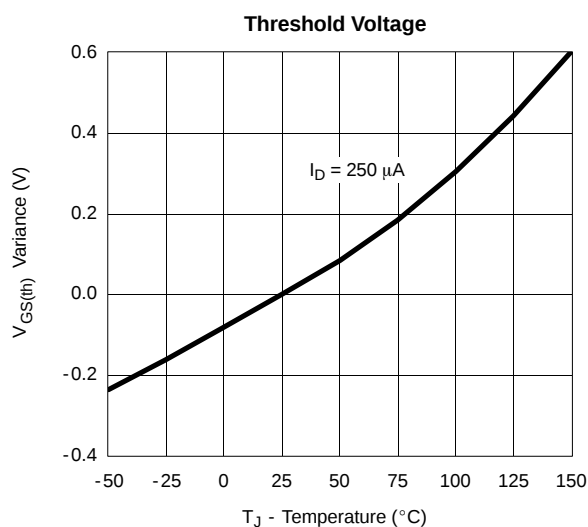
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