

**DIGITAL OUTPUT PHOTO REFLECTOR****■ GENERAL DESCRIPTION**

The NJL5804K is thin package digital output type photo reflector which consist of New JRC original designed one chip photo receiving IC and high output LED.

**■ FEATURES**

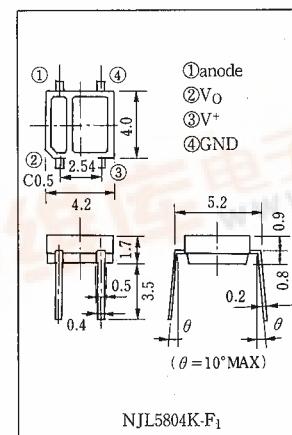
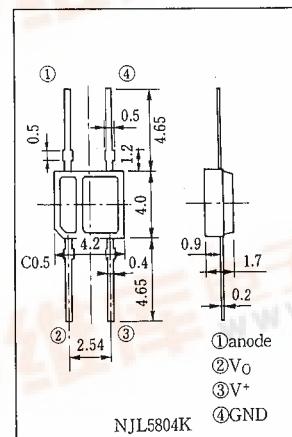
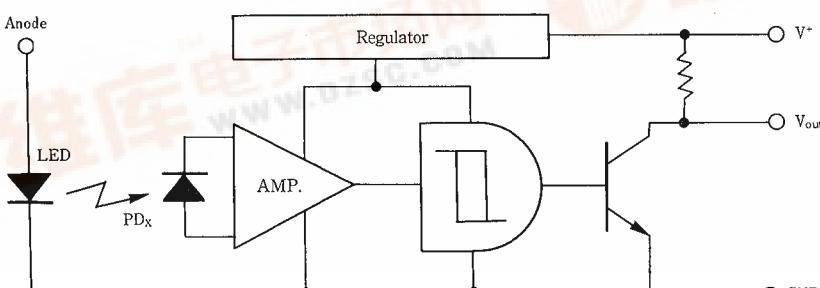
- Normally off type
- With schmitt trigger circuit
- TTL Compatible
- Built-in visible light cut-off filter.
- With pull up resistance

**■ APPLICATIONS**

- Tape end sensor
- Reel rotation sensor
- Paper detector, Paper end sensor
- Bar code reader
- Sensor of FDD, Robot, manufacturing installation, etc.

**■ ABSOLUTE MAXIMUM RATINGS (Ta=25°C)**

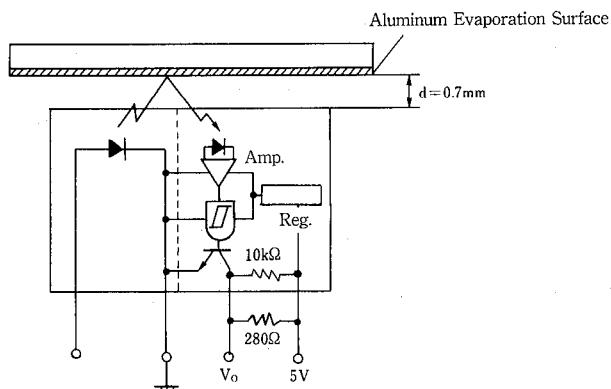
PARAMETER	SYMBOL	RATINGS	UNIT
Emitter			
Forward Current (Continuous)	I <sub>F</sub>	50	mA
Reverse Voltage (Continuous)	V <sub>R</sub>	6	V
Power Dissipation	P <sub>D</sub>	75	mW
Detector			
Supply Voltage	V <sup>+</sup>	16	V
High Level Output Voltage	V <sub>OH</sub>	16	V
Low Level Output Current	I <sub>OL</sub>	50	mA
Power Dissipation	P <sub>O</sub>	110	mW
Coupler			
Total Power Dissipation	P <sub>tot</sub>	130	mW
Operating Temperature	T <sub>opr</sub>	-20~+85	°C
Storage Temperature	T <sub>sg</sub>	-30~+100	°C
Soldering Temperature	T <sub>sot</sub>	260	°C
		(5sec. 1.5mm from body)	

**■ OUTLINE (typ.) Unit: mm****■ BLOCK DIAGRAM**

■ ELECTRO-OPTICAL CHARACTERISTICS ( $T_a = 25^\circ\text{C}$ )

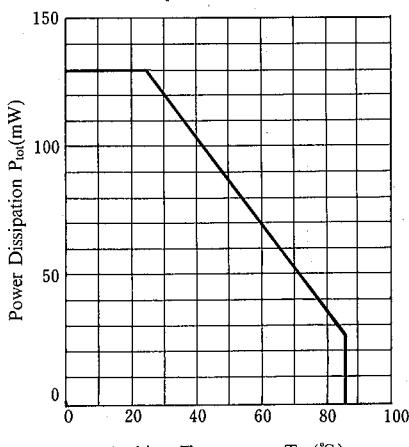
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Emitter						
Forward Voltage	$V_F$	$I_F = 10\text{mA}$	—	1.1	1.3	V
Reverse Current	$I_R$	$V_R = 6\text{V}$	—	—	1.0	$\mu\text{A}$
Capacitance	$C_t$	$V_R = 0\text{V}, f = 1\text{MHz}$	—	25	—	pF
Detector						
Supply Voltage Range	$V^+$		3.5	—	15	V
Low Level Output Voltage	$V_{OL}$	$I_{OL} = 16\text{mA}, V^+ = 5\text{V}, I_F = 10\text{mA}, d = 0.7\text{mm}$	—	0.2	0.5	V
High Level Output Voltage	$V_{OH}$	$V^+ = 15\text{V}, I_F = 0\text{mA}$	14.5	—	—	V
Low Level Supply Current	$I_{CL}$	$V^+ = 5\text{V}, I_F = 10\text{mA}, d = 0.7\text{mm}$	—	3	10	mA
High Level Supply Current	$I_{CH}$	$V^+ = 5\text{V}, I_F = 0\text{mA}$	—	4.5	10	mA
Coupled						
$H \rightarrow L$ Threshold Input Current	$I_{FHLL}/I_{FHHL}$	$V^+ = 5\text{V}, R_L = 280\Omega, d = 0.7\text{mm}$	—	—	10	mA
Hysteresis	$I_{FHLL}/I_{FHHL}$	$V^+ = 5\text{V}, R_L = 280\Omega, d = 0.7\text{mm}$	—	0.8	—	—
$H \rightarrow L$ Delay Time	$t_{PHL}$	$V^+ = 5\text{V}, R_L = 280\Omega, I_F = 10\text{mA}, d = 0.7\text{mm}$	—	10	—	$\mu\text{s}$
$L \rightarrow H$ Delay Time	$t_{PLH}$	$V^+ = 5\text{V}, R_L = 280\Omega, I_F = 10\text{mA}, d = 0.7\text{mm}$	—	5	—	$\mu\text{s}$
Fall Time	$t_f$	$V^+ = 5\text{V}, R_L = 280\Omega, I_F = 10\text{mA}, d = 0.7\text{mm}$	—	0.1	—	$\mu\text{s}$
Rise Time	$t_r$	$V^+ = 5\text{V}, R_L = 280\Omega, I_F = 10\text{mA}, d = 0.7\text{mm}$	—	0.1	—	$\mu\text{s}$

## ■ MEASURING SPECIFICATION FOR THRESHOLD INPUT CURRENT

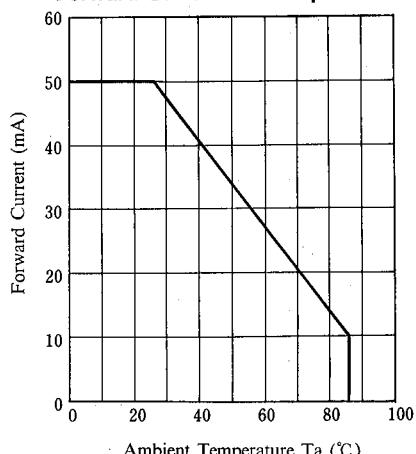


## ■ MAXIMUM RATING CURVES

Power Dissipation vs. Temperature



Forward Current vs. Temperature



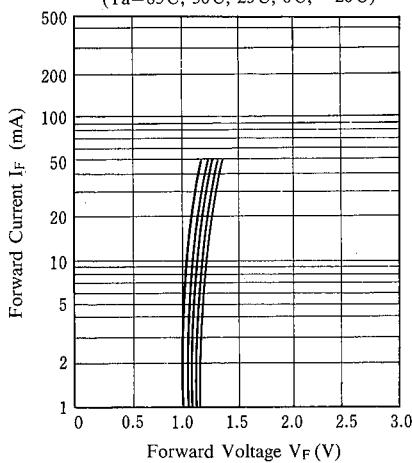
# NJL5804K

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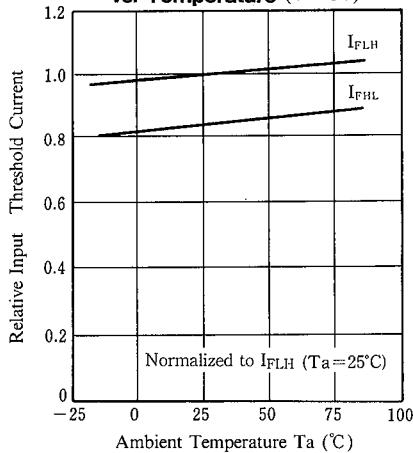
## ■ TYPICAL CHARACTERISTICS

**Forward Current vs. Forward Voltage**

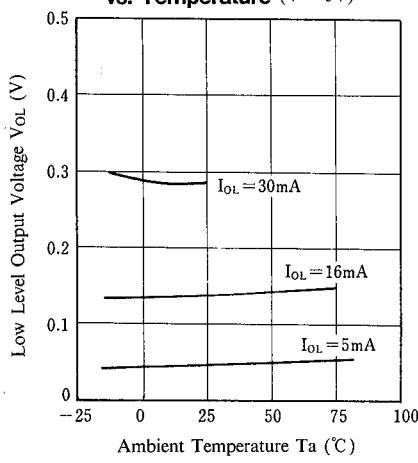
(Ta=85°C, 50°C, 25°C, 0°C, -20°C)



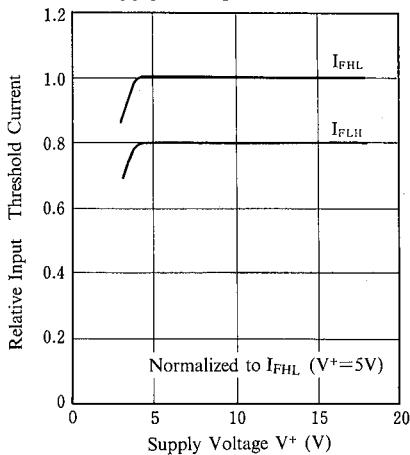
**Input Threshold Current vs. Temperature (V<sup>+</sup>=5V)**



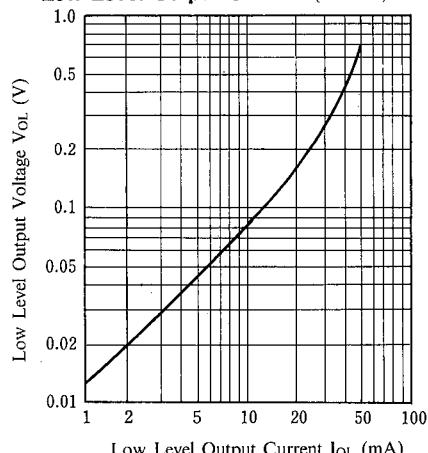
**Low Level Output Voltage vs. Temperature (V<sup>+</sup>=5V)**



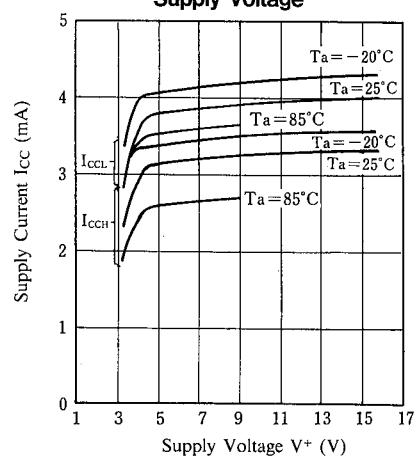
**Input Threshold Current vs. Supply Voltage (Ta=25°C)**

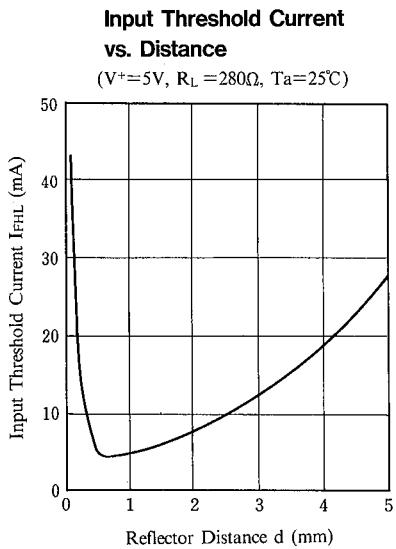
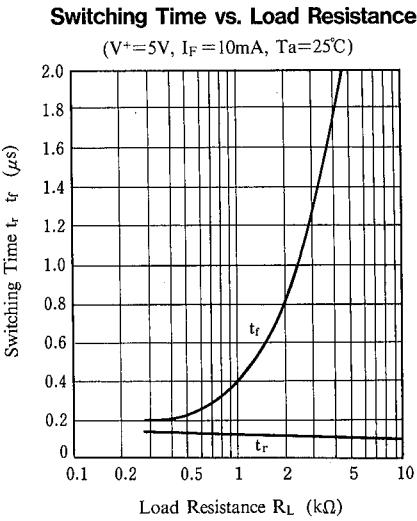
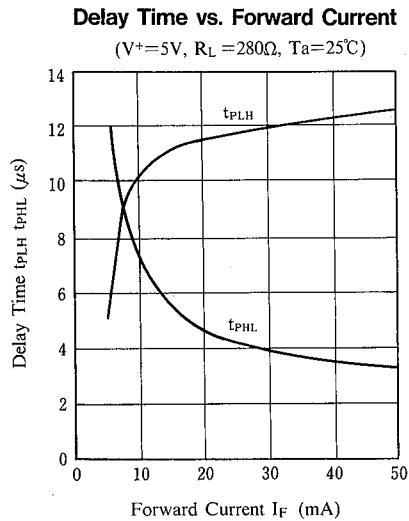


**Low Level Output Voltage vs. Low Level Output Current (V<sup>+</sup>=5V, Ta=25°C)**

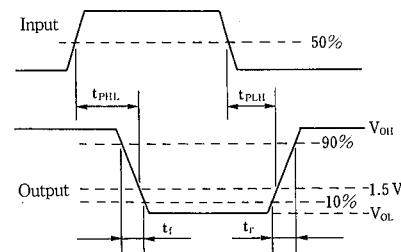
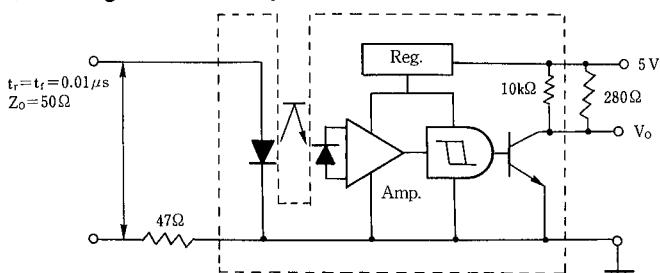


**Supply Current vs. Supply Voltage**





### Measuring Circuit for Response Time



# NJL5804K

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## MEMO

### [CAUTION]

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