

MECHANICAL CASE OUTLINE PACKAGE DIMENSIONS

捷多邦，专业PCB打样工厂，24小时加急出货

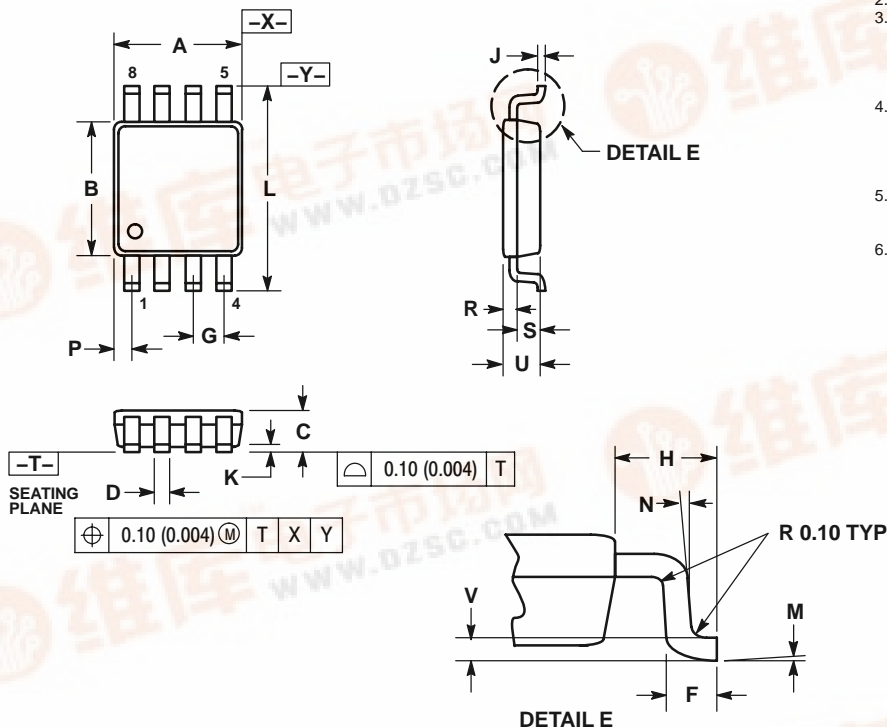
ON Semiconductor

ON

US8 CASE 493-02 ISSUE A

DATE 25 JUN 2003

SCALE 4:1

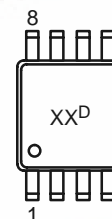


NOTES:

1. DIMENSIONING AND TOLERANCING PER ANSI Y14.5M, 1982.
2. CONTROLLING DIMENSION: MILLIMETERS.
3. DIMENSION "A" DOES NOT INCLUDE MOLD FLASH, PROTRUSION OR GATE BURR. MOLD FLASH, PROTRUSION AND GATE BURR SHALL NOT EXCEED 0.140 MM (0.0055") PER SIDE.
4. DIMENSION "B" DOES NOT INCLUDE INTER-LEAD FLASH OR PROTRUSION. INTER-LEAD FLASH AND PROTRUSION SHALL NOT EXCEED 0.140 MM (0.0055") PER SIDE.
5. LEAD FINISH IS SOLDER PLATING WITH THICKNESS OF 0.0076-0.0203 MM. (300-800 °).
6. ALL TOLERANCE UNLESS OTHERWISE SPECIFIED ± 0.0508 (0.0002 °).

DIM	MILLIMETERS		INCHES	
	MIN	MAX	MIN	MAX
A	1.90	2.10	0.075	0.083
B	2.20	2.40	0.087	0.094
C	0.60	0.90	0.024	0.035
D	0.17	0.25	0.007	0.010
E	0.20	0.35	0.008	0.014
F	0.50 BSC		0.020 BSC	
G	0.40 REF		0.016 REF	
H	0.10	0.18	0.004	0.007
J	0.00	0.10	0.000	0.004
K	3.00	3.20	0.118	0.126
L	0°	6°	0°	6°
M	5°	10°	5°	10°
N	0.23	0.34	0.010	0.013
P	0.23	0.33	0.009	0.013
R	0.37	0.47	0.015	0.019
S	0.60	0.80	0.024	0.031
U	0.12 BSC		0.005 BSC	

GENERIC MARKING DIAGRAM*




XX = Specific Device Code
D = Date Code

*This information is generic. Please refer to device data sheet for actual part marking.

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