



DIM375WKS06-S000

IGBT Chopper Module (Upper Arm Control)

DS5732-1.0 February 2004

FEATURES

- Low Forward Voltage Drop
- Isolated Copper Baseplate

APPLICATIONS

- Choppers
- Motor Controllers

The Powerline range of modules includes half bridge, chopper, bi-directional, dual and single switch configurations covering voltages from 600V to 3300V and currents up to 3600A.

The DIM375WKS06-S000 is a 600V n channel enhancement mode, insulated gate bipolar transistor (IGBT) chopper module configured with the upper arm of the bridge controlled. The module is suitable for a variety of medium voltage applications in motor drives and power conversion.

The IGBT has a wide reverse bias safe operating area (RBSOA) for ultimate reliability in demanding applications.

These modules incorporate electrically isolated base plates and low inductance construction enabling circuit designers to optimise circuit layouts and utilise earthed heat sinks for safety.

Typical applications include dc motor drives, ac pwm drives and ups systems..

ORDERING INFORMATION

Order as: **DIM375WKS06-S000**

Note: When ordering, use complete part number.

KEY PARAMETERS

V_{CES}		600V
$V_{CE(sat)}^*$	(typ)	2.1V
I_C	(max)	375A
$I_{C(PK)}$	(max)	750A

*(measured at the power busbars and not the auxiliary terminals)

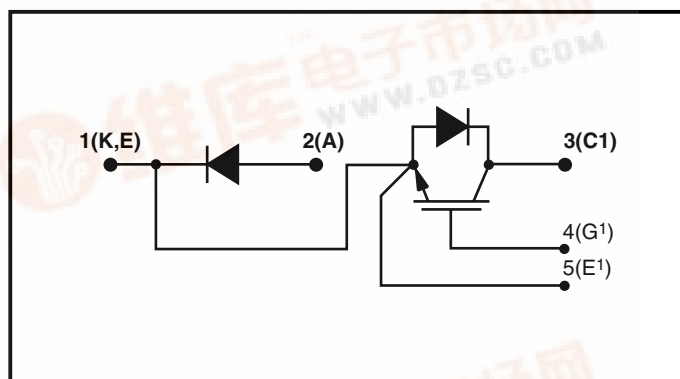


Fig. 1 Chopper circuit diagram



Outline type code: W

(See package details for further information)

Fig. 2 Electrical connections - (not to scale)

ABSOLUTE MAXIMUM RATINGS - PER ARM

Stresses above those listed under 'Absolute Maximum Ratings' may cause permanent damage to the device. In extreme conditions, as with all semiconductors, this may include potentially hazardous rupture of the package. Appropriate safety precautions should always be followed. Exposure to Absolute Maximum Ratings may affect device reliability.

T_{case} = 25°C unless stated otherwise

Symbol	Parameter	Test Conditions	Max.	Units
V _{CES}	Collector-emitter voltage	V _{GE} = 0V	600	V
V _{GES}	Gate-emitter voltage	-	±20	V
I _C	Continuous collector current	T _{case} = 65°C	375	A
I _{C(PK)}	Peak collector current	1ms, T _{case} = 95°C	750	A
P _{max}	Max. transistor power dissipation	T _{case} = 25°C, T _j = 150°C	1736	W
I ² t	Diode I ² t value	V _R = 0, t _p = 10ms, T _{vj} = 125°C	TBD	kA ² s
V _{isol}	Isolation voltage - per module	Commoned terminals to base plate. AC RMS, 1 min, 50Hz	2.5	kV

THERMAL AND MECHANICAL RATINGS

Internal insulation: Al₂O₃

Clearance: 13mm

Baseplate material: Cu

CTI (Critical Tracking Index): 175

Creepage distance: 24mm

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
R _{th(j-c)}	Thermal resistance - transistor arm	Continuous dissipation - junction to case	-	-	72	°C/kW
R _{th(j-c)}	Thermal resistance - diode (per arm) (Antiparallel and freewheel diode)	Continuous dissipation - junction to case	-	-	135	°C/kW
R _{th(c-h)}	Thermal resistance - case to heatsin (per module)	Mounting torque 5Nm (with mounting grease)	-	-	15	°C/kW
T _j	Junction temperature	Transistor	-	-	150	°C
		Diode	-	-	125	°C
T _{stg}	Storage temperature range	-	-40	-	125	°C
-	Screw torque	Mounting - M6	3	-	5	Nm
		Electrical connections - M6	2.5	-	5	Nm

ELECTRICAL CHARACTERISTICS

$T_{case} = 25^{\circ}\text{C}$ unless stated otherwise.

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
I_{CES}	Collector cut-off current (IGBT and Diode arm)	$V_{GE} = 0V, V_{CE} = V_{CES}$	-	-	2	mA
		$V_{GE} = 0V, V_{CE} = V_{CES}, T_{case} = 125^{\circ}\text{C}$	-	-	10	mA
I_{GES}	Gate leakage current	$V_{GE} = \pm 20V, V_{CE} = 0V$	-	-	2	μA
$V_{GE(TH)}$	Gate threshold voltage	$I_C = 15\text{mA}, V_{GE} = V_{CE}$	4.5	5.5	7.5	V
$V_{CE(sat)}^{\dagger}$	Collector-emitter saturation voltage	$V_{GE} = 15V, I_C = 350A$	-	2.1	2.6	V
		$V_{GE} = 15V, I_C = 350A, T_{case} = 125^{\circ}\text{C}$	-	2.3	2.8	V
I_F	Diode forward current	DC	-	-	375	A
I_{FM}	Diode maximum forward current	$t_p = 1\text{ms}$	-	-	750	A
$V_F^{\dagger}$	Diode forward voltage (IGBT and Diode arm)	$I_F = 350A$	-	1.5	1.8	V
		$I_F = 350A, T_{case} = 125^{\circ}\text{C}$	-	1.5	1.8	V
C_{ies}	Input capacitance	$V_{CE} = 25V, V_{GE} = 0V, f = 1\text{MHz}$	-	40	-	nF
L_M	Module inductance	-	-	20	-	nH
R_{INT}	Internal transistor resistance - per arm	-	-	0.23	-	m Ω

Note:

$\dagger$ Measured at the power busbars and not the auxiliary terminals.

L^* is the circuit inductance + L_M

ELECTRICAL CHARACTERISTICS

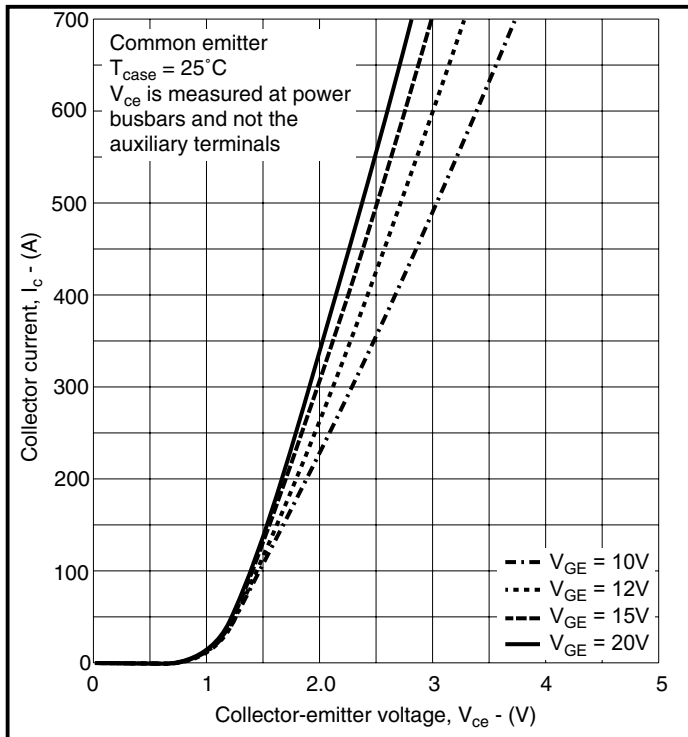
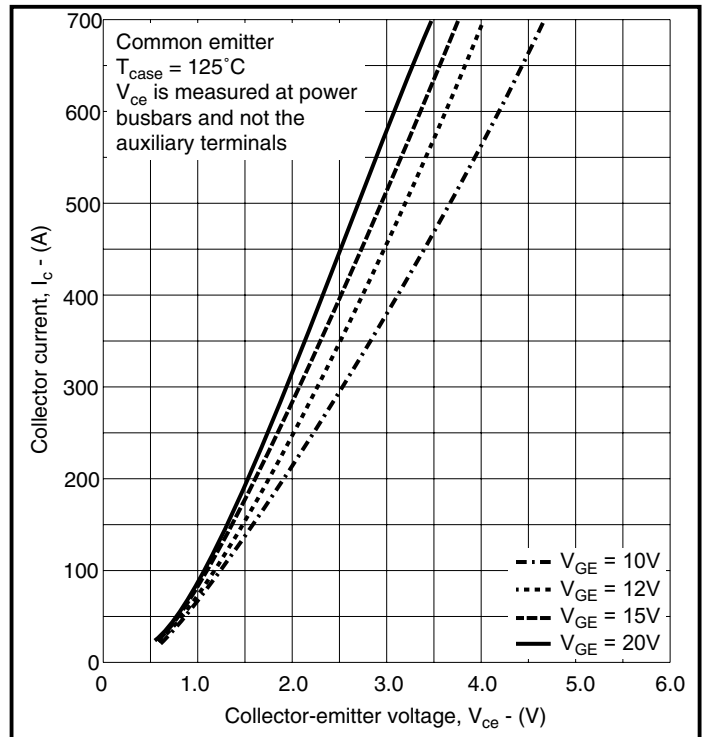
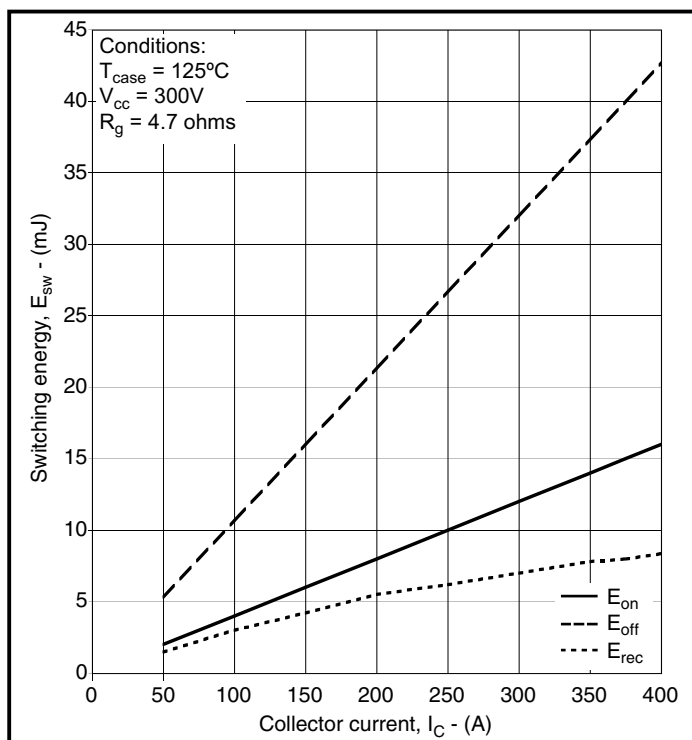
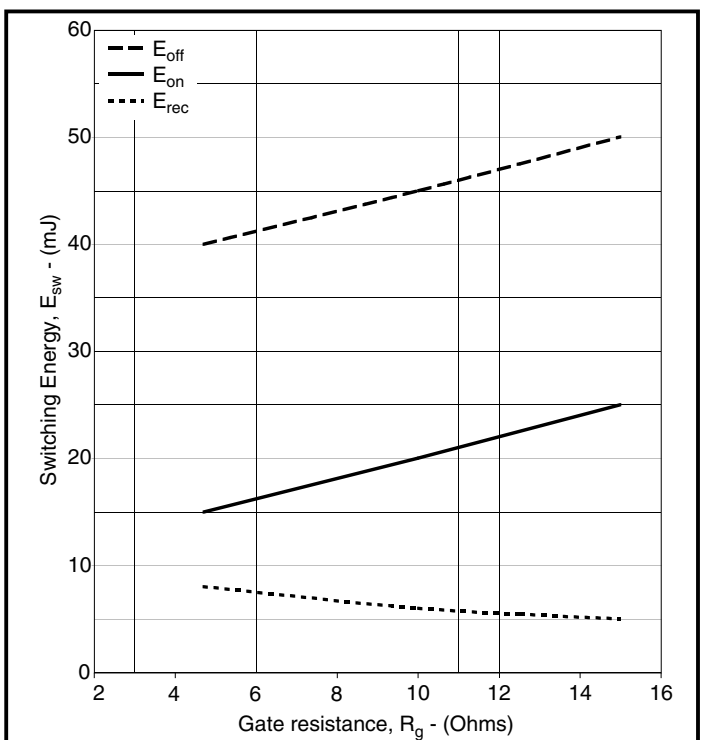
 $T_{\text{case}} = 25^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$t_{d(\text{off})}$	Turn-off delay time	$I_C = 375\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 300\text{V}$ $R_{G(\text{ON})} = R_{G(\text{OFF})} = 4.7\Omega$ $L \sim 100\text{nH}$	-	600	-	ns
t_f	Fall time		-	250	-	ns
E_{OFF}	Turn-off energy loss		-	26	-	mJ
$t_{d(\text{on})}$	Turn-on delay time		-	200	-	ns
t_r	Rise time		-	150	-	ns
E_{ON}	Turn-on energy loss		-	16	-	mJ
Q_g	Gate charge		-	3	-	μC
Q_{rr}	Diode reverse recovery charge	$I_F = 375\text{A}, V_R = 300\text{V},$ $dI_F/dt = 3000\text{A}/\mu\text{s}$	-	20	-	μC
I_{rr}	Diode reverse current		-	200	-	A
E_{REC}	Diode reverse recovery energy		-	5	-	mJ

 $T_{\text{case}} = 125^{\circ}\text{C}$ unless stated otherwise

Symbol	Parameter	Test Conditions	Min.	Typ.	Max.	Units
$t_{d(\text{off})}$	Turn-off delay time	$I_C = 375\text{A}$ $V_{GE} = \pm 15\text{V}$ $V_{CE} = 300\text{V}$ $R_{G(\text{ON})} = R_{G(\text{OFF})} = 4.7\Omega$ $L \sim 100\text{nH}$	-	650	-	ns
t_f	Fall time		-	500	-	ns
E_{OFF}	Turn-off energy loss		-	40	-	mJ
$t_{d(\text{on})}$	Turn-on delay time		-	200	-	ns
t_r	Rise time		-	100	-	ns
E_{ON}	Turn-on energy loss		-	15	-	mJ
Q_{rr}	Diode reverse recovery charge	$I_F = 375\text{A}, V_R = 300\text{V},$ $dI_F/dt = 3000\text{A}/\mu\text{s}$	-	30	-	μC
I_{rr}	Diode reverse current		-	230	-	A
E_{REC}	Diode reverse recovery energy		-	8	-	mJ

TYPICAL CHARACTERISTICS


Fig.3 Typical output characteristics

Fig.4 Typical output characteristics

Fig.4 Typical switching energy vs collector current

Fig.5 Typical switching energy vs gate resistance

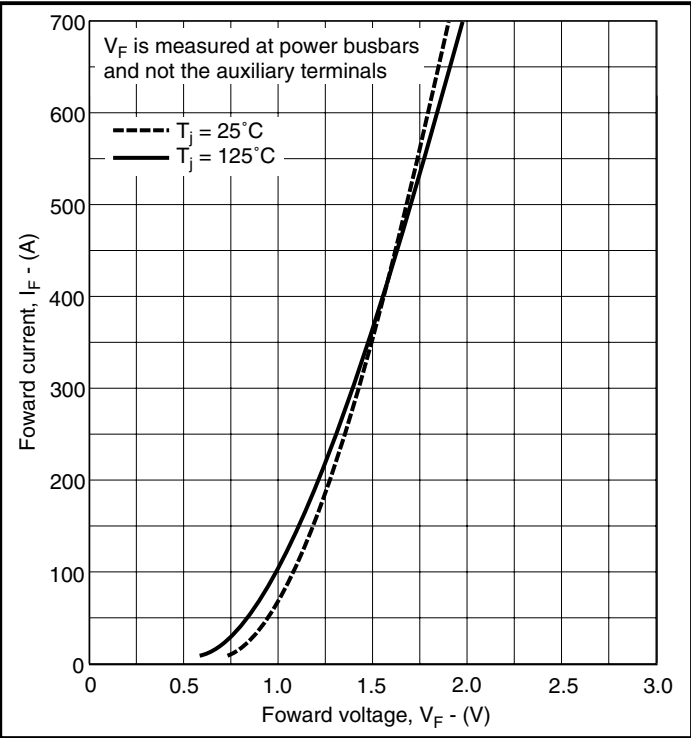


Fig.6 Diode typical forward characteristics

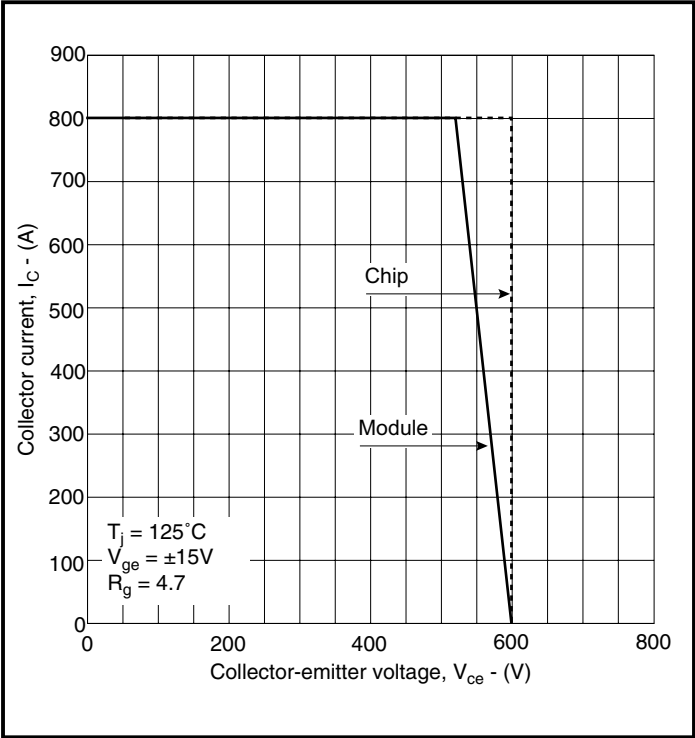


Fig.7 Reverse bias safe operating area

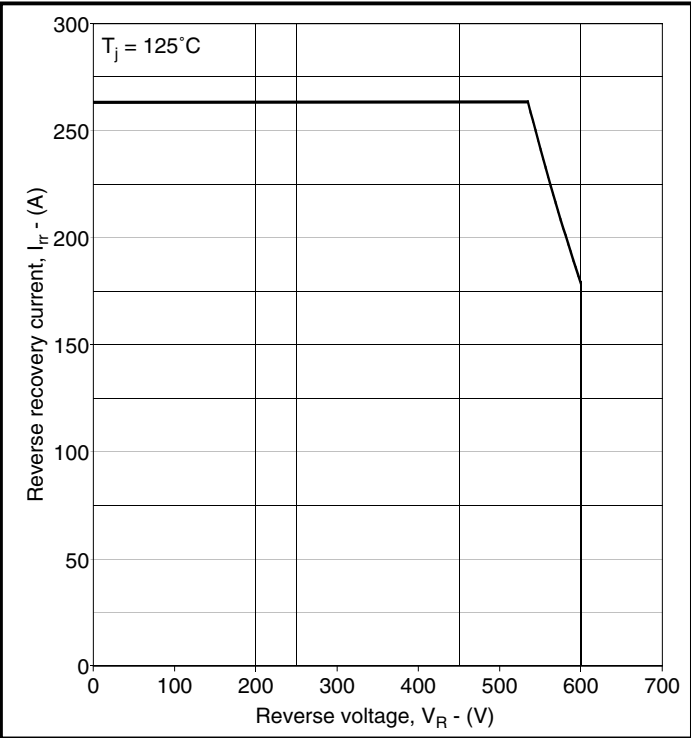


Fig.7 Diode reverse bias safe operating area

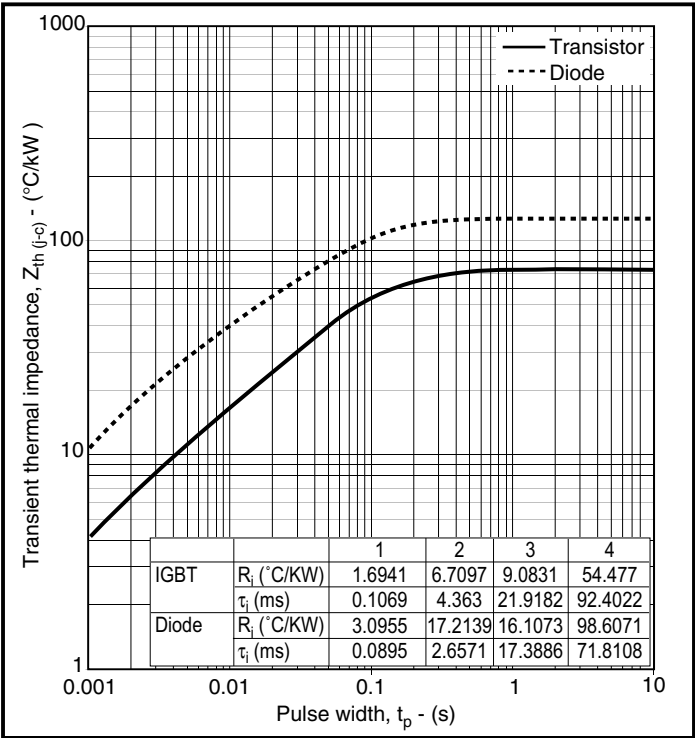


Fig.8 Transient thermal impedance

PACKAGE DETAILS

For further package information, please visit our website or contact Customer Services. All dimensions in mm, unless stated otherwise.
DO NOT SCALE.

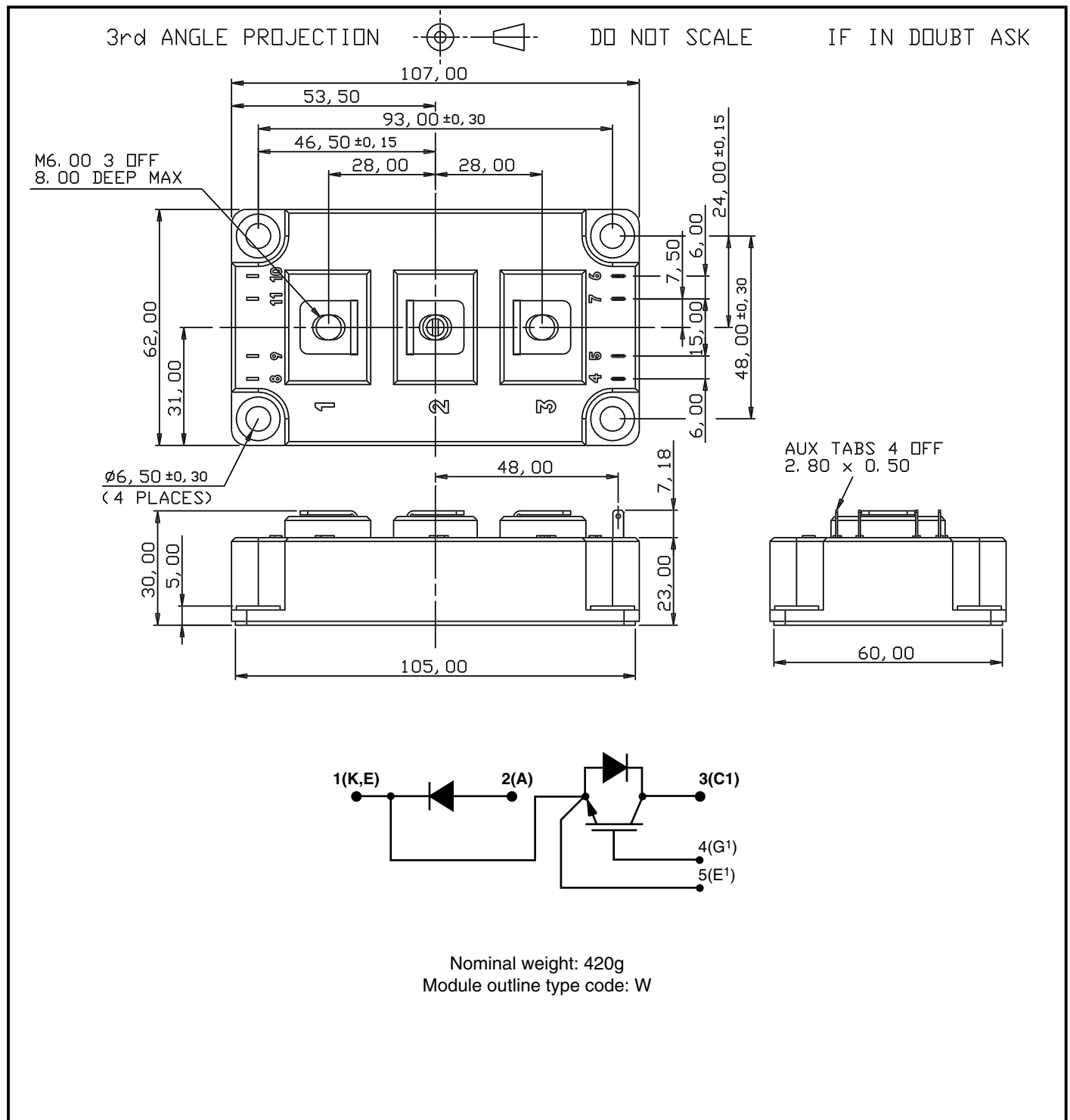


Fig. 15 Package details

POWER ASSEMBLY CAPABILITY

The Power Assembly group was set up to provide a support service for those customers requiring more than the basic semiconductor, and has developed a flexible range of heatsink and clamping systems in line with advances in device voltages and current capability of our semiconductors.

We offer an extensive range of air and liquid cooled assemblies covering the full range of circuit designs in general use today. The Assembly group offers high quality engineering support dedicated to designing new units to satisfy the growing needs of our customers.

Using the latest CAD methods our team of design and applications engineers aim to provide the Power Assembly Complete Solution (PACs).

HEATSINKS

The Power Assembly group has its own proprietary range of extruded aluminium heatsinks which have been designed to optimise the performance of Dynex semiconductors. Data with respect to air natural, forced air and liquid cooling (with flow rates) is available on request.

For further information on device clamps, heatsinks and assemblies, please contact your nearest sales representative or Customer Services.



<http://www.dynexsemi.com>

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