



Micropower Adjustable Overvoltage Protection Controllers

General Description

The MAX1807/MAX1808 monitor up to five supply rails for an overvoltage condition and provide a latched output when any one of the five supplies exceeds the trip thresholds. The latched output drives an external P-channel load switch to remove power when an overvoltage condition is detected. The latch is reset when a logic low is input to ON or the power supply is cycled.

The MAX1807 provides a 28V open-drain fault output that can be used to trigger an alert, trip a resettable fuse, or for other purposes. The MAX1808 includes a low-battery comparator with hysteresis to drive the DP output high, turning off the external P-channel switch when the input voltage is too low.

The MAX1807/MAX1808 are available in a miniature 10-pin μ MAX package.

Features

- ◆ Five 3% Accurate Overvoltage Comparators
- ◆ Series PFET Gate Driver with VGS Limiter
- ◆ 21 μ A Quiescent Supply Current
- ◆ 4 μ A Shutdown Current
- ◆ 4.4V to 28V Operating Voltage Range
- ◆ 3% Accurate Comparator with 10% Hysteresis for Low-Battery Detection (MAX1808)
- ◆ 28V Open-Drain N-Channel Output (MAX1807)
- ◆ Small 10-Pin μ MAX Package

Applications

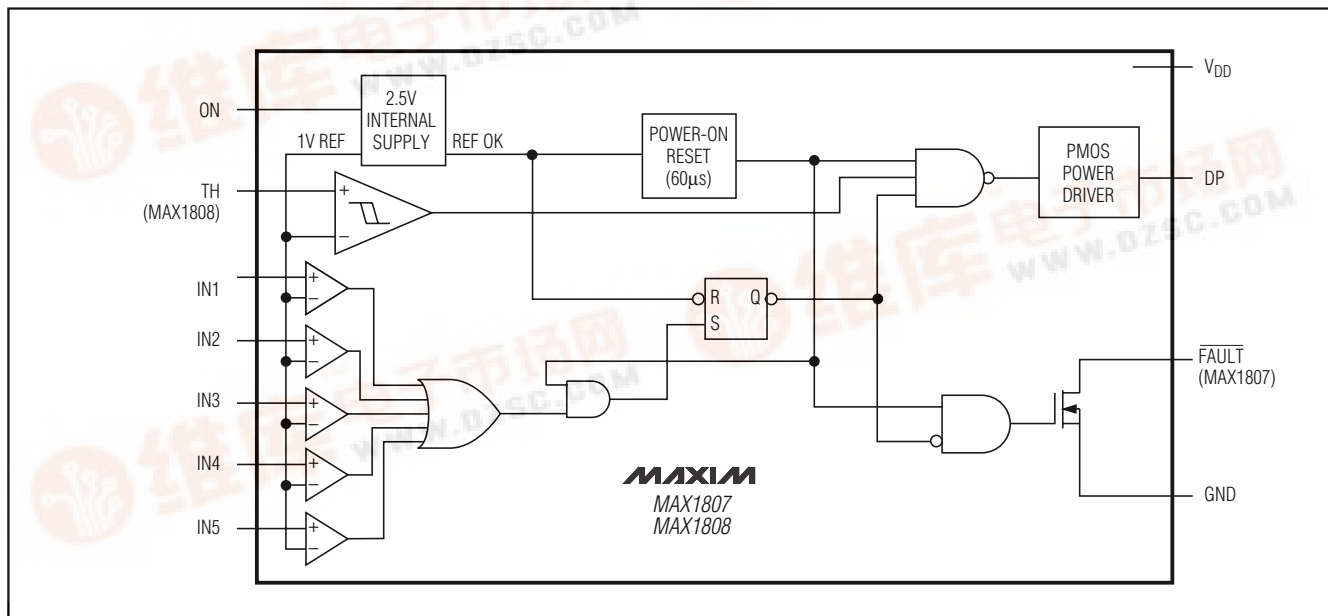
Notebook Computers
Power-Supply Modules
Multi-Output Power Supplies

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX1807EUB	-40°C to +85°C	10 μ MAX
MAX1808EUB	-40°C to +85°C	10 μ MAX

Pin Configuration and Typical Operating Circuit appear at end of data sheet.

Functional Diagram



MAX1807/MAX1808

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ABSOLUTE MAXIMUM RATINGS

IN1₊, TH to GND.....-0.3V to +6V
 ON, V_{DD}, $\overline{\text{FAULT}}$ to GND.....-0.3V to +30V
 DP to GND.....-0.3V to (V_{DD} + 0.3V)
 Continuous Power Dissipation
 10-Pin μ MAX (derate 5.6mW/°C above +70°C).....448mW

Operating Temperature Range-40°C to +85°C
 Junction Temperature+150°C
 Storage Temperature Range-65°C to +150°C
 Lead Temperature (soldering, 10s)+300°C

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS

(V_{DD} = V_{ON} = 15V, V_{IN1}–V_{IN5} = 0.5V, V_{TH} = 2.0V, C_{DP} = 5nF, $\overline{\text{FAULT}}$ = open, T_A = 0°C to +85°C, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
GENERAL						
V _{DD} Input Voltage Range		DP and $\overline{\text{FAULT}}$ in correct state (Table 1)	2		28	V
V _{DD} Operating Voltage Range			4.4		28	V
V _{DD} Undervoltage Lockout Threshold		Rising trip level, typical 2% hysteresis; when V _{DD} is below this level, DP = Hi and $\overline{\text{FAULT}}$ = Hi-Z	2	2.7	4.0	V
Supply Current		V _{TH} = 2V or 0.5V		21	45	μ A
Shutdown Current		V _{DD} = 15V, V _{ON} = GND, V _{IN1} –V _{IN5} = V _{TH} = GND		4	8.5	μ A
COMPARATORS (IN1–IN5, TH)						
IN1–IN5 Input Trip Level		Rising edge, typical 1% hysteresis, V _{DD} = 4.4V to 28V	0.97	1	1.03	V
TH Input Trip Level, Falling		V _{DD} = 4.4V to 28V (MAX1808 only)	0.97	1	1.03	V
TH Input Trip Level, Rising		V _{DD} = 4.4V to 28V (MAX1808 only)	1.045	1.1	1.155	V
IN1–IN5 Propagation Delay		IN1–IN5 rising, 10mV overdrive, V _{DD} = 4.4V		40		μ s
TH Propagation Delay		TH rising, 10mV overdrive, V _{DD} = 4.4V (MAX1808 only)		11		μ s
		TH falling, 10mV overdrive, V _{DD} = 4.4V (MAX1808 only)		40		
IN1–IN5 Input Leakage Current		V _{IN} = 1.5V		0.5	50	nA
TH Input Leakage Current		V _{TH} = 1.5V (MAX1808 only)		0.5	50	nA
ON Input High Logic Level		V _{DD} = 4.4V to 28V	1.6			V
ON Input Low Logic Level		V _{DD} = 4.4V to 28V			0.5	V
ON Input Leakage Current		V _{ON} = 5V		0.03	1.2	μ A
		V _{ON} = 28V			10	
$\overline{\text{FAULT}}$ Output High Leakage Current		V _{FAULT} = 28V (MAX1807 only)		0.01	2	μ A
$\overline{\text{FAULT}}$ Output Low Voltage		I _{SINK} = 4mA (MAX1807 only)			0.4	V

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ELECTRICAL CHARACTERISTICS (continued)

($V_{DD} = V_{ON} = 15V$, $V_{IN1}-V_{IN5} = 0.5V$, $V_{TH} = 2.0V$, $C_{DP} = 5nF$, $\overline{FAULT}$ = open, $T_A = 0^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
DP Source Current (PMOS Turn-Off)		$V_{ON} = V_{DD}$, $V_{IN1} = 1.5V$	$V_{DP} = V_{DD} - 0.4V$	1	50	μA
			$V_{DP} = V_{DD} - 2V$	5	20	mA
DP Sink Current (PMOS Turn-On)		$V_{DP} = V_{DD} - 5V$	4	50		mA
DP Pullup Current (PMOS Off)		$V_{DP} = V_{DD} - 2V$, $V_{ON} = GND$, in shutdown state		25		μA
DP Turn-On Clamp Voltage ($V_{DD} - V_{DP}$)		$V_{ON} = V_{DD}$, $I_{DPSINK} = 10\mu A$	$V_{DD} = 8.5V$ to $28V$	7.5	9.5	11.5
			$V_{DD} = 4.4V$	3.4	4.1	4.4

ELECTRICAL CHARACTERISTICS

($V_{DD} = V_{ON} = 15V$, $V_{IN1}-V_{IN5} = 0.5V$, $V_{TH} = 2.0V$, $C_{DP} = 5nF$, $\overline{FAULT}$ = open, $T_A = -40^{\circ}C$ to $+85^{\circ}C$, unless otherwise noted.) (Note 1)

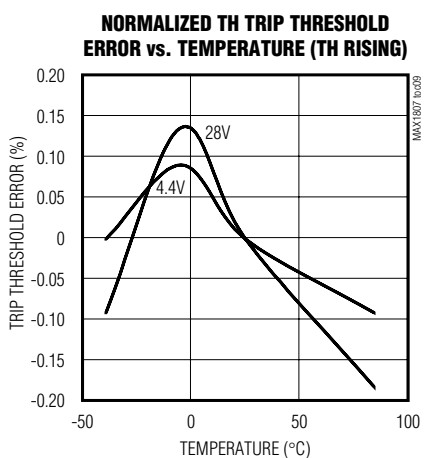
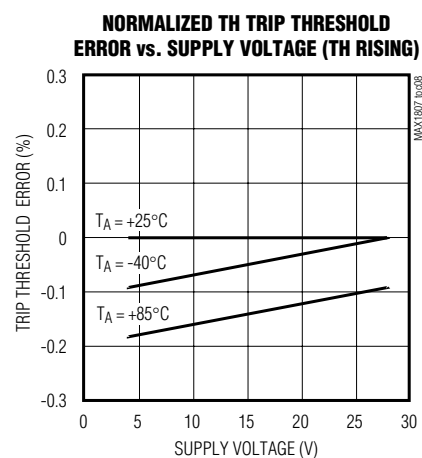
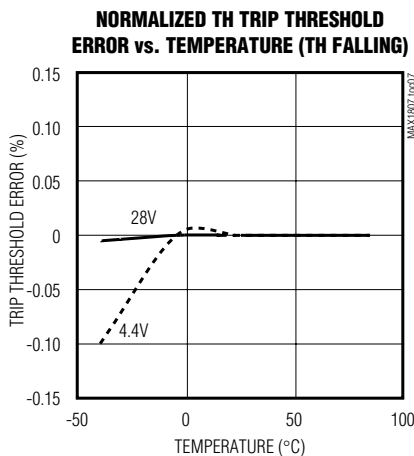
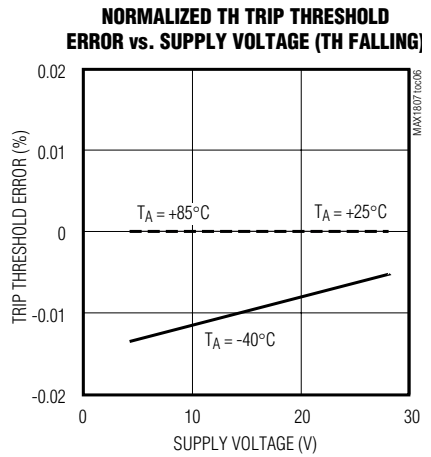
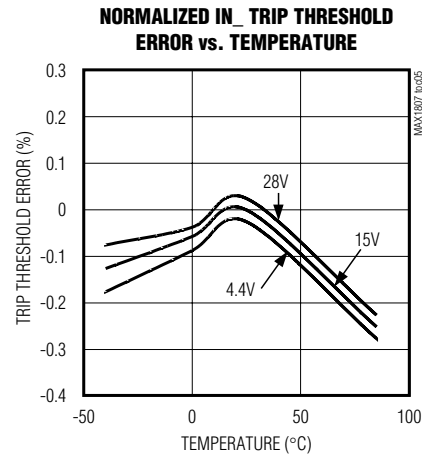
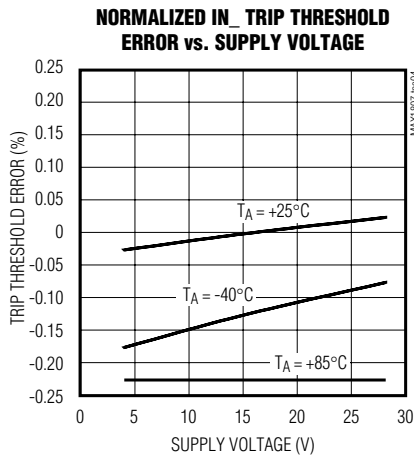
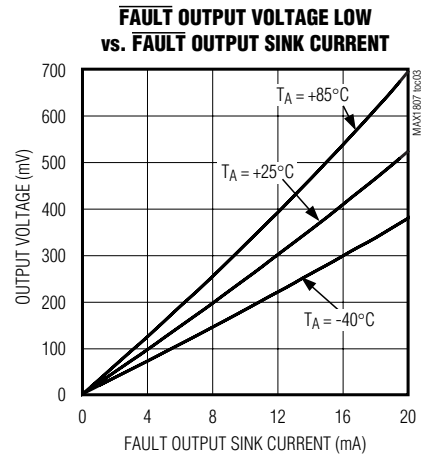
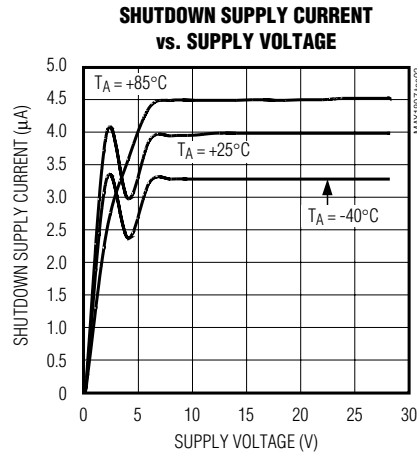
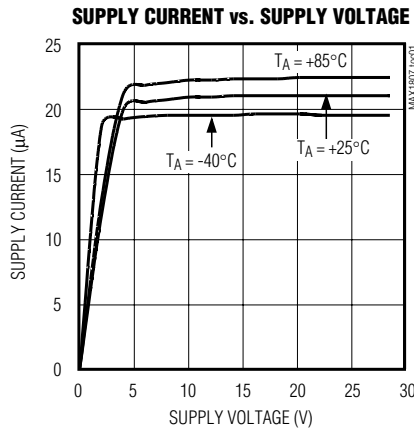
PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
GENERAL						
V_{DD} Input Voltage Range		DP and $\overline{FAULT}$ in correct state (Table 1)	2		28	V
V_{DD} Operating Voltage Range			4.4		28	V
V_{DD} Undervoltage Lockout Threshold		Rising trip level, typical 2% hysteresis; when V_{DD} is below this level, DP = Hi and $\overline{FAULT}$ = Hi-Z	2		4.0	V
Supply Current		$V_{TH} = 2V$ or $0.5V$			45	μA
Shutdown Current		$V_{DD} = 15V$, $V_{ON} = GND$, $V_{IN1}-V_{IN5} = V_{TH} = GND$			8.5	μA
COMPARATORS (IN1-IN5, TH)						
IN1-IN5 Input Trip Level		Rising edge, typical 1% hysteresis	0.95		1.05	V
TH Input Trip Level, Falling		$V_{DD} = 4.4V$ to $28V$ (MAX1808 only)	0.95		1.05	V
TH Input Trip Level, Rising		$V_{DD} = 4.4V$ to $28V$ (MAX1808 only)	1.045		1.155	V
IN1-IN5 Input Leakage Current		$V_{IN} = 1.5V$			50	nA
TH Input Leakage Current		$V_{TH} = 1.5V$ (MAX1808 only)			50	nA
ON Input High Logic Level		$V_{DD} = 4.4V$ to $28V$	1.8			V
ON Input Low Logic Level		$V_{DD} = 4.4V$ to $28V$			0.4	V
ON Input Leakage Current		$V_{ON} = 5V$			1.2	μA
		$V_{ON} = 28V$			10	
$\overline{FAULT}$ Output High Leakage Current		$V_{\overline{FAULT}} = 28V$ (MAX1807 only)			2	μA
$\overline{FAULT}$ Output Low Voltage		$I_{SINK} = 4mA$ (MAX1807 only)			0.4	V
DP Source Current (PMOS Turn-Off)		$V_{ON} = V_{DD}$, $V_{IN1} = 1.5V$	$V_{DP} = V_{DD} - 0.4V$	1		μA
			$V_{DP} = V_{DD} - 2V$	4		mA
DP Sink Current (PMOS Turn-On)		$V_{DP} = V_{DD} - 5V$	2			mA
DP Turn-On Clamp Voltage ($V_{DD} - V_{DP}$)		$V_{ON} = V_{DD}$, $I_{DPSINK} = 10\mu A$	$V_{DD} = 8.5V$ to $28V$	7.5		11.5
			$V_{DD} = 4.4V$	3.4		4.4

Note 1: Specifications to $-40^{\circ}C$ are guaranteed by design, not production tested.

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Typical Operating Characteristics

(Typical Operating Circuit, $V_{DD} = 15V$, $T_A = +25^\circ C$, unless otherwise noted.)

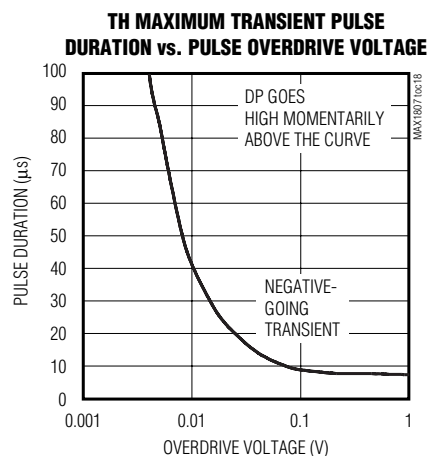
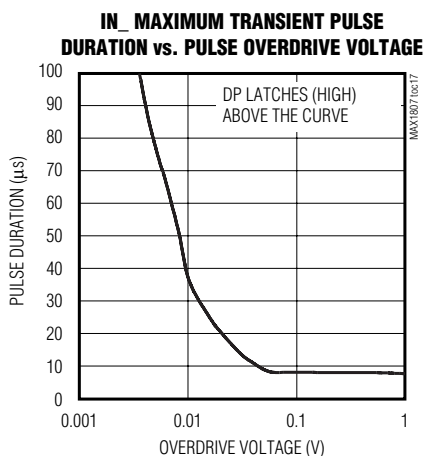
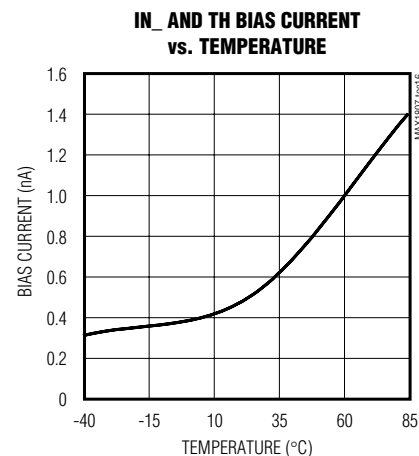
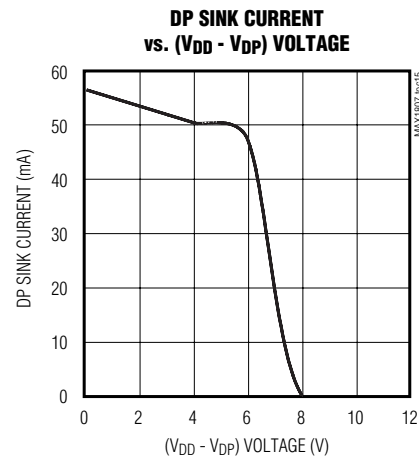
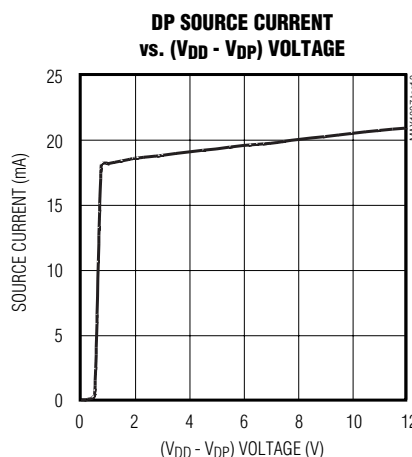
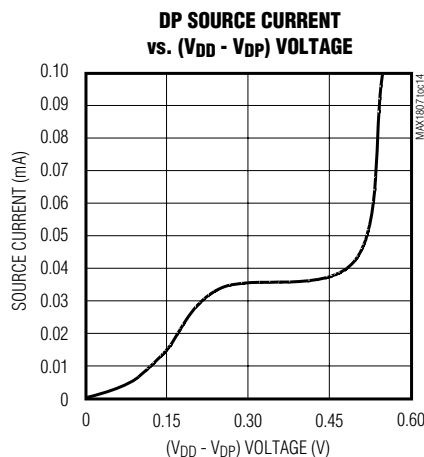
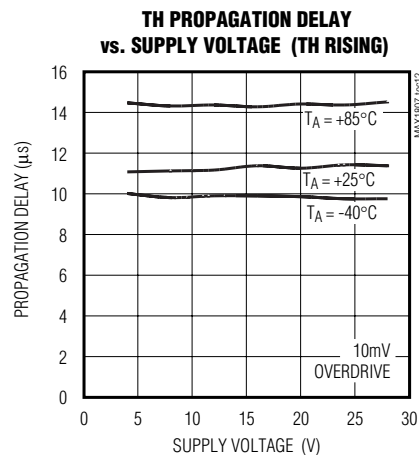
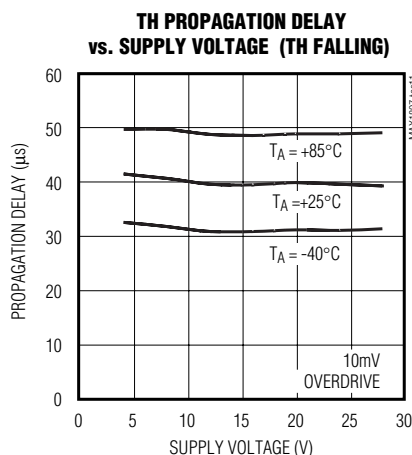
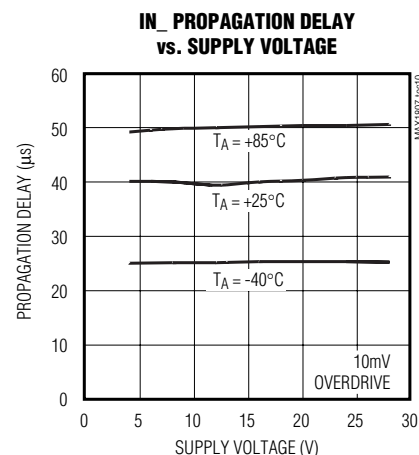


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Typical Operating Characteristics (continued)

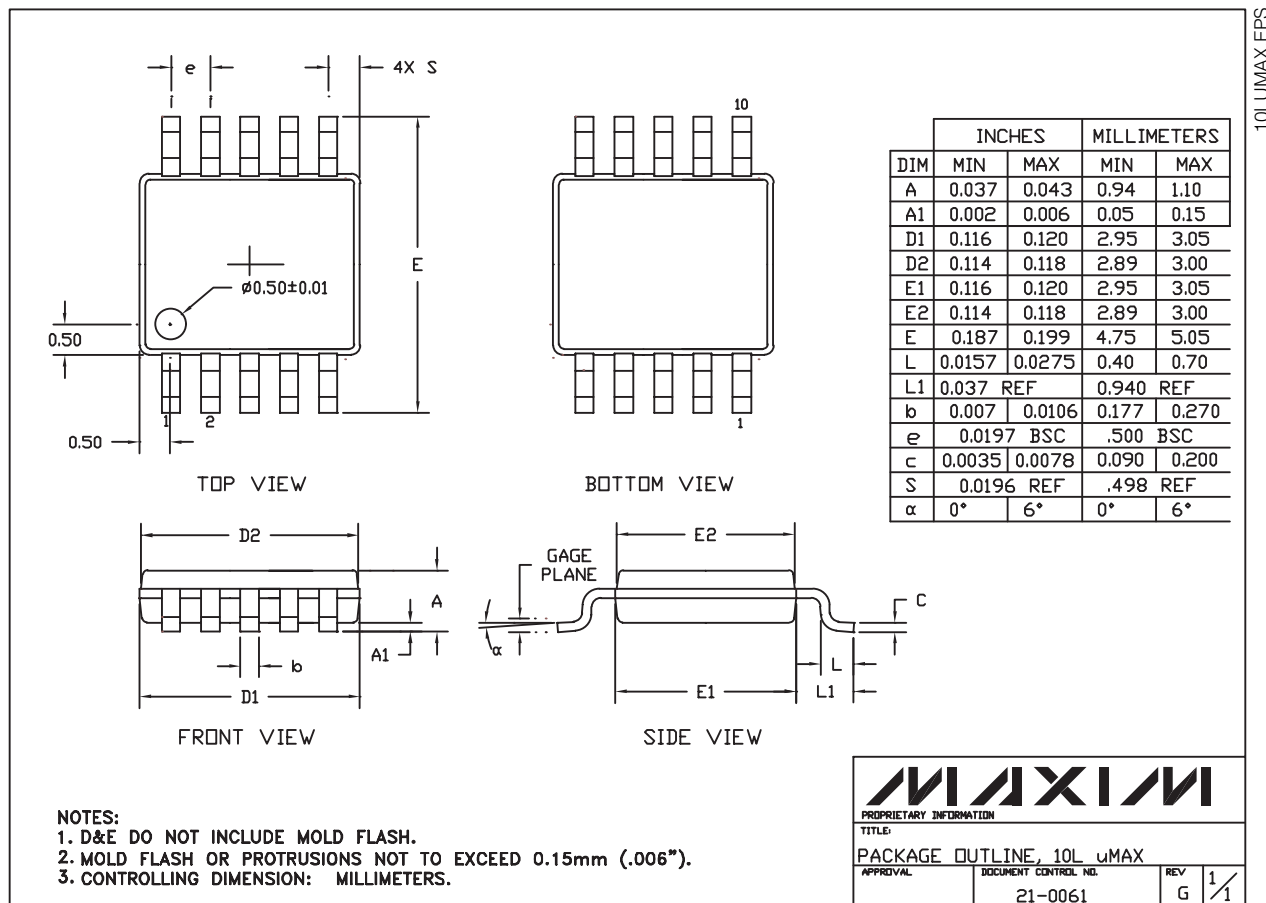
(Typical Operating Circuit, $V_{DD} = 15V$, $T_A = +25^\circ C$, unless otherwise noted.)

MAX1807/MAX1808



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Package Information



Maxim cannot assume responsibility for use of any circuitry other than circuitry entirely embodied in a Maxim product. No circuit patent licenses are implied. Maxim reserves the right to change the circuitry and specifications without notice at any time.

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