MAXIM

High-Current, 25Ω, SPDT, CMOS Analog Switches

MAX4659/MAX4660

General Description

The MAX4659/MAX4660 are medium voltage CMOS analog switches with a low on-resistance of 25Ω max specifically designed to handle large switch currents. With a switch capability of up to 200mA peak current and 150mA continuous current (MAX4660), and up to 150mA peak current and 75mA continuous current (MAX4659), these parts can switch loads as low as 50Ω. They can replace reed relays with a million times the speed and a virtually unlimited number of lifetime cycles. Normal power consumption is only 3mW, whether the switch is on or off. These parts are TTL/CMOS compatible and will switch any voltage within their power-supply range.

The devices are single-pole/double-throw (SPDT) switches. The MAX4659/MAX4660 contain one normally closed (NC) switch and one normally open (NO) switch.

The MAX4659/MAX4660s' power-supply range is from ±4.5V to ±20V for dual-supply operation and +9V to +40V for single-supply operation. These switches can operate from any combination of supplies, within a 40V V+ to V- range. They conduct equally well in either direction and can handle Rail-to-Rail® analog signals. The off-leakage current is only 1nA max at T_A = +25°C. The MAX4659 is available in 8-pin µMAX and SO packages. The MAX4660 is available in thermally enhanced exposed paddle µMAX and SO packages.

Applications

Relay Replacement

Test Equipment

Communication Systems

xDSL Modems

PBX, PABX Systems

Audio Signal Routing

Audio Systems

PC Multimedia Boards

Redundant/Backup Systems

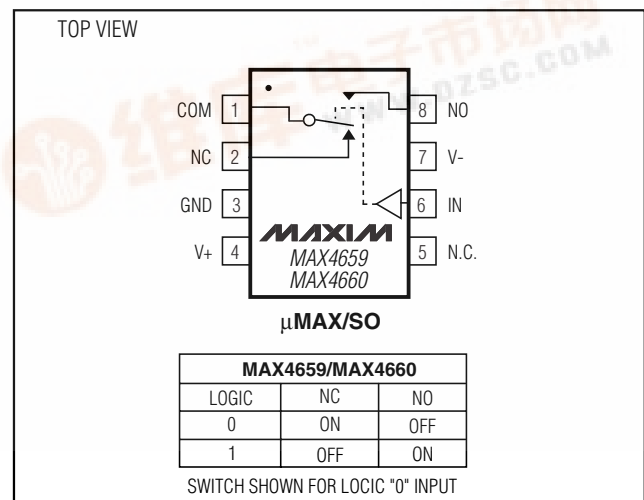*Rail-to-Rail is a registered trademark of Nippon Motorola, Ltd.*

Features

♦ **High Continuous Current Handling**
   150mA Continuous Current (MAX4660)
   75mA Continuous Current (MAX4659)

♦ **High Peak Current Handling**
   200mA Peak Current (MAX4660)
   150mA Peak Current (MAX4659)

♦ **25Ω max On-Resistance (±15V Supplies)**

♦ **V_L Supply Not Required**

♦ **1.5Ω max R_{ON} Flatness (±15V Supplies)**

♦ **Rail-to-Rail Signal Handling**

♦ **+12V Single Supply or ±15V Dual-Supply Operation**

♦ **Pin Compatible with DG419, MAX319**

Ordering Information

PART	TEMP. RANGE	PIN-PACKAGE
MAX4659EUA	-40°C to +85°C	8 µMAX
MAX4659ESA	-40°C to +85°C	8 SO
MAX4660EUA	-40°C to +85°C	8 µMAX EP*
MAX4660ESA	-40°C to +85°C	8 SO EP*

* Exposed paddle

Pin Configuration

TOP VIEW

COM	1			8	NO
NC	2			7	V-
GND	3	*MAXIM*		6	IN
V+	4	*MAX4659 MAX4660*		5	N.C.

µMAX/SO

MAX4659/MAX4660		
LOGIC	NC	NO
0	ON	OFF
1	OFF	ON
SWITCH SHOWN FOR LOGIC "0" INPUT		

High-Current, 25Ω, SPDT, CMOS Analog Switches

ABSOLUTE MAXIMUM RATINGS

V+ to GND ....-0.3V to +44V
V- to GND ....-44V to +0.3V
V+ to V-..-0.3V to +44V
All Other Pins to GND (Note 1) .... (V- - 0.3V) to (V+ + 0.3V)
Continuous Current COM, NO, NC (MAX4660).............±150mA
Continuous Current COM, NO, NC (MAX4659)...............±75mA
Continuous Current IN...±30mA
Peak Current COM, NO, NC
   MAX4660 (pulsed at 1ms, 10% duty cycle) ....±200mA
   MAX4659 (pulsed at 1ms, 10% duty cycle) ....±150mA
Continuous Power Dissipation (T$_A$ = +70°C)
   8-Pin μMAX-EP (derate 10.3mW/°C above +70°C)
   MAX4660..825mW

8-Pin μMAX (derate 4.50mW/°C above +70°C)
   MAX4659...362mW
8-Pin SO-EP (derate 18.9mW/°C above +70°C)
   MAX4660...1509mW
8-Pin SO (derate 5.88mW/°C above +70°C)
   MAX4659...471mW
Operating Temperature Ranges
   MAX4659/MAX4660.......................................-40°C to +85°C
Junction Temperature...+150°C
Storage Temperature Range ....-65°C to +150°C
Lead Temperature (soldering, 10s) ....+300°C

Note 1: Signals on NO, NC, COM, or IN exceeding V+ or V- are clamped by internal diodes. Limit forward-diode current to maximum current rating.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

ELECTRICAL CHARACTERISTICS—Dual Supplies

(V+ = +15V, V- = -15V, V$_{IH}$ = 2.4V, V$_{IL}$ = 0.8V, T$_A$ = T$_{MIN}$ to T$_{MAX}$, unless otherwise noted. Typical values are at T$_A$ = +25°C.) (Notes 2, 6)

PARAMETER	SYMBOL	CONDITIONS	T$_A$	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V$_{COM}$, V$_{NO}$, V$_{NC}$			V-		V+	V
On-Resistance	R$_{ON}$	I$_{COM}$ = 50mA; V$_{NO}$ or V$_{NC}$ = ±10V	+25°C		18	25	Ω
			T$_{MIN}$ to T$_{MAX}$			30	
On-Resistance Matching Between Channels	ΔR$_{ON}$	I$_{COM}$ = 50mA; V$_{NO}$ or V$_{NC}$ = ±10V	+25°C		0.4	1.2	Ω
			T$_{MIN}$ to T$_{MAX}$			1.5	
On-Resistance Flatness (Note 3)	R$_{FLAT (ON)}$	I$_{COM}$ = 50mA; V$_{NO}$ or V$_{NC}$ = -5V, 0, +5V	+25°C		0.5	1.5	Ω
			T$_{MIN}$ to T$_{MAX}$			2	
NO or NC Off-Leakage Current (Note 4)	I$_{NO(OFF)}$ or I$_{NC(OFF)}$	V$_{COM}$ = -14.5V, +14.5V; V$_{NO}$ or V$_{NC}$ = +14.5V, -14.5V	+25°C	-1	0.01	1	nA
			T$_{MIN}$ to T$_{MAX}$	-10		10	
COM On-Leakage Current (Note 4)	I$_{COM(ON)}$	V$_{COM}$ = +14.5V, -14.5V; V$_{NO}$ or V$_{NC}$ = +14.5V, -14.5V, or floating	+25°C	-2	0.02	2	nA
			T$_{MIN}$ to T$_{MAX}$	-20		20	
DYNAMIC CHARACTERISTICS							
Transition Time	t$_{TRANS}$	V$_{NO}$ or V$_{NC}$ = 10V; R$_L$ = 300Ω, C$_L$ = 35pF; Figure 3	+25°C		85	150	ns
			T$_{MIN}$ to T$_{MAX}$			200	
Break-Before-Make Delay	t$_{BBM}$	V$_{NO}$ or V$_{NC}$ = 10V; R$_L$ = 300Ω, C$_L$ = 35pF, Figure 3	+25°C	10	20		ns
			T$_{MIN}$ to T$_{MAX}$	5			

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ELECTRICAL CHARACTERISTICS—Dual Supplies (continued)

(V+ = +15V, V- = -15V, V_{IH} = 2.4V, V_{IL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 6)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
Charge Injection	Q	V_{GEN} = 0, R_{GEN} = 0, C_L = 1nF, Figure 4	+25°C		1.5		pC
-3dB Bandwidth	BW		+25°C		225		MHz
Off-Isolation (Note 5)	V_{ISO}	f = 1MHz, R_L = 50Ω, Figure 5	+25°C		-70		dB
Total Harmonic Distortion	THD	f = 20Hz to 20kHz, $V_{N\_}$ = 5Vp-p, R_L = 600Ω	+25°C		0.005		%
Crosstalk	V_{CROSS}	R_L = 50Ω, C_L = 5pF, f = 1MHz, Figure 6	+25°C		-76		dB
NO or NC Off-Capacitance	$C_{NO(OFF)}$, $C_{NC(OFF)}$	f = 1MHz, Figure 7	+25°C		6		pF
COM On-Capacitance	$C_{COM(ON)}$	f = 1MHz, Figure 8	+25°C		25		pF
DIGITAL I/O							
Input Logic High	V_{IH}		T_{MIN} to T_{MAX}	2.4			V
Input Logic Low	V_{IL}		T_{MIN} to T_{MAX}			0.8	V
Input Leakage Current	I_{IN}	V_{IN} = 0.8V or 2.4V	T_{MIN} to T_{MAX}	-1		1	µA
POWER SUPPLY							
Power-Supply Range			T_{MIN} to T_{MAX}	±4.5		±20	V
Positive Supply Current	I+	V_{IN} = 0 or 5V, $V_{N\_}$ = 3V, I_{SWITCH} = 100mA, MAX4660;	+25°C		135	200	µA
		I_{SWITCH} = 50mA, MAX4659	T_{MIN} to T_{MAX}			300	
Negative Supply Current	I-	V_{IN} = 0 or 5V, $V_{N\_}$ = 3V, I_{SWITCH} = 100mA, MAX4660;	+25°C		30	50	µA
		I_{SWITCH} = 50mA, MAX4659	T_{MIN} to T_{MAX}			75	
Ground Current	I_{GND}	V_{IN} = 0 or 5V, $V_{N\_}$ = 3V, I_{SWITCH} = 100mA, MAX4660;	+25°C		100	175	µA
		I_{SWITCH} = 50mA, MAX4659	T_{MIN} to T_{MAX}			225	

MAX4659/MAX4660

ELECTRICAL CHARACTERISTICS—Single Supply

(V+ = +12V, V- = 0, V_{IH} = 2.4V, V_{IL} = 0.8V, T_A = T_{MIN} to T_{MAX}, unless otherwise noted. Typical values are at T_A = +25°C.) (Notes 2, 6)

PARAMETER	SYMBOL	CONDITIONS	T_A	MIN	TYP	MAX	UNITS
ANALOG SWITCH							
Analog Signal Range	V_{IN}		T_{MIN} to T_{MAX}	0		V+	V
On-Resistance	R_{ON}	I_{COM} = 25mA; V_{NO} or V_{NC} = +10V	+25°C		38	50	Ω
			T_{MIN} to T_{MAX}			60	
On-Resistance Matching Between Channels	ΔR_{ON}	I_{COM} = 25mA; V_{NO} or V_{NC} = ±10V	+25°C		0.4	2	Ω
			T_{MIN} to T_{MAX}			2.5	
On-Resistance Flatness (Note 3)	$R_{FLAT (ON)}$	I_{COM} = 25mA; V_{NO} or V_{NC} = +2V, +6V, +10V	+25°C		4	7	Ω
			T_{MIN} to T_{MAX}			9	
DYNAMIC CHARACTERISTICS							
Transition Time	t_{TRANS}	V_{NO} or V_{NC} = 10V; R_L = 300Ω; C_L = 35pF, Figure 2	+25°C		120	200	ns
			T_{MIN} to T_{MAX}			250	
Break-Before-Make Delay	t_{BBM}	V_{NO} or V_{NC} = 10V; R_L = 300 Ω; C_L = 35pF, Figure 2	+25°C	20	50		ns
			T_{MIN} to T_{MAX}	10			
Charge Injection	Q	V_{GEN} = 0, R_{GEN} = 0, C_L = 1nF, Figure 4	+25°C		1		pC
POWER SUPPLY							
Power-Supply Range	V+			+9		+40	V
Positive Supply Current	I+	V_{IN} = 0 or 12V, $V_{N\_}$ = 3V; I_{SWITCH} = 50mA, MAX4660; I_{SWITCH} = 25mA, MAX4659	+25°C		50	100	µA
			T_{MIN} to T_{MAX}			125	
		V_{IN} = 5V, $V_{N\_}$ = 3V; I_{SWITCH} = 50mA, MAX4660; I_{SWITCH} = 25mA, MAX4659	+25°C		70	125	
			T_{MIN} to T_{MAX}			150	

Note 2: The algebraic convention is used in this data sheet; the most negative value is shown in the minimum column.

Note 3: Flatness is defined as the difference between the maximum and minimum value of on-resistance as measured over the specified analog signal range.

Note 4: Leakage parameters are 100% tested at maximum-rated hot temperature and guaranteed by correlation at T_A = +25°C.

Note 5: Off-isolation = $20\log_{10}$ [V_{COM} / (V_{NC} or V_{NO})], V_{COM} = output, V_{NC} or V_{NO} = input to off switch.

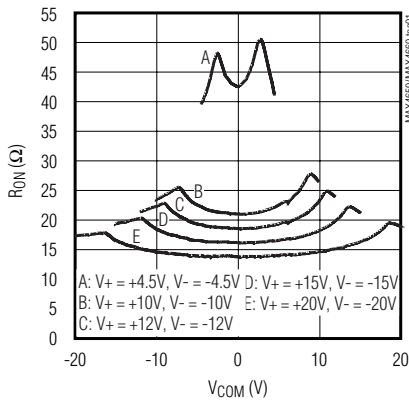**Note 6:** -40°C specifications are guaranteed by design.

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MAX4659/MAX4660

Typical Operating Characteristics

(T_A = +25°C, unless otherwise noted.)
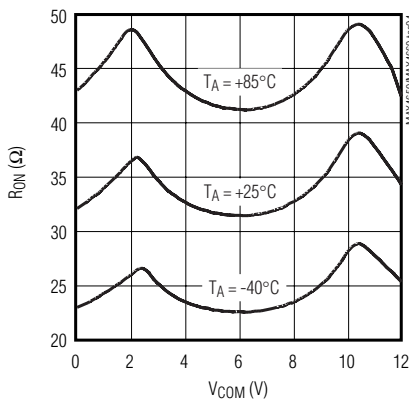

ON-RESISTANCE vs. V_COM (DUAL SUPPLIES)

A: V+ = +4.5V, V- = -4.5V D: V+ = +15V, V- = -15V
B: V+ = +10V, V- = -10V E: V+ = +20V, V- = -20V
C: V+ = +12V, V- = -12V

ON-RESISTANCE vs. V_COM (SINGLE SUPPLY)

A: V+ = +9V, V- = 0
B: V+ = +12V, V- = 0
C: V+ = +24V, V- = 0
D: V+ = +36V, V- = 0
E: V+ = +40V, V- = 0
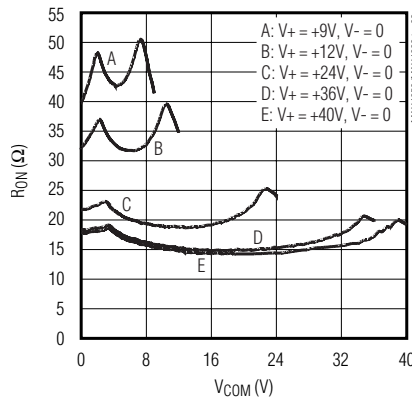
ON-RESISTANCE vs. V_COM AND TEMPERATURE (DUAL SUPPLIES)
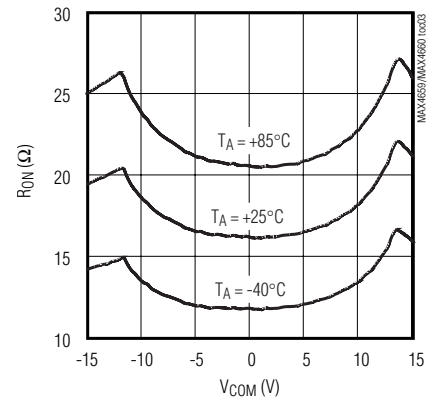
ON-RESISTANCE vs. V_COM AND TEMPERATURE (SINGLE SUPPLY)
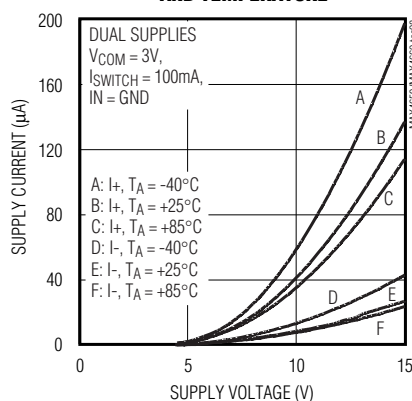

LEAKAGE CURRENT vs. TEMPERATURE
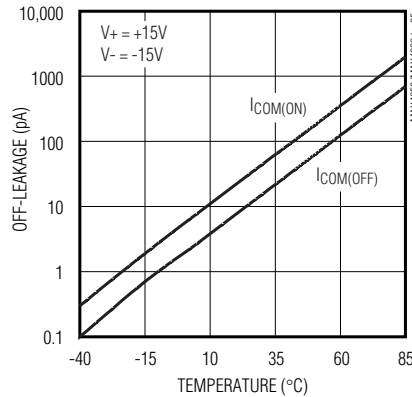
CHARGE INJECTION vs. V_COM

A: V+ = 15V, V- = -15V
B: V+ = 12V, V- = 0
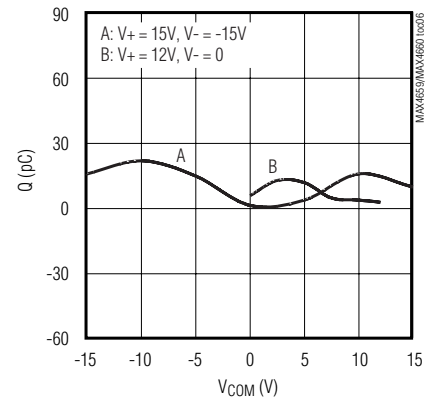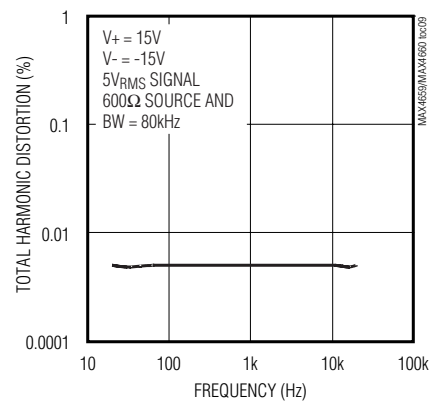

SUPPLY CURRENT vs. SUPPLY VOLTAGE (SINGLE SUPPLY)
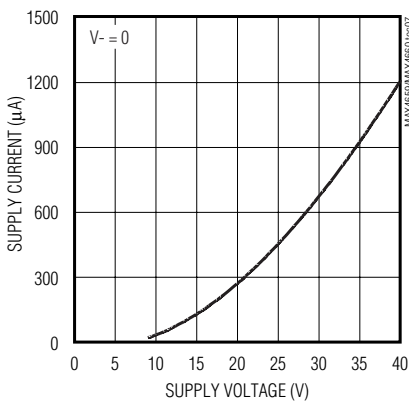
SUPPLY CURRENT vs. SUPPLY VOLTAGE AND TEMPERATURE

DUAL SUPPLIES
V_COM = 3V,
I_SWITCH = 100mA,
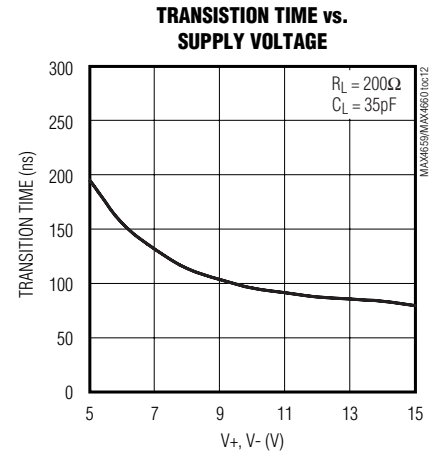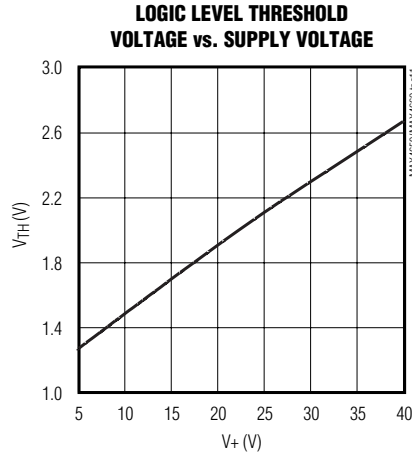IN = GND

A: I+, T_A = -40°C
B: I+, T_A = +25°C
C: I+, T_A = +85°C
D: I-, T_A = -40°C
E: I-, T_A = +25°C
F: I-, T_A = +85°C

TOTAL HARMONIC DISTORTION vs. FREQUENCY

V+ = 15V
V- = -15V
5V_RMS SIGNAL
600Ω SOURCE AND
BW = 80kHz

MAX4659/MAX4660

_____*Typical Operating Characteristics (continued)*

(T_A = +25°C, unless otherwise noted.)

FREQUENCY RESPONSE

LOGIC LEVEL THRESHOLD
VOLTAGE vs. SUPPLY VOLTAGE

TRANSITION TIME vs.
SUPPLY VOLTAGE

BREAK-BEFORE-MAKE DELAY vs.
SUPPLY VOLTAGE

SUPPLY CURRENT vs.
VOLTAGE ACROSS SWITCH
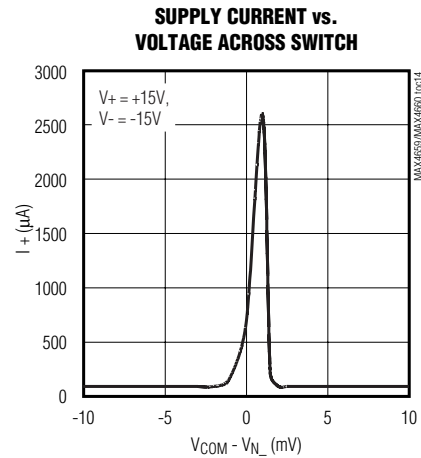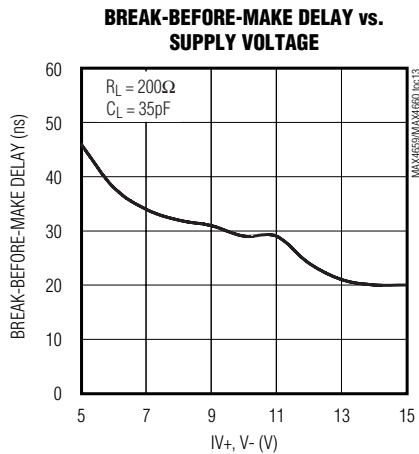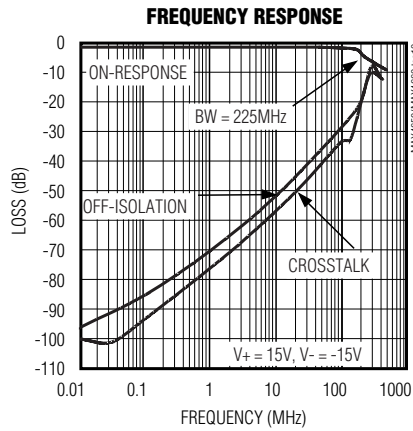
_____*Pin Description*

PIN	NAME	FUNCTION
1	COM	Analog Switch Common
2	NC	Normally Closed Switch Terminal. NC is connected to COM when IN is low.
3	GND	Ground
4	V+	Positive Supply Voltage Input
5	N.C.	No Connection
6	IN	Digital Control Input
7	V-	Negative Supply Voltage Input
8	NO	Normally Open Switch Terminal. NO is connected to COM when IN is high.

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Detailed Description

The MAX4659/MAX4660 are single, single-pole/double-throw (SPDT) CMOS analog switches. The CMOS switch construction provides rail-to-rail signal handling while consuming very little power. The switch is controlled by a TTL/CMOS level compatible digital input. The MAX4659/MAX4660 have a normally open switch and a normally closed switch.

These devices can be operated with either single power supplies or dual power supplies. Operation at up to ±20V supplies allows users a wide switching dynamic range. Additionally, asymmetrical operation is possible to tailor performance to a particular application.

These switches have been specifically designed to handle high switch currents, up to 200mA peak current and 150mA continuous currents. In order to do this, a new technique is used to drive the body of the output N-channel device. (Note: The basic switch between the input, NC/NO terminal and the output common terminal consists of an N-channel MOSFET and a P-channel MOSFET in parallel.) The standard method limits operation to approximately a 600mV drop across the switch. More than 600mV causes an increase in Id_{ON} leakage current (due to the turn-on of on-chip parasitic diodes), and an increase in V+ supply current. With this new sensing method, there is no limitation to the voltage drop across the switch. Current and voltage are limited only by the power dissipation rating of the package and the absolute maximum ratings of the switch.

When the analog input voltage drop is approximately 7mV there is an increase in power supply current from 90µA to 2mA (typ) within a 1mV to 7mV range, caused by the new sensing/driving circuitry.

Applications Information

Overvoltage Protection

Proper power-supply sequencing is recommended for all CMOS devices. Do not exceed the absolute maximum ratings, because stresses beyond the listed ratings can cause permanent damage to the devices. First, connect GND, followed by V+, V-, and the remaining pins. If power-supply sequencing is not possible, add two small-signal diodes (D1, D2) in series with supply pins (Figure 1). Adding diodes reduces the analog signal range to one diode drop below V+ and one diode drop above V-, but does not affect the devices' low switch resistance and low leakage characteristics. Device operation is unchanged, and the difference between V+ and V- should not exceed 44V. The protection diode for the negative supply is not required when V- is connected to GND.

Off-Isolation at High Frequencies

In 50Ω systems, the high-frequency on-response of these parts extends from DC to above 100MHz, with a typical loss of -2dB. When the switch is turned off, however, it behaves like a capacitor and off-isolation decreases with increasing frequency. This effect is more pronounced with higher source and load impedances. Above 5MHz, circuit board layout becomes critical. The graphs shown in the *Typical Operating Characteristics* were taken using a 50Ω source and load connected with BNC connectors.

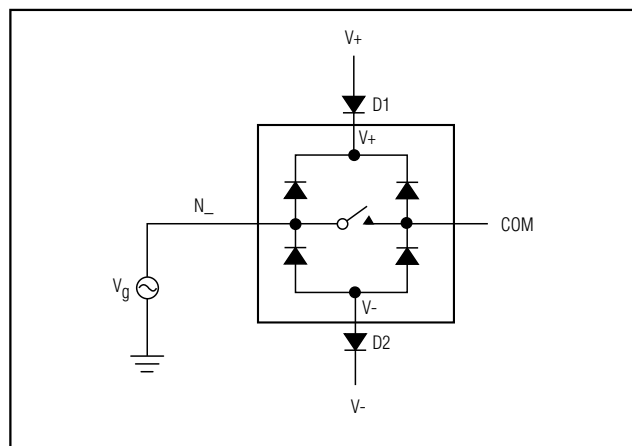

Figure 1. Overvoltage Protection Using Blocking Diodes

High-Current, 25Ω, SPDT, CMOS Analog Switches

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Figure 2. Functional Diagram
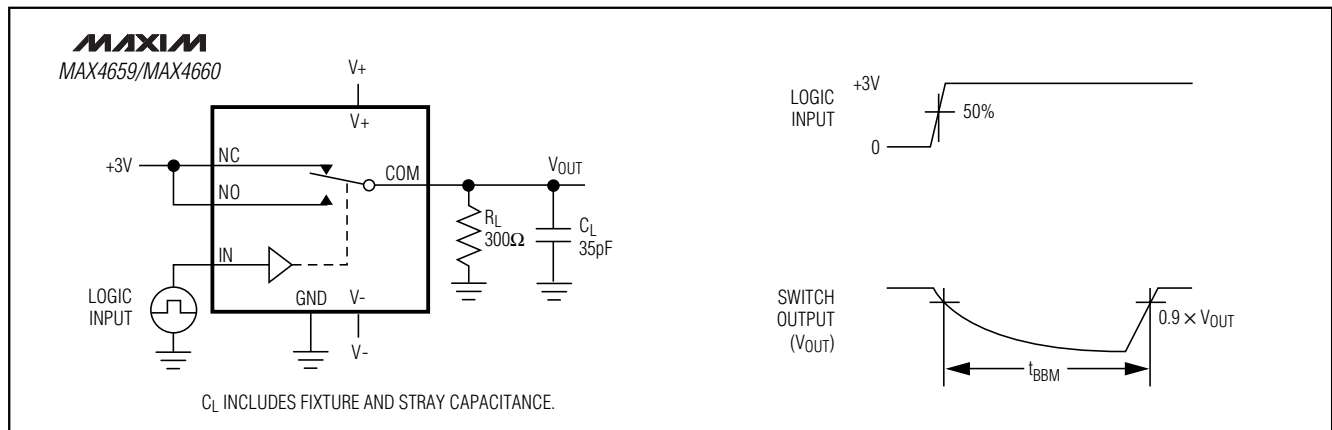
Figure 3. Break-Before-Make Time
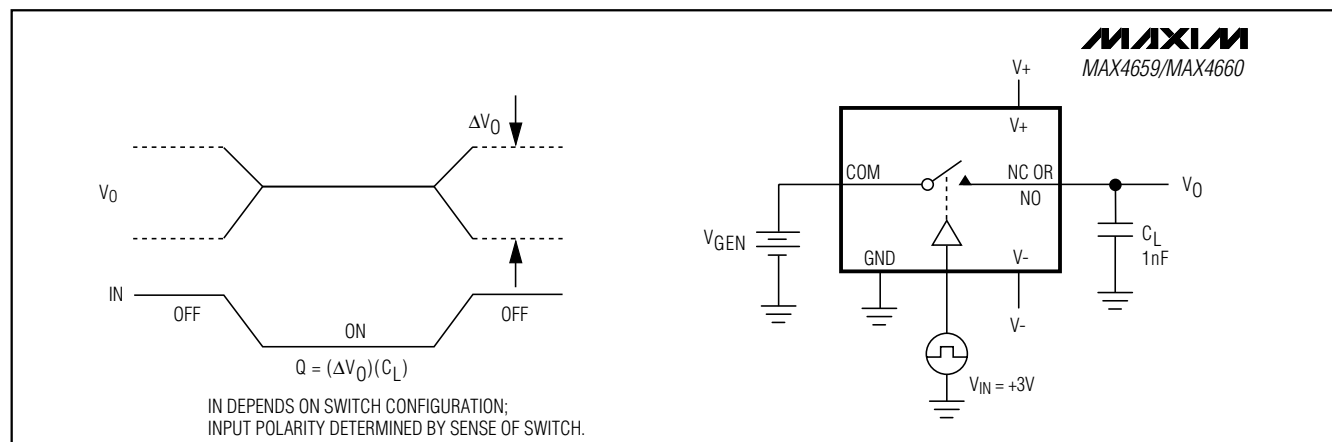
Figure 4. Charge Injection

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Test Circuits/Timing Diagrams (continued)
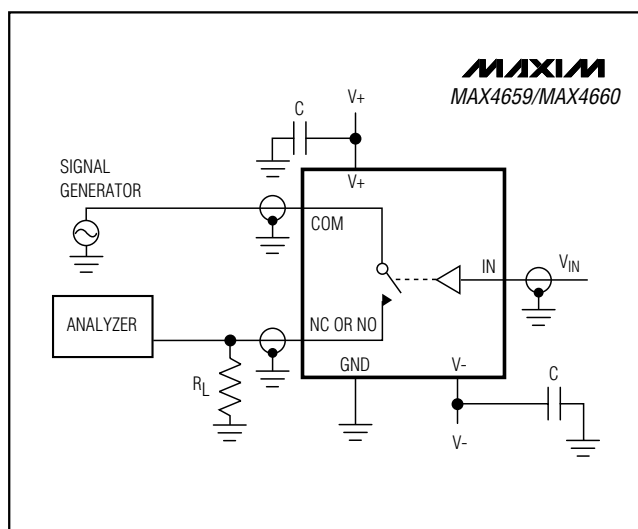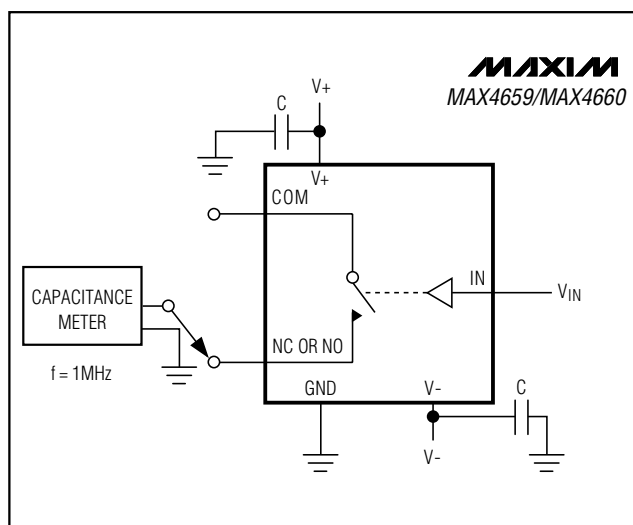

Figure 5. Off-Isolation

Figure 6. Crosstalk

Figure 7. Channel Off-Capacitance
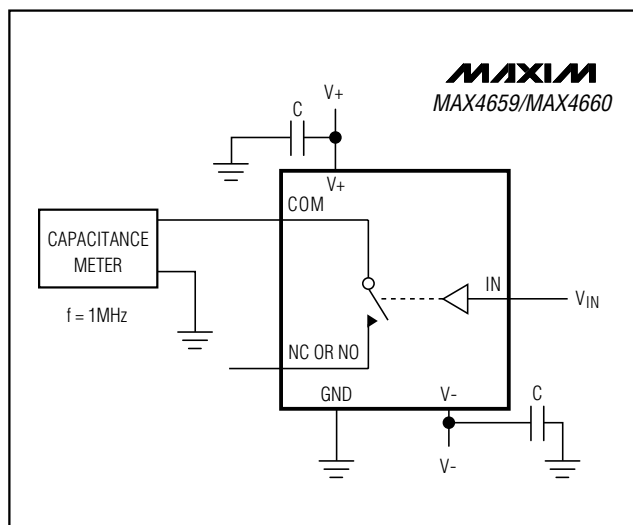
Figure 8. Channel On-Capacitance

Chip Information

TRANSISTOR COUNT: 45
PROCESS: CMOS

MAX4659/MAX4660

High-Current, 25Ω, SPDT, CMOS Analog Switches

8L, μMAX, EXP PAD.EPS

	JEDEC							
	INCHES		MILLIMETERS		INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX	MIN	MAX	MIN	MAX
A	0.037	0.043	0.94	1.10	---	0.043	---	1.10
A1	0.002	0.006	0.05	0.15	0.002	0.006	0.05	0.15
B	0.010	0.014	0.25	0.36	0.010	0.016	0.25	0.40
C	0.005	0.007	0.13	0.18	0.005	0.009	0.13	0.23
D	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
e	0.0256 BSC		0.65 BSC		0.0256 BSC		0.64 BSC	
E	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
H	0.188	0.198	4.78	5.03	0.193 BSC		4.9 BSC	
L	0.016	0.026	0.41	0.66	0.016	0.027	0.40	0.70
α	0°	6°	0°	6°	0°	6°	0°	6°
✳X	0.087	0.099	2.210	2.515				
✳Y	0.062	0.074	1.575	1.880				

✳ EXPOSED PAD

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15MM (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187.
5. EXPOSED PAD FLUSH WITH BOTTOM OF PACKAGE WITHIN .002".

MAXIM
PROPRIETARY INFORMATION
TITLE:
PACKAGE OUTLINE, 8L μMAX, EXPOSED PAD

APPROVAL	DOCUMENT CONTROL NO.	REV	
	21-0107	A	1/1

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Package Information (continued)

SOICN.EPS

	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.053	0.069	1.35	1.75
A1	0.004	0.010	0.10	0.25
B	0.014	0.019	0.35	0.49
C	0.007	0.010	0.19	0.25
e	0.050		1.27	
E	0.150	0.157	3.80	4.00
H	0.228	0.244	5.80	6.20
h	0.010	0.020	0.25	0.50
L	0.016	0.050	0.40	1.27

	INCHES		MILLIMETERS			
	MIN	MAX	MIN	MAX	N	MS012
D	0.189	0.197	4.80	5.00	8	A
D	0.337	0.344	8.55	8.75	14	B
D	0.386	0.394	9.80	10.00	16	C

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH
2. MOLD FLASH OR PROTRUSIONS NOT
   TO EXCEED .15mm (.006")
3. LEADS TO BE COPLANAR WITHIN
   .102mm (.004")
4. CONTROLLING DIMENSION: MILLIMETER
5. MEETS JEDEC MS012-XX AS SHOWN
   IN ABOVE TABLE
6. N = NUMBER OF PINS

MAXIM
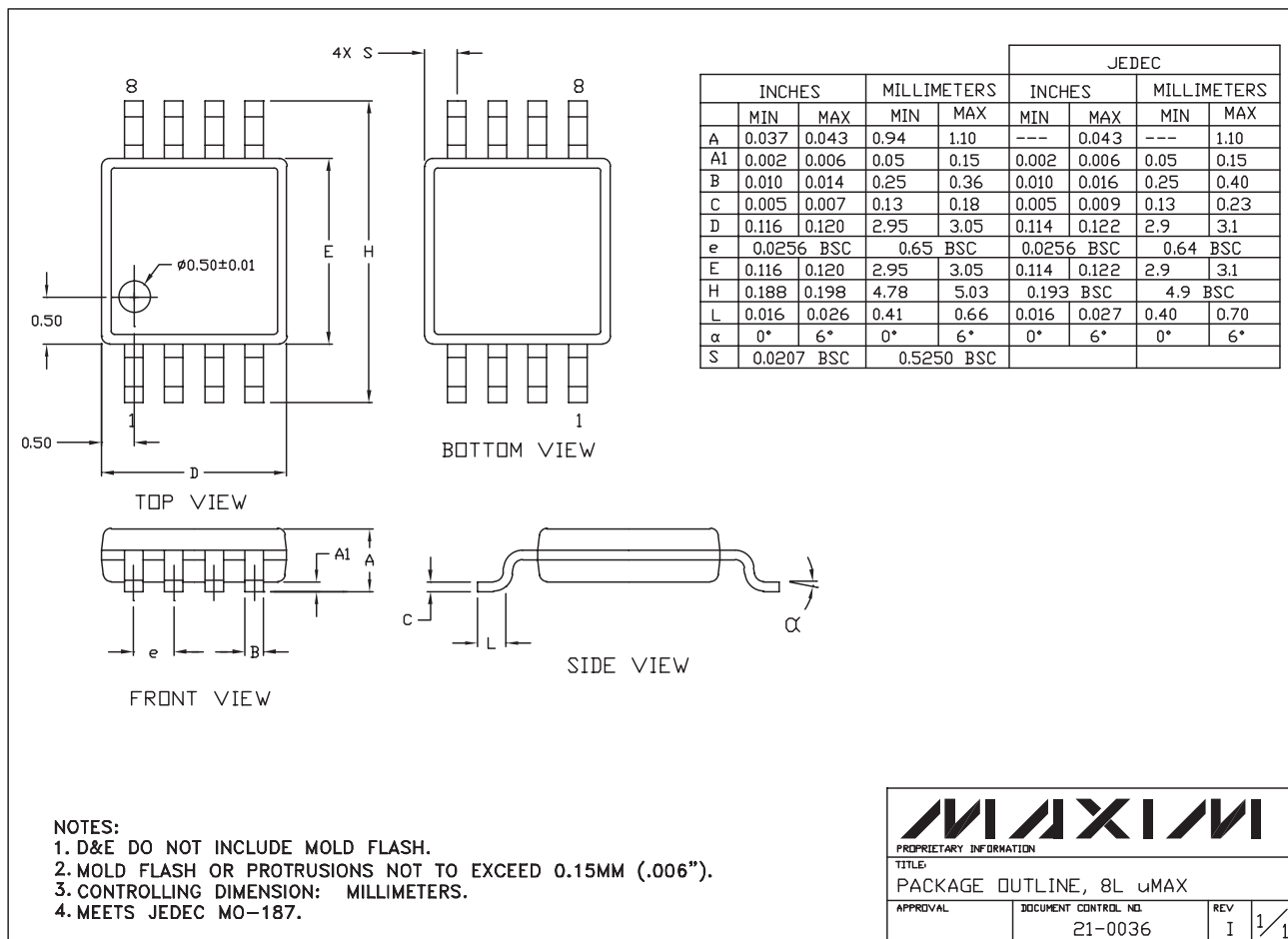120 SAN GABRIEL DR. SUNNYVALE CA 94086. FAX (408) 737-7194
PROPRIETARY INFORMATION

PACKAGE FAMILY OUTLINE: SOIC .150"
TITLE

1/1

21-0041 A
DOCUMENT CONTROL NUMBER   REV

MAXIM

High-Current, 25Ω, SPDT, CMOS Analog Switches

	INCHES MIN	INCHES MAX	MILLIMETERS MIN	MILLIMETERS MAX	JEDEC INCHES MIN	JEDEC INCHES MAX	JEDEC MILLIMETERS MIN	JEDEC MILLIMETERS MAX
A	0.037	0.043	0.94	1.10	---	0.043	---	1.10
A1	0.002	0.006	0.05	0.15	0.002	0.006	0.05	0.15
B	0.010	0.014	0.25	0.36	0.010	0.016	0.25	0.40
C	0.005	0.007	0.13	0.18	0.005	0.009	0.13	0.23
D	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
e	0.0256 BSC		0.65 BSC		0.0256 BSC		0.64 BSC	
E	0.116	0.120	2.95	3.05	0.114	0.122	2.9	3.1
H	0.188	0.198	4.78	5.03	0.193 BSC		4.9 BSC	
L	0.016	0.026	0.41	0.66	0.016	0.027	0.40	0.70
α	0°	6°	0°	6°	0°	6°	0°	6°
S	0.0207 BSC		0.5250 BSC					

NOTES:
1. D&E DO NOT INCLUDE MOLD FLASH.
2. MOLD FLASH OR PROTRUSIONS NOT TO EXCEED 0.15MM (.006").
3. CONTROLLING DIMENSION: MILLIMETERS.
4. MEETS JEDEC MO-187.

MAXIM

PROPRIETARY INFORMATION

TITLE:
PACKAGE OUTLINE, 8L uMAX

APPROVAL	DOCUMENT CONTROL NO.	REV	
	21-0036	I	1/1

TOP VIEW · BOTTOM VIEW · FRONT VIEW · SIDE VIEW

Ø0.50±0.01