



UML1

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

The ASI UML1 is Designed for

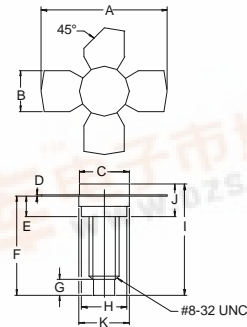
FEATURES:

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- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	2.0 A
V_{CB0}	45 V
V_{CEO}	25 V
V_{EBO}	3.5 V
P_{DISS}	31.8 W @ T _C = 25 °C
T_J	-65 °C to +200 °C
T_{STG}	-65 °C to +150 °C
θ_{JC}	20 °C/W

PACKAGE STYLE .280 4L STUD



DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	1.010 / 25.65	1.055 / 26.80
B	.220 / 5.59	.230 / 5.84
C	.270 / 6.86	.285 / 7.24
D	.003 / 0.08	.007 / 0.18
E	.117 / 2.97	.137 / 3.48
F	.572 / 14.53	
G	.130 / 3.30	
H	.245 / 6.22	.255 / 6.48
I	.640 / 16.26	
J	.175 / 4.45	.217 / 5.51
K	.275 / 6.99	.285 / 7.24

ORDER CODE: ASI10688

CHARACTERISTICS T_C = 25 °C

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CB0}	I _C = 1.0 mA	45			V
BV_{CEO}	I _C = 20 mA	24			V
BV_{EBO}	I _E = 0.25 mA	3.5			V
I_{CB0}	V _{CB} = 28 V			0.45	mA
h_{FE}	V _{CE} = 5.0 V I _C = 100 mA	15		120	---
C_{ob}	V _{CB} = 28 V f = 1.0 MHz			5.0	pF
P_G η_D	V _{CC} = 28 V P _{OUT} = 1.0 W f = 400 MHz	13	60		dB %

