

BB110TB, BB110B AND BB2110DI DDR SDRAM TERMINATOR NETWORKS.

DESCRIPTION

This "BB" family of networks is for terminating SSTL_2 Class I and Class II systems, such as DDR SDRAM. These specialty networks employ solder balls for surface mount flip chip attachment. Their unique construction yields extremely low capacitance and inductance parasitics making them ideal for high-speed bus speeds.



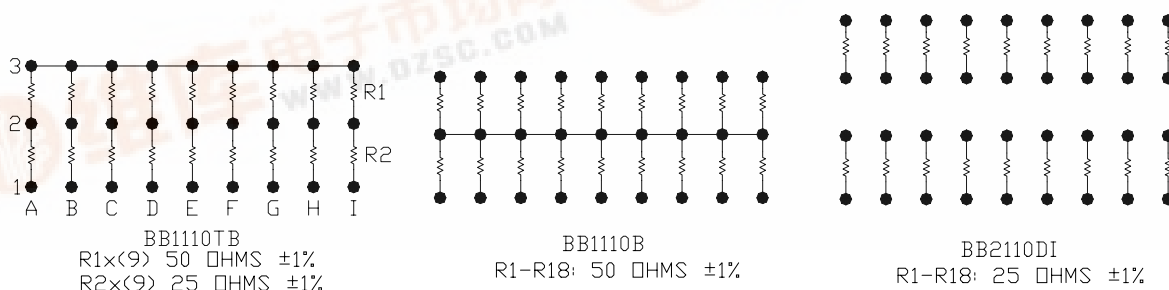
FEATURES

- 18 Bit SSTL_2 Termination Sets.
- Compliant to JEDEC Std. 8-9B.
- Superior High Frequency Performance.
- Minimal stray capacitance and inductance are achieved by placing resistors and BGA termination on the same side of ceramic substrate.
- The device can be easily surface mounted using automatic Pick and Place equipment.

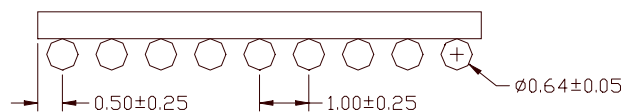
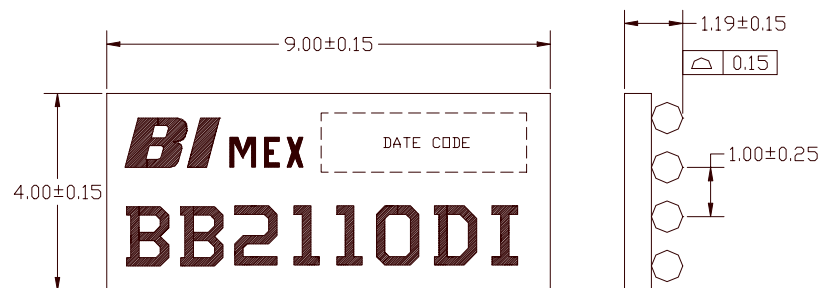
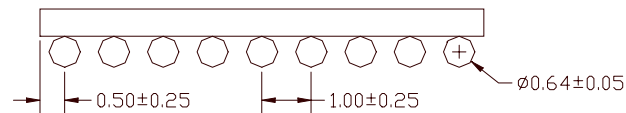
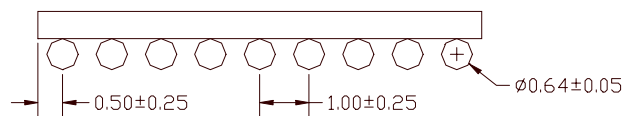
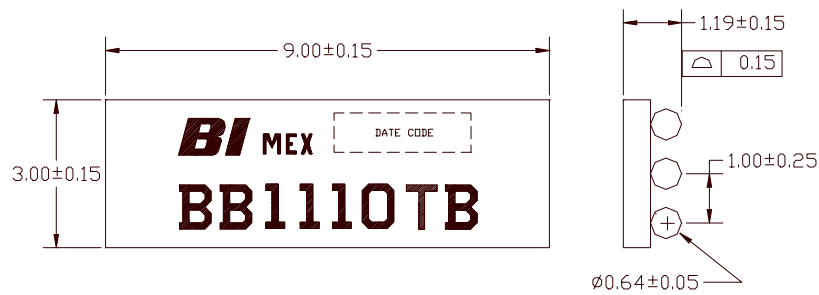
ELECTRICAL

PARAMETER (Maximum)	LIMIT	UNITS
Resistance, Nominal	Various	Ω
Absolute Tolerance	1	%
Temperature Coefficient of Resistance	200	ppm/ $^{\circ}\text{C}$
Interlead Capacitance, Maximum	0.1	pF
Operation Temperature	-55 to 125	$^{\circ}\text{C}$
Power Rating (per package)	1	Watt

Schematic



Dimensions (mm)



ORDERING INFORMATION

Part Number	Resistor Number	Resistance Value (Ω)	Array	Pitch (mm)
BB1110TB	R1/R2	50/25	3X9	1.0
BB1110B	R1	50	3X9	1.0
BB2110DI	R1	25	4X9	1.0

Tape/Reel

Reel Sizes	Part count
7 inch	1000
13 inch	4000

Add 7 for 7-inch reel or 13 for 13-inch reel to part number.

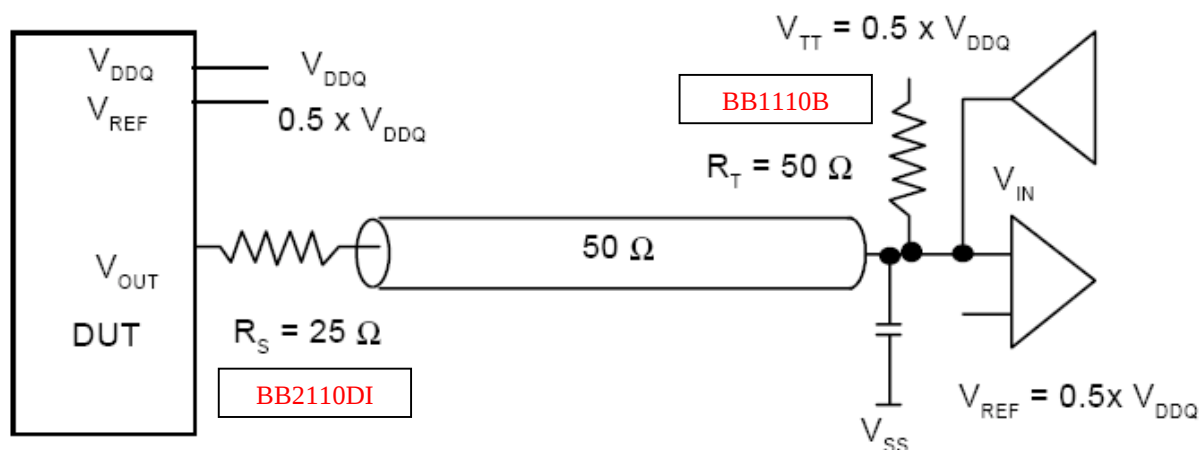
Example:

BB1110B7 (1000 parts packaged in 7 inch reel)

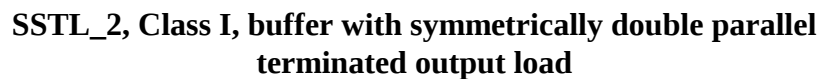
BB1110B13 (4000 parts packaged in 13 inch reel)

Application Notes

Schematics per JEDEC Standard No. 8-9B.



SSTL_2, Class I, symmetrically single parallel terminated output load, and series resistor



For additional data and pricing please contact your local BI Technologies Representative or Distributor or call our main office: (714) 447-2300. Email: sales@bitechnologies.com