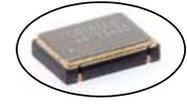


# C33xx Model 5X7 mm SMD, 3.3V, HCMOS



# Clock Oscillator

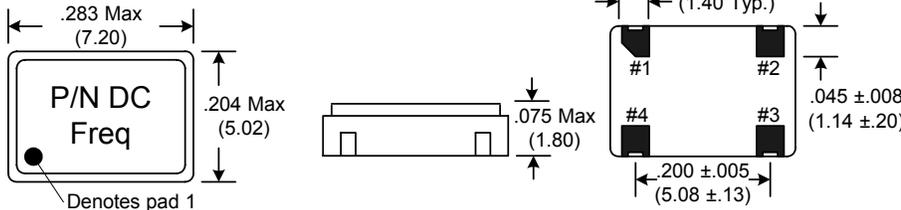


<b>Frequency Range:</b>	1.544MHz to 156.250MHz
<b>Frequency Stability:</b>	±20ppm to ±100ppm
<b>Temperature Range:</b>	0°C to 70°C
(Option M)	-20°C to 70°C
(Option E)	-40°C to 85°C
<b>Storage:</b>	-55°C to 120°C
<b>Input Voltage:</b>	3.3V ± 0.3V
<b>Input Current:</b>	
1.544~34.00MHz	18mA Max
35.00~50.00MHz	25mA Max
51.00~69.00MHz	30mA Max
70.00~156.25 MHz	45mA Max
<b>Standby Current</b>	3uA Typ., 10uA Max
<b>Output:</b>	CMOS
Symmetry:	45/55% Max @ 50% Vdd
Rise/Fall Time:	
1.54~10.00 MHz	5ns Max @ 20% to 80%
10.10~30.00 MHz	4ns Max @ 20% to 80%
30.10~50.00 MHz	3ns Max @ 20% to 80%
50.10~80.00 MHz	2.5ns Max @ 20% to 80%
80.10~156.25 MHz	2ns Max @ 20% to 80%
Logic:	"0" = 10% Vdd Max "1" = 90% Vdd Min
Start-up Time:	10ms Max
Load:	30pF Max, >125MHz 15pF Max
<b>Jitter RMS:</b> 12KHz~80MHz	0.5ps Typ, 1ps Max
<b>Aging:</b>	<3ppm 1st/yr, <1ppm every year thereafter

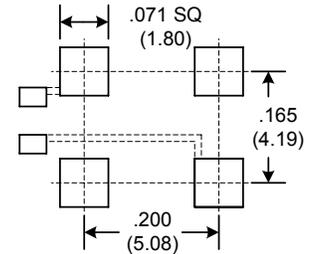
Designed to meet today's requirements for low jitter 3.3V applications. The C33xx Series utilizes fundamental and 3rd overtone crystal technology. No multiplier is used thereby reducing output jitter. Available on 16mm tape and reel in quantities of 1K.

Dimensions inches (mm)

All dimensions are Max unless otherwise specified.

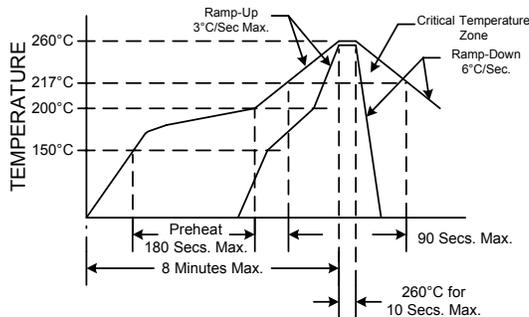


### SUGGESTED PAD LAYOUT



0.01uF Bypass Capacitor Recommended

### RECOMMENDED REFLOW SOLDERING PROFILE

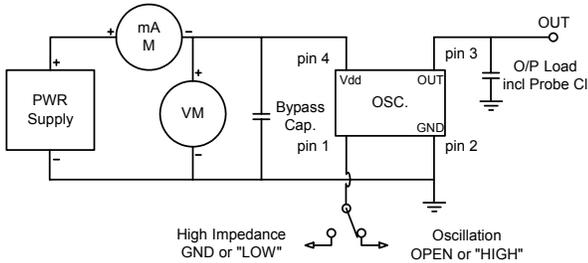


NOTE: Reflow Profile with 240°C peak also acceptable.

### Crystek Part Number Guide

Example: C3392-44.736MHZ  
Example: CM3392-44.736MHZ  
Example: CE3392-44.736MHZ

Temperature			Frequency Stability
0/ 70°C	-20/ 70°C	-40/ 85°C	
C3390	CM3390	CE3390	+/- 100ppm
C3392	CM3392	CE3392	+/- 50ppm
C3391	CM3391	CE3391	+/- 25ppm
C3398	CM3398	CE3398	+/- 20ppm



Tri-State Function	
Function pin 1	Output pin
Open	Active
"1" level 2.4V Min	Active
"0" level 0.4V Max	High Z

Specifications subject to change without notice.

TD-02073 Rev.F



**Crystek Crystals Corporation**

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