



Central™ Semiconductor Corp.

FEATURES:

- MEETS GALVANIC ISOLATION REQUIREMENTS OF IEEE 1394
- HIGH VOLTAGE (70V)
- ULTRAmi™ PACKAGE
- REQUIRES LESS BOARD SPACE THAN 3 INDIVIDUAL DIODES
- LOW FORWARD VOLTAGE

DESCRIPTION:

The CENTRAL SEMICONDUCTOR CMKD6263 contains three Galvanically isolated, High Voltage Silicon Schottky diodes, epoxy molded in an ULTRAmi™ surface mount package, designed for fast switching applications requiring a low forward voltage drop. MARKING CODE: K63.

MAXIMUM RATINGS: PER DIODE (T_A=25°C)

	SYMBOL		UNITS
Peak Repetitive Reverse Voltage	V _{RRM}	70	V
Continuous Forward Current	I _F	15	mA
Forward Surge Current, tp=1.0 s	I _{FSM}	50	mA
Power Dissipation	P _D	250	mW
Operating and Storage			
Junction Temperature	T _J , T _{stg}	-65 to +150	°C
Thermal Resistance	Θ _{JA}	500	°C/W

ELECTRICAL CHARACTERISTICS: PER DIODE (T_A=25°C)

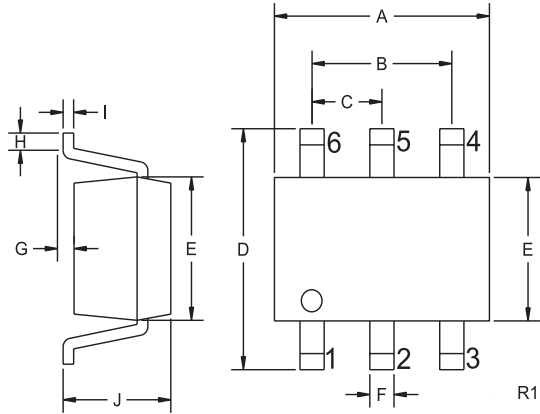
SYMBOL	TEST CONDITIONS	MIN	TYP	MAX	UNITS
BV _R	I _R =10μA	70			V
V _F	I _F =1.0mA		395	410	mV
I _R	V _R =50V		98	200	nA
C _T	V _R =0V, f=1.0MHz			2.0	pF
t _{rr}	I _R =I _F =10mA, I _r =1mA, R _L =100Ω			5.0	ns

R0 (30-August 2001)

SOT-363 CASE - MECHANICAL OUTLINE

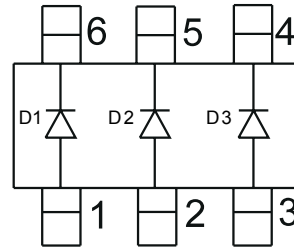
SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.073	0.085	1.85	2.15
B	0.051		1.30	
C	0.026		0.65	
D	0.075	0.091	1.90	2.30
E	0.043	0.055	1.10	1.40
F	0.006	0.012	0.15	0.30
G	0.000	0.004	0.00	0.10
H	0.010	-	0.25	-
I	0.004	0.010	0.10	0.25
J	0.031	0.039	0.80	1.00

SOT-363 (REV: R1)



LEAD CODE:

- 1) Anode D1
- 2) Anode D2
- 3) Anode D3
- 4) Cathode D3
- 5) Cathode D2
- 6) Cathode D1



MARKING CODE: K63