



# Voltage Sample and Infinite Hold

## General Description

The DS4303 is a nonvolatile (NV) sample and infinite-hold adjustable voltage reference. The reference voltage is programmed in-circuit during factory calibration/programming. Programming the reference voltage,  $V_{OUT}$ , is as simple as applying the desired voltage on  $V_{IN}$  and toggling the adjust pin (ADJ) to lock the  $V_{OUT}$  voltage level indefinitely, even if the device is power cycled. The DS4303 replaces current cumbersome factory adjustment arrangements with a low-cost solution that can be adjusted using automated techniques. In addition, the DS4303 has the ability to be readjusted after the unit has been fully assembled and tested. This results in a much more flexible manufacturing arrangement, lower inventory costs, and a quicker time-to-market.

## Applications

Power-Supply Calibration  
Threshold Setting  
Offset Nulling  
Bias Adjusting  
Power Amps  
Pressure Bridges  
Factory-Calibrated Equipment

## Features

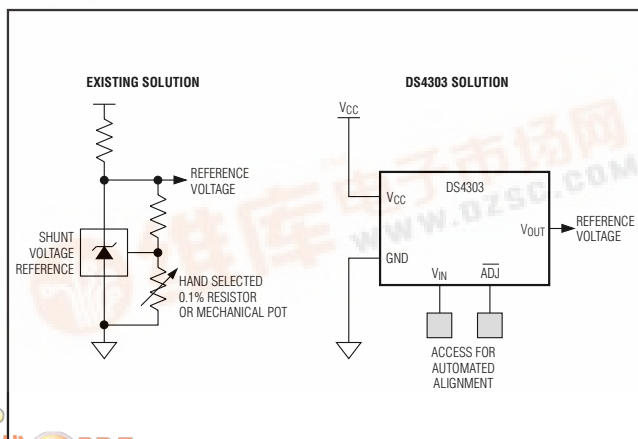
- ◆ Precise Electronically Adjustable Voltage Reference
- ◆ Enables Automated Factory Trimming of Devices Needing Voltage Adjustment
- ◆ Can be Adjusted to Within  $\pm 1\text{mV}$
- ◆ Wide Adjustable Output Voltage Range Within 300mV of the Supply Rails
- ◆ Low Temperature Coefficient
- ◆  $\pm 1\text{mA}$  of Output-Current Drive
- ◆ NV Memory Stores the Voltage Indefinitely
- ◆ Output Short-Circuit Protection
- ◆ Low Cost
- ◆ Low Power Consumption
- ◆ 2.4V to 3.6V Single-Supply Operation
- ◆ Small 5-Lead SOT23 Package

## Ordering Information

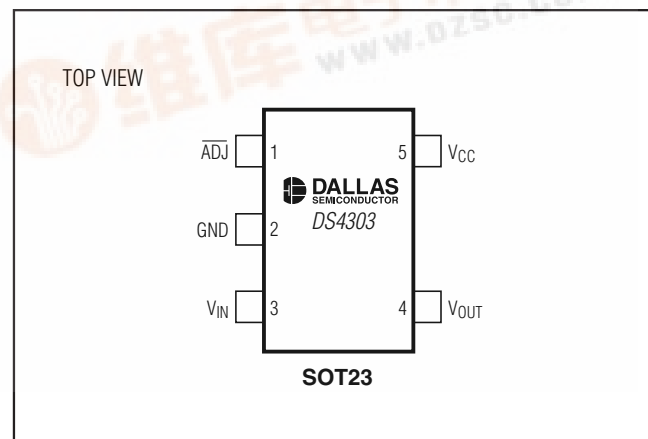
PART	TEMP RANGE	PIN-PACKAGE	SOT MARK
DS4303R/T&R	-40°C to +85°C	SOT23-5	4303
DS4303R+T&R	-40°C to +85°C	SOT23-5	4303+

+Denotes lead-free package.

## Typical Operating Circuit



## Pin Configuration



DS4303

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## ABSOLUTE MAXIMUM RATINGS

Voltage on  $V_{CC}$  Relative to GND .....-0.5V to +6.0V  
 Voltage on  $V_{IN}$ ,  $\overline{ADJ}$ , and  $V_{OUT}$   
 Relative to GND ...-0.5V to ( $V_{CC} + 0.5V$ ), not to exceed +6.0V  
 Operating Temperature Range .....-40°C to +85°C

EEPROM Programming Adjust Temperature.....0°C to +70°C  
 $V_{OUT}$  to GND Short-Circuit Duration .....Continuous  
 Storage Temperature Range .....-55°C to +125°C  
 Soldering Temperature ...See IPC/JEDEC J-STD-020 Specification

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

## RECOMMENDED OPERATING CONDITIONS

( $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ )

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Voltage	$V_{CC}$	(Note 1)	2.4		3.6	V
$V_{IN}$ Voltage Range	$V_{IN}$		0.3	$V_{CC} - 0.3$		V
$\overline{ADJ}$ Logic 0	$V_{IL}$		-0.3	$0.3 \times V_{CC}$		V
$V_{OUT}$ Current	$V_{OUTI}$		-1		+1	mA
$V_{OUT}$ Load	$V_{OUTL}$				100	pF

## ELECTRICAL CHARACTERISTICS

( $V_{CC} = +2.4$  to  $+3.6V$ ,  $T_A = -40^\circ\text{C}$  to  $+85^\circ\text{C}$ , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Supply Current	$I_{CC}$	$V_{IN}$ , $\overline{ADJ}$ and $V_{OUT}$ = open circuit		1.1	1.6	mA
$V_{IN}$ Resistance	$R_{PD}$		95			k $\Omega$
$\overline{ADJ}$ Pullup Resistance	$R_{PU}$		18			k $\Omega$
$V_{OUT}$ Voltage Range	$V_{OUTR}$	(Note 1)	0.3	$V_{CC} - 0.3$		V
$V_{OUT}$ Tracking Accuracy	$V_{OUTTA}$	(Note 2)			$\pm 20$	mV
$V_{OUT}$ Quantization	$V_{OUTQ}$	(Note 3)			$\pm 1$	mV
$V_{OUT}$ Temperature Coefficient	$V_{OUTTC}$	-40°C to +85°C, $V_{OUT} = 0.4V$		$\pm 104$		$\mu V/^\circ\text{C}$
		-40°C to +25°C, $V_{OUT} = 3.0V$	-5	+28	+62	ppm/°C
		+25°C to +85°C, $V_{OUT} = 3.0V$	-31	-13	+6	
$V_{OUT}$ Line Regulation	$V_{OUTLN}$		-4.0		+1.0	mV/V
$V_{OUT}$ Load Regulation	$V_{OUTLD}$	$-1\text{mA} \leq V_{OUTI} \leq +1\text{mA}$			5.5	mV/mA
Long-Term Stability	$V_{OUTLTS}$	1000 hours at +25°C		TBD		ppm
$V_{OUT}$ Noise	$e_{n1}$	$0.1\text{Hz} \leq f \leq 10\text{Hz}$		200		$\mu V_{P-P}$
	$e_{n2}$	$10\text{Hz} \leq f \leq 1\text{kHz}$		26		$\mu V_{RMS}$
$V_{OUT}$ PSRR	$V_{OUTPSRR}$	$f = 200\text{kHz}$		21		dB
$V_{OUT}$ Self-Adjust Settling Time	$t_{ST}$	(Note 4)		11	15	ms/V
EEPROM Programming Time	$t_{PW}$	(Note 5)		9	12	ms
Turn-On Time	$t_{ON}$	$V_{IN}$ and $\overline{ADJ}$ = open circuit (Note 6)			10	$\mu s$
$\overline{ADJ}$ Toggle Low Time	$t_{ADJ}$		100			ns
$V_{OUT}$ Factory Trimmed Value	$V_{OUTFT}$	+25°C, $V_{CC} = 3.3V$ (Note 7)		1200		mV

# Voltage Sample and Infinite Hold

## NONVOLATILE MEMORY CHARACTERISTICS

( $V_{CC} = +2.4V$  to  $3.6V$ , unless otherwise noted.)

PARAMETER	SYMBOL	CONDITIONS	MIN	TYP	MAX	UNITS
Programming Cycles		+70°C (Note 8)	50,000			Cycles

**Note 1:** All voltages referenced to ground.

**Note 2:** Tracking accuracy is defined as  $V_{OUT} - V_{IN}$  after the DS4303 has completed self-adjustment.

**Note 3:** Quantization refers to the size of the voltage steps used to track the input signal.

**Note 4:** Settling time is the maximum amount of time  $V_{OUT}$  requires to self-adjust. The settling time is determined by the following formula:  $\Delta V_{OUT} \times t_{ST}$ .

**Note 5:** EEPROM programming time is the hold time required after the DS4303 has completed self-adjustment before  $V_{IN}$  or  $V_{CC}$  can be removed or before  $\overline{ADJ}$  can be toggled low once again.

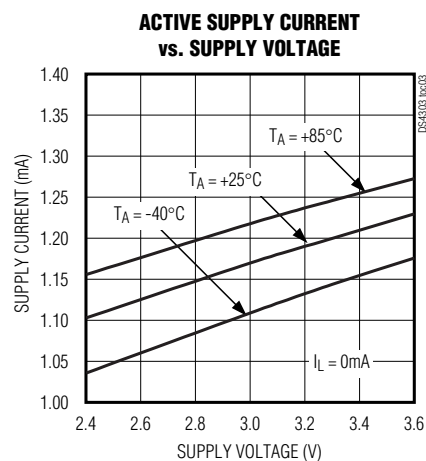
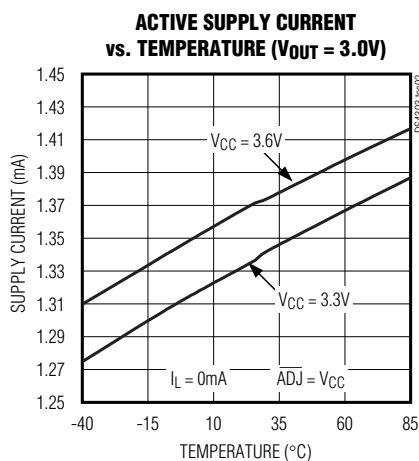
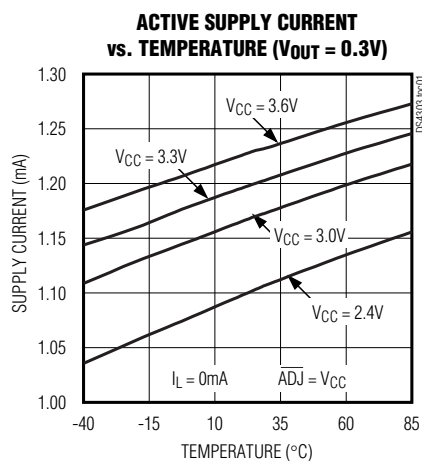
**Note 6:** Turn-on time is defined as the time required for  $V_{OUT}$  to reach its specified accuracy after the required supply voltage is applied.

**Note 7:**  $V_{OUT}$  not loaded.

**Note 8:** Guaranteed by design.

## Typical Operating Characteristics

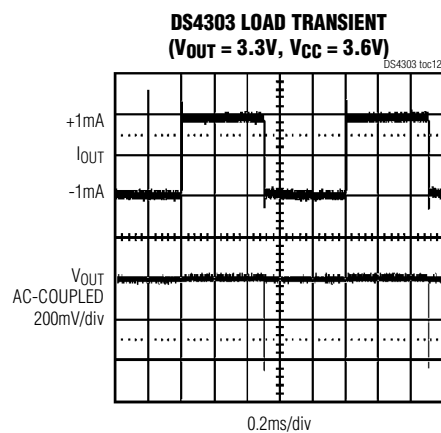
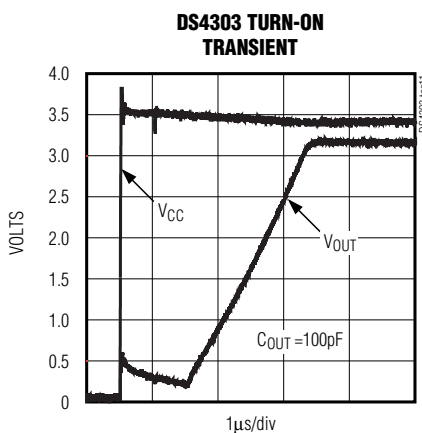
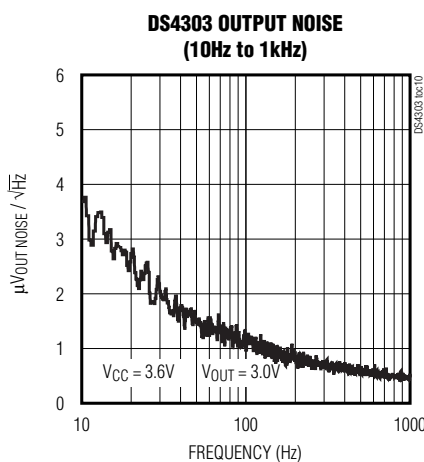
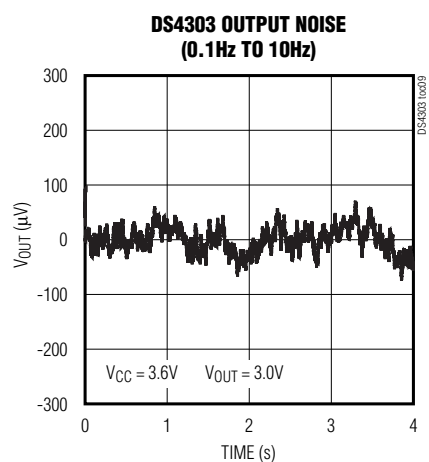
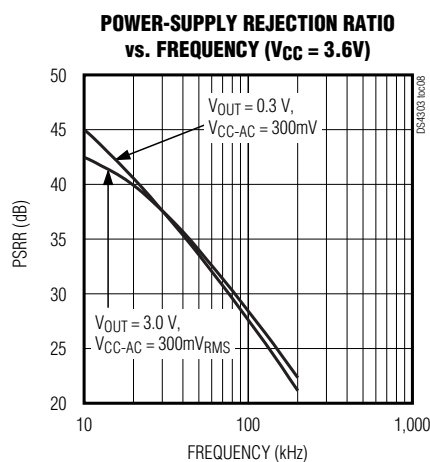
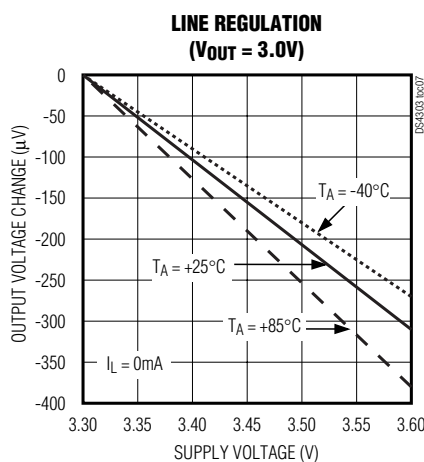
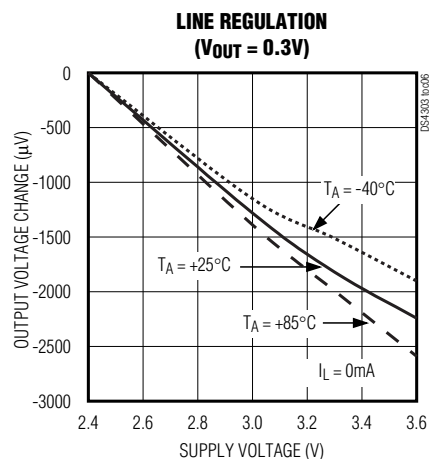
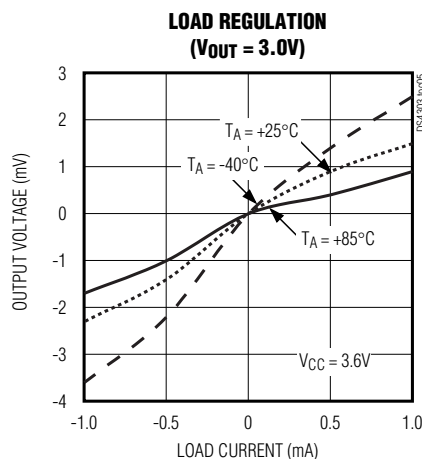
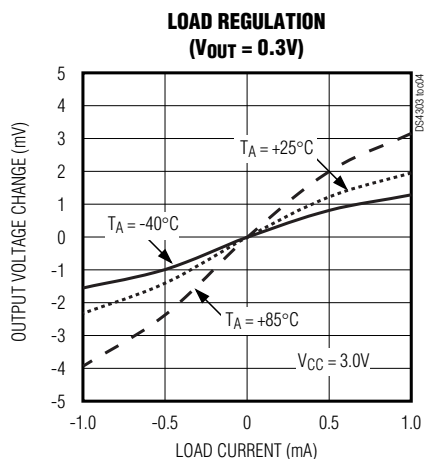
( $V_{CC} = 3.3V$ ,  $T_A = +25^\circ C$ , unless otherwise noted.)



# Voltage Sample and Infinite Hold

## Typical Operating Characteristics (continued)

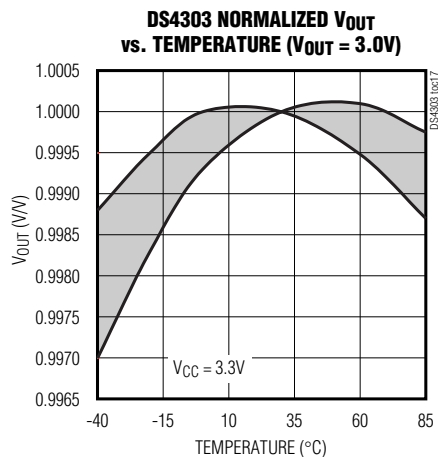
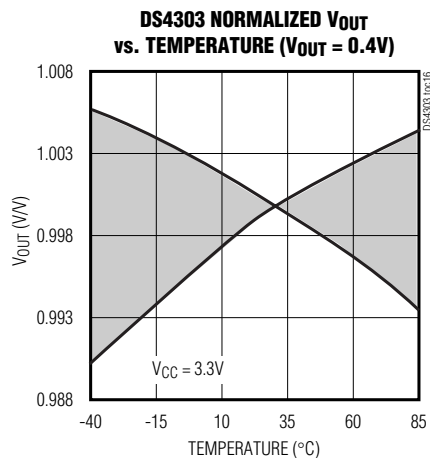
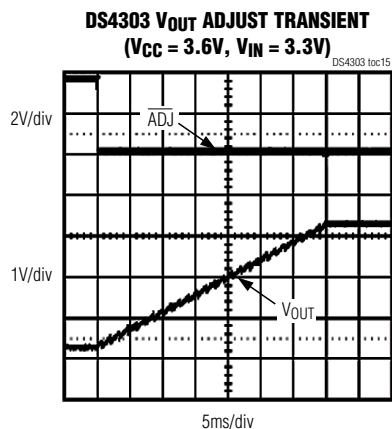
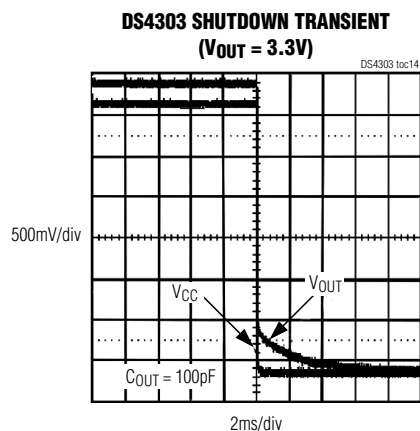
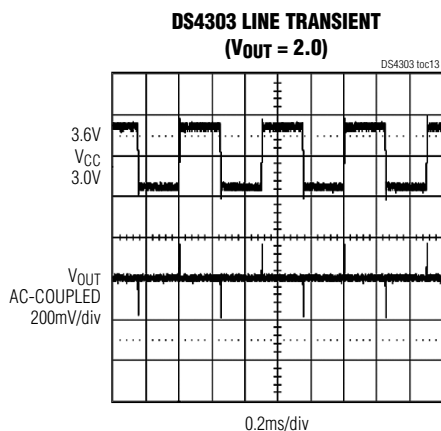
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# Voltage Sample and Infinite Hold

## Typical Operating Characteristics (continued)

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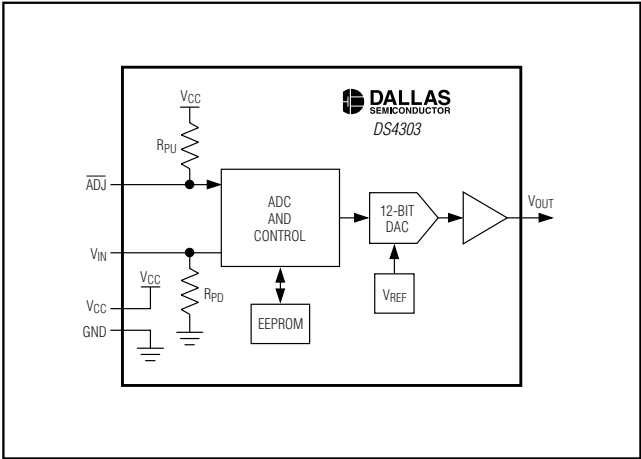


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## Pin Description

PIN	NAME	FUNCTION
1	$\overline{\text{ADJ}}$	Adjust Control Input
2	GND	Ground
3	$V_{\text{IN}}$	Sample Voltage Input
4	$V_{\text{OUT}}$	Voltage Output
5	$V_{\text{CC}}$	Power-Supply Voltage

## Block Diagram



## Detailed Description

The DS4303 provides a precise, NV output voltage,  $V_{\text{OUT}}$ , making it an ideal solution for factory calibration of embedded systems. The DS4303 output voltage can be adjusted over almost the entire operating supply range of the device, and it can be precisely set to within  $\pm 1\text{mV}$ . A graphical description of the DS4303 is provided in the block diagram.

During factory calibration, a simple adjustment procedure must be followed. This entire procedure includes setting  $V_{\text{IN}}$ , toggling  $\overline{\text{ADJ}}$ , waiting as  $V_{\text{OUT}}$  self-adjusts, and waiting for the completion of the EEPROM storage cycle (See the timing diagram in Figure 1). At the start of calibration, a voltage must be placed on  $V_{\text{IN}}$ . This voltage needs to be completely stable before the adjustment procedure begins, and it must remain stable throughout the entire adjustment procedure. The DS4303 will start its self-adjust procedure when the  $\overline{\text{ADJ}}$  pin is pulled low and held low for at least  $t_{\text{ADJ}}$ , after which it can be released at any time. Once  $\overline{\text{ADJ}}$  has been released, it should not be toggled again for the remainder of the adjustment procedure. After the falling edge on  $\overline{\text{ADJ}}$  and the wait time,  $t_{\text{ADJ}}$ , the  $V_{\text{OUT}}$  self-adjust period begins. The length of the  $V_{\text{OUT}}$  self-adjust period can be determined using the formula  $\Delta V \times t_{\text{ST}}$ , where  $\Delta V$  is  $|V_{\text{OUT OLD}} - V_{\text{OUT NEW}}|$ .

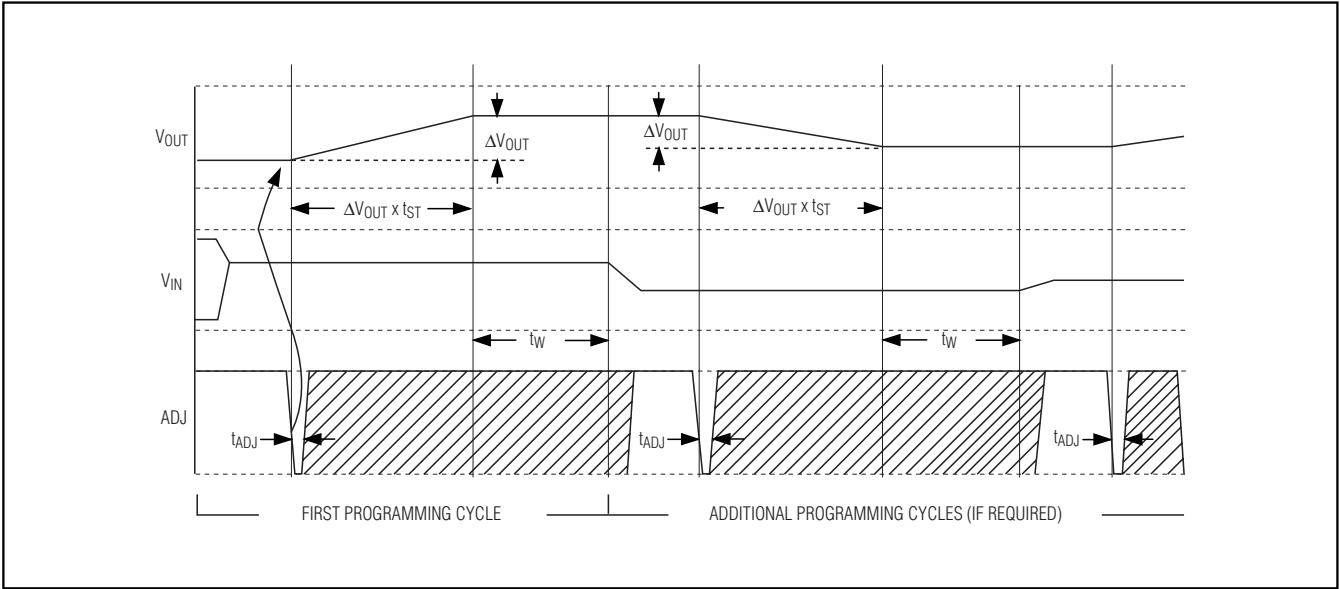


Figure 1. Timing Diagram

## Voltage Sample and Infinite Hold

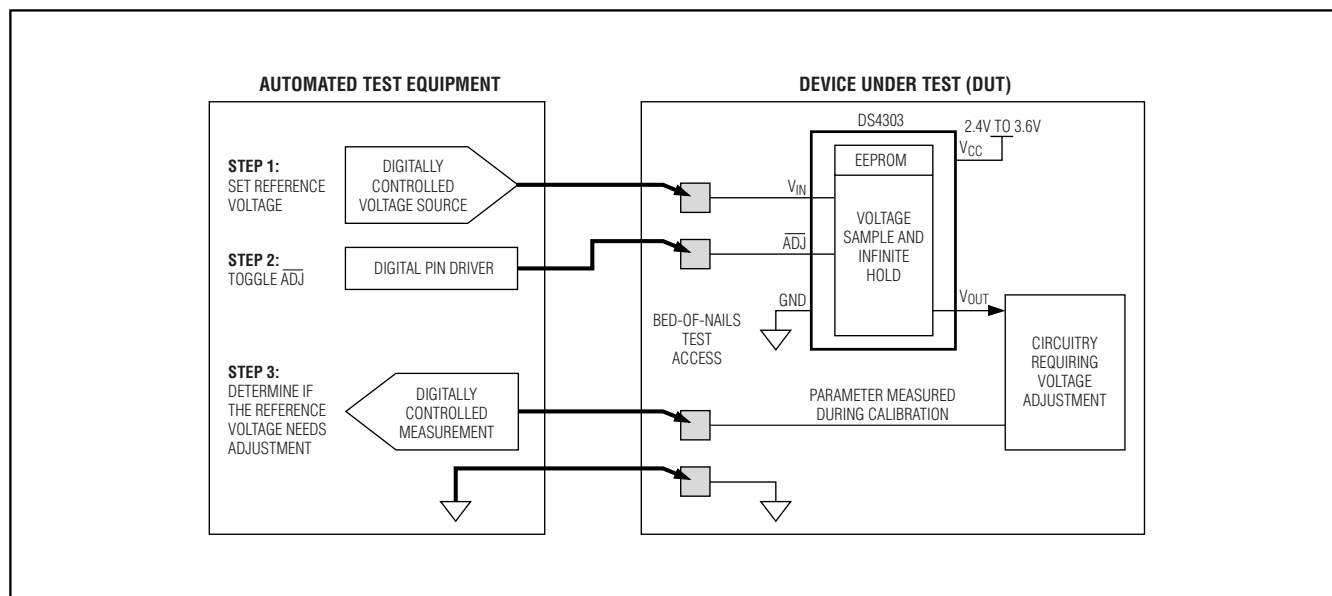


Figure 2. Application Circuit

During the  $V_{OUT}$  self-adjust period, the DS4303 internally adjusts the onboard DAC until  $V_{OUT}$  matches  $V_{IN}$ . After  $V_{OUT}$  has stabilized to within the tracking accuracy,  $V_{OUTTA}$ , of  $V_{IN}$ , it will be automatically stored in EEPROM. The storage period lasts for the duration of the EEPROM write time,  $t_{w}$ . After the first adjustment procedure has completed,  $V_{OUT}$  can be measured, and if necessary  $V_{IN}$  can be readjusted and the entire adjustment procedure can be repeated to fine-tune  $V_{OUT}$  within the  $V_{OUTQ}$  range.

Following each self-adjust procedure,  $V_{OUT}$  is saved indefinitely, even if the DS4303 is power cycled.

### Automated Programming Procedure

Figure 2 details an example of how the DS4303 can be adjusted in an application. During factory alignment, a three/four-node bed-of-nails is used to: (1) provide the adjustment voltage through the  $V_{IN}$  pin, (2) control the  $\overline{ADJ}$  input, and (3) sense the needed feedback parameter. During manufacture, an automated test procedure adjusts  $V_{OUT}$ , by changing  $V_{IN}$ , until the feedback parameter is optimized. After the bed-of-nails operation is complete, both the  $V_{IN}$  and  $\overline{ADJ}$  inputs are left open

circuit.  $V_{OUT}$  can be readjusted at any time by following the same procedure. The closed-loop nature of the adjustment process removes all the system inaccuracies such as resistor tolerances, amplifier offsets, gain mismatches, and even the inaccuracies in the automated equipment that provides the reference voltage.

### Typical Operating Circuit

The typical operating circuit shows an example of how the DS4303 can replace most existing calibration solutions. Many power supplies use a shunt voltage reference to provide the internal reference voltage, and fine-tune adjustments are often made with hand-selected discrete resistors. The DS4303 replaces this cumbersome arrangement with a solution that is capable of being adjusted by automated techniques. An additional benefit of the DS4303 is the ability to provide a much lower voltage (down to 300mV) than is possible with shunt voltage references. Another benefit of the DS4303 is the ability to be adjusted after the unit has been fully assembled and tested, resulting in a much more flexible manufacturing arrangement, lower inventory costs, and a quicker time-to-market.

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## Layout Considerations

To prevent an inadvertent programming cycle from occurring during power-up, minimize capacitive loading on the  $\overline{\text{ADJ}}$  pin. A large capacitance on this pin could potentially hold  $\overline{\text{ADJ}}$  in a low state long enough that a programming cycle is initiated.

## Power-Supply Decoupling

To achieve best results, it is highly recommended that a decoupling capacitor is used on the IC power-supply pin. Typical values of decoupling capacitors are 0.01 $\mu\text{F}$  or 0.1 $\mu\text{F}$ . Use a high-quality, ceramic, surface-mount capacitor, and mount it as close as possible to the  $\text{VCC}$  and  $\text{GND}$  pins of the IC to minimize lead inductance.

## Chip Topology

TRANSISTOR COUNT: 6001

SUBSTRATE CONNECTED TO GROUND

## Package Information

For the latest package outline information, go to [www.maxim-ic.com/DallasPackInfo](http://www.maxim-ic.com/DallasPackInfo).

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