

DV200-27000S Series

HIGH RELIABILITY DC-DC CONVERTERS

DESCRIPTION

The DV200 series of high reliability, isolated DC-DC converters is operable over a wide (-55 °C to +100 °C) temperature range with no power derating. Unique to the DV200 series is a magnetic feedback circuit that is radiation immune. Operating at a nominal fixed frequency of 500 kHz, these regulated, isolated units utilize well-controlled undervoltage lockout circuitry to eliminate slow start-up problems. The current sharing function allows a maximum of five units to be connected in parallel to boost the total output power to 5 times. The output voltage is trimmable up to +10% or down -20%.

These converters are designed and manufactured in a facility qualified to ISO9001 and certified to MIL-PRF-38534 and MIL-STD-883.

FEATURES

- High Reliability
- Parallel Up to 5 Units With Current Sharing
- Output Voltage Trim Up +10% or Down –20%
- Wide Input Voltage Range: 160 to 400 Volts
- Up to 175 Watts Output Power
- Radiation Immune Magnetic Feedback Circuit
- NO Use of Optoisolators
- Undervoltage Lockout
- Indefinite Short Circuit Protection
- Current Limit Protection
- Input Transient Voltage: 500 Volts for 1 second
- High Power Density: ≈ 80 W/in³
- Custom Versions Available
- Additional Environmental Screening Available

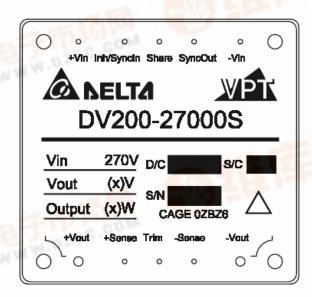


Figure 1 – DV200-27000S DC-DC Converter (Not To Scale)





DV200-27000S Series

 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +100^{\circ}C, \ V_{IN} = +270V \pm 5\%, \ Full \ Load, \ Unless \ Otherwise \ Specified)$

ABSOLUTE MAXIMUM RATINGS

Power Dissipation (Full Load, T_{CASE} = +100°C) 40 Watts

Junction Temperature Rise to Case

Storage Temperature

-65°C to +135°C

Lead Solder Temperature (10 seconds)

270°C

+25°C

Weight (Maximum) 115 Grams

Parameter		Conditions	DV	200-2703F	R3S	DV200-27005S			Units
Parameter		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC									
INPUT		Continuous	160	270	400	160	270	400	V
Voltage		Transient, 1 sec	-	-	500	-	-	500	V
Current		Inhibited	-	3	5	-	3	5	mA
Guitent		No Load	-	8	20	-	8	20	mA
Ripple Current		Full Load, 20Hz to 10MHz	-	100	150	-	100	150	mA_{p-p}
Inhibit Pin Input ³		To Disable Output	0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Volta	age ³		9.5	10.5	11.5	9.5	10.5	11.5	V
UVLO Turn On			140	150	159	140	150	159	V
UVLO Turn Off ³			135	145	155	135	145	155	V
OUTPUT	$V_{\text{OUT}} \\$	T _{CASE} = 25°C	3.27	3.30	3.33	4.95	5.00	5.05	V
Voltage	V_{OUT}	$T_{CASE} = -55^{\circ}C$ to $+100^{\circ}C$	3.23	3.30	3.37	4.90	5.00	5.10	V
Power ²		V _{IN} = 160V to 400V	0	-	100	0	-	150	W
Current ²	I_{OUT}	V _{IN} = 160V to 400V	0	-	30	0	-	30	Α
Ripple Voltage	V_{OUT}	Full Load, 10kHz to 10MHz	-	50	100	-	50	100	mV_{p-p}
Line Regulation	V_{OUT}	V _{IN} = 160V to 400V	-	10	50	-	10	50	mV
Load Regulation	$V_{\text{OUT}} \\$	No Load to Full Load	-	10	50	-	10	50	mV
Voltage Trim	V_{OUT}	Full Load	-20	0	10	-20	-	10	%
EFFICIENCY		Full Load	76	78	-	80	83	-	%
LOAD FAULT POWER DISSIPA	TION	Overload ³	-	45	-	-	45	-	W
LOAD FACET FOWER DISSIFF	TION	Short Circuit	-	45	-	-	45	-	W
CAPACITIVE LOAD ³			-	-	2000	-	-	2000	μF
SWITCHING FREQUENCY			400	500	600	400	500	600	kHz
SYNC FREQUENCY RANGE		$V_H - V_L = 5V$ Duty Cycle = 20% - 80%	450	500	550	450	500	550	kHz
ISOLATION⁴		1000 V _{DC}	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)	MTBF (MIL-HDBK-217F)		-	955	-	-	955	-	kHrs
DYNAMIC									
Load Step Output Transient	V_{OUT}	Half Load to Full Load	ı	200	400	-	200	400	mV_{PK}
Load Step Recovery ⁵		Tian Load to I dil Load	ı	200	300	-	200	300	μSec
Line Step Output Transient ³	V_{OUT}	V _{IN} = 180V to 400V	ı	200	500	-	200	500	mV_{PK}
Line Step Recovery ^{3, 5}		VIN - 100V 10 400V	-	50	200	-	50	200	μSec
Turn On Delay	V_{OUT}	\/ = 0\/ to 270\/	-	150	300	-	150	300	mSec
Turn On Overshoot		$V_{IN} = 0V \text{ to } 270V$	-	0	15	-	0	25	mV_{PK}

Notes: 1. Dependant on output voltage. 2. Derate linearly to 0 at 125°C. 3. Verified by qualification testing. 4. Input to output and input to case. Isolation from output to case is 500VDC. 5. Time for output voltage to settle within 1% of its nominal value.



DV200-27000S Series

 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +100^{\circ}C, \ V_{IN} = +270V \pm 5\%, \ Full \ Load, \ Unless \ Otherwise \ Specified)$

ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous) 400 V_{DC} Input Voltage (Transient, 1 second) 500 Volts
Output Power^{1,2} 175 Watts

Output Power^{1,2} 175 Watts Power Dissipation (Full Load, $T_{CASE} = +100$ °C) 40 Watts Junction Temperature Rise to Case

Storage Temperature (10 accords)

Lead Solder Temperature (10 seconds) 270°C

Weight (Maximum)

115 Grams

-65°C to +135°C

+25°C

Dovometer		Conditions	DV	/200-2701	2S	D\	/200-2701	5S	Units
Parameter		Conditions	Min	Тур	Max	Min	Тур	Max	Units
STATIC									
INPUT		Continuous	160	270	400	160	270	400	V
Voltage		Transient, 1 sec	ı	-	500	-	-	500	V
Current		Inhibited	-	3	5	-	3	5	mA
Current		No Load	-	8	20	-	8	20	mA
Ripple Current		Full Load, 20Hz to 10MHz	-	100	200	-	100	200	mA _{p-p}
Inhibit Pin Input ³		To Disable Output	0	-	1.5	0	-	1.5	V
Inhibit Pin Open Circuit Volt	age ³		9.5	10.5	11.5	9.5	10.5	11.5	V
UVLO Turn On			140	150	159	140	150	159	V
UVLO Turn Off ³			135	145	155	135	145	155	V
OUTPUT	V_{OUT}	T _{CASE} = 25°C	11.88	12.0	12.12	14.85	15.0	15.15	V
Voltage	V_{OUT}	T _{CASE} = -55°C to +100°C	11.76	12.0	12.24	14.70	15.0	15.30	V
Power ²		V _{IN} = 160V to 400V	0	-	175	0	-	175	W
Current ²	I _{OUT}	V _{IN} = 160V to 400V	0	-	14.6	0	-	11.7	Α
Ripple Voltage	V_{OUT}	Full Load, 10kHz to 10MHz	-	50	150	-	50	150	mV _{p-p}
Line Regulation	V _{OUT}	V _{IN} = 160V to 400V	-	10	50	-	10	50	mV
Load Regulation	V _{OUT}	No Load to Full Load	-	10	50	-	10	50	mV
Voltage Trim	V _{OUT}	Full Load	-20	-	10	-20	-	10	%
EFFICIENCY		Full Load	82	85	-	82	86	-	%
LOAD FAULT POWER DISSIPA	ATION	Overload ³	-	45	-	-	45	-	W
LOAD FAULT POWER DISSIPA	ATION	Short Circuit	-	45	-	-	45	-	W
CAPACITIVE LOAD ³			-	-	1000	-	-	1000	μF
SWITCHING FREQUENCY			400	500	600	400	500	600	kHz
SYNC FREQUENCY RANGE		$V_H - V_L = 5V$ Duty Cycle = 20% - 80%	450	500	550	450	500	550	kHz
ISOLATION⁴		1000 V _{DC}	100	-	-	100	-	-	ΜΩ
MTBF (MIL-HDBK-217F)		GB @ T _C = 55°C	-	955	-	-	955	-	kHrs
DYNAMIC									
Load Step Output Transient	V_{OUT}	Half Load to Full Load	1	900	1200	-	900	1200	mV_{PK}
Load Step Recovery ⁵		TIAII LUAU IU FUII LUAU	-	200	400	-	200	400	μSec
Line Step Output Transient ³	V_{OUT}	\/ = 400\/ to 400\/	-	900	1200	-	900	1200	mV_{PK}
Line Step Recovery3,5		V _{IN} = 180V to 400V	-	200	400	-	200	400	μSec
Turn On Delay	V _{OUT}	\\ \ 0\\\\\ 070\\\	-	150	300	-	150	300	mSec
Turn On Overshoot		$V_{IN} = 0V \text{ to } 270V$	-	-	50	-	-	50	mV_{PK}
Taill off overellost				l	I .		I .	I .	L

Notes: 1. Dependant on output voltage. 2. Derate linearly to 0 at 125°C. 3. Verified by qualification testing. 4. Input to output and input to case. Isolation from output to case is 500VDC. 5. Time for output voltage to settle within 1% of its nominal value.





 $SPECIFICATIONS \ (T_{CASE} = -55^{\circ}C \ to \ +100^{\circ}C, \ V_{IN} = +270V \pm 5\%, \ Full \ Load, \ Unless \ Otherwise \ Specified)$

ABSOLUTE MAXIMUM RATINGS

Input Voltage (Continuous) 400 V_{DC}

Input Voltage (Transient, 1 second) 500 Volts
Output Power^{1,2} 175 Watts

Power Dissipation (Full Load, $T_{CASE} = +100^{\circ}C$) 40 Watts

Junction Temperature Rise to Case +25°C
Storage Temperature -65°C to +135°C

Lead Solder Temperature (10 seconds) 270°C

Weight (Maximum) 115 Grams

Parameter		Conditions	D۱	DV200-27028S			
Parameter		Conditions	Min	Тур	Max	Units	
STATIC							
INPUT		Continuous	160	270	400	V	
Voltage		Transient, 1 sec	-	-	500	V	
Current		Inhibited	-	3	5	mA	
Current		No Load	-	8	20	mA	
Ripple Current		Full Load, 20Hz to 10MHz	-	100	200	mA _{p-p}	
Inhibit Pin Input ³		To Disable Output	0	-	1.5	V	
Inhibit Pin Open Circuit Vol	tage ³		9.5	10.5	11.5	V	
UVLO Turn On			140	150	159	V	
UVLO Turn Off ³			135	145	155	V	
OUTPUT	V_{OUT}	T _{CASE} = 25°C	27.72	28.0	28.28	V	
Voltage	V_{OUT}	T _{CASE} = -55°C to +100°C	27.44	28.0	28.56	V	
Power ²		V _{IN} = 160V to 400V	0	-	175	W	
Current ²	I _{OUT}	V _{IN} = 160V to 400V	0	-	6.25	Α	
Ripple Voltage	V _{OUT}	Full Load, 10kHz to 10MHz	-	100	250	mV _{p-p}	
Line Regulation	V _{out}	V _{IN} = 160V to 400V	-	10	100	mV	
Load Regulation	V _{out}	No Load to Full Load	-	10	100	mV	
Voltage Trim	V _{out}	Full Load	-20	-	10	%	
EFFICIENCY		Full Load	82	86	-	%	
		Overload ³	-	45	-	W	
LOAD FAULT POWER DISSIP	ATION	Short Circuit	-	45	-	W	
CAPACITIVE LOAD ³			-	-	1000	μF	
SWITCHING FREQUENCY			400	500	600	kHz	
SYNC FREQUENCY RANGE		V _H - V _L = 5V Duty Cycle = 20% - 80%	450	500	550	kHz	
ISOLATION⁴		1000 V _{DC}	100	-	-	ΜΩ	
MTBF (MIL-HDBK-217F)		GB @ T _C = 55°C	-	955	-	kHrs	
DYNAMIC				L			
Load Step Output Transient	V_{OUT}	Half Load to Full Load	-	1600	2400	mV_{PK}	
Load Step Recovery ⁵		Half Load to Full Load	-	200	400	μSec	
Line Step Output Transient ³	V _{OUT}	1001/1 (20)	-	1600	2400	mV _{PK}	
Line Step Recovery ^{3,5}		V _{IN} = 180V to 400V	-	200	400	μSec	
Turn On Delay	V _{OUT}		-	150	300	mSec	
Turn On Overshoot		V_{IN} = 0V to 270V	-	-	100	mV_{PK}	
		2 Derate linearly to 0 at 125°0) () \(\frac{1}{2} = \frac{1}{2} \text{fit}	l l 1:4			

Notes: 1. Dependant on output voltage. 2. Derate linearly to 0 at 125°C. 3. Verified by qualification testing. 4. Input to output and input to case. Isolation from output to case is 500VDC. 5. Time for output voltage to settle within 1% of its nominal value.



BLOCK DIAGRAM

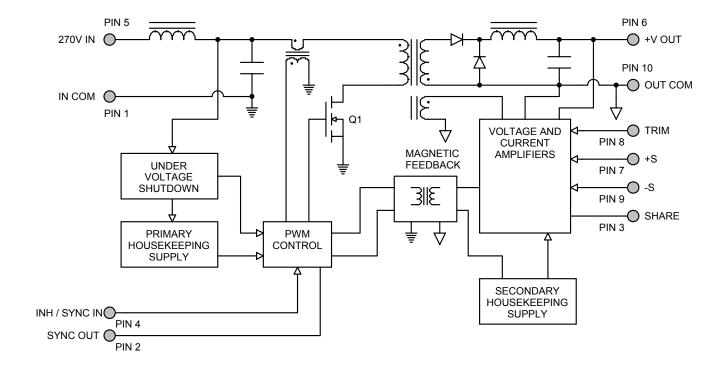


Figure 2

CONNECTION DIAGRAM

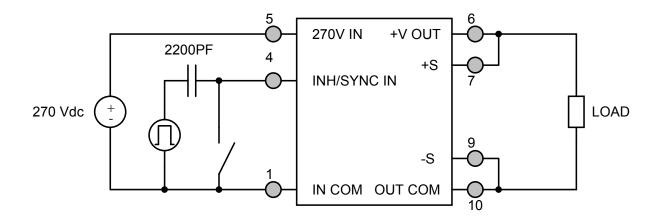


Figure 3



INHIBIT DRIVE CONNECTION DIAGRAMS

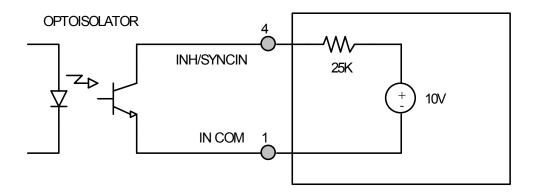


Figure 4 – Isolated Inhibit Drive and Internal Equivalent Circuit

PARALLEL CONNECTION DIAGRAM

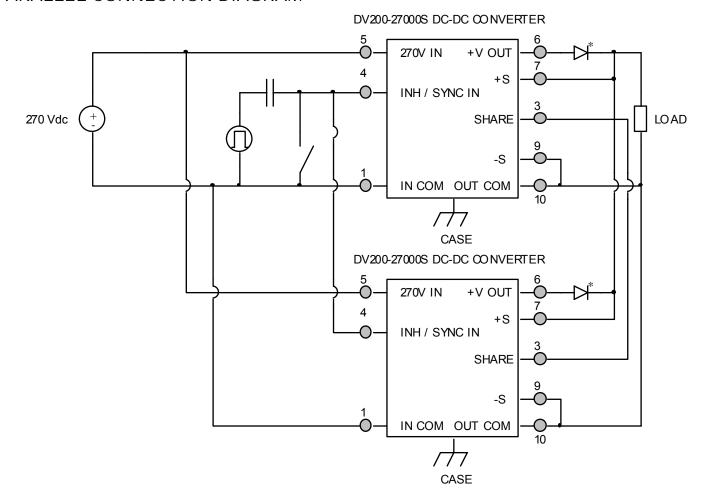
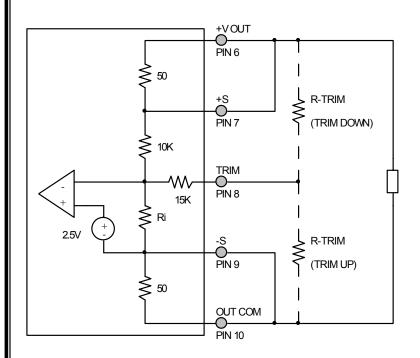


Figure 5 – Parallel Connection with Current Sharing (*Shown with optional "OR" ing diode)



OUTPUT VOLTAGE TRIM



The output voltage can be trimmed down by connecting a resistor between the TRIM pin (PIN 8) and the +V OUT pin (PIN 6), or can be trimmed up by connecting a resistor between the TRIM pin (PIN 8) and the OUT COM pin (PIN 10). The maximum trim range is +10% up and -20% down. The appropriate resistor values versus the output voltage are given in the trim table below.

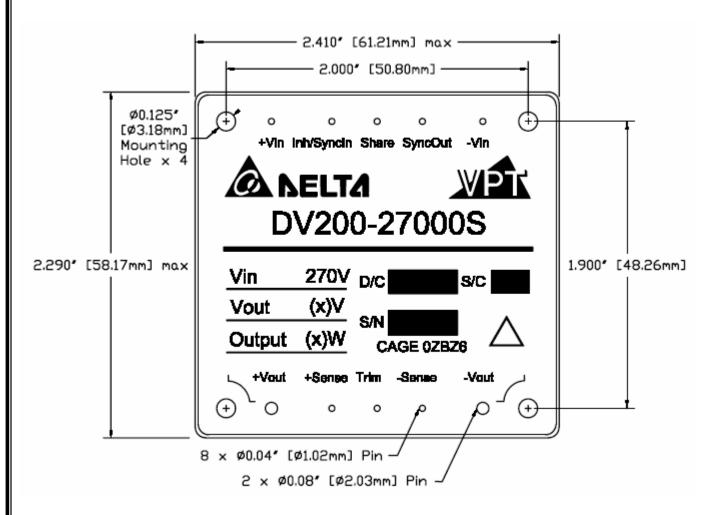
Figure 6 - Output Voltage Trim

DV200-	2703R3S	DV200	-27005S	DV200	-27012S	DV200-27015S		DV200	-27028S
+V _{OUT} (V)	R _{TRIM} (Ω)								
3.60	68.3k	5.5	35k	13.2	5.8k	16.50	1.7k	30.5	1.9k
3.55	85k	5.4	47.5k	13.0	10k	16.25	5k	30	6.2k
3.50	110k	5.3	68.3k	12.8	16.2k	16.00	10k	29.5	13.2k
3.45	151.7k	5.2	110k	12.6	26.6k	15.75	18.3k	29	27.3k
3.40	235k	5.1	235k	12.4	47.3k	15.50	35k	28.5	69.7k
3.35	485k	5.0	-	12.2	109k	15.25	85k	28	-
3.30	-	4.9	225k	12.0	-	15.00	-	27.5	832k
3.25	135k	4.8	100k	11.8	454k	14.75	475k	27	400k
3.20	55k	4.7	58.3k	11.6	213k	14.50	225k	26.5	256k
3.15	28.3k	4.6	37.5k	11.4	134k	14.25	142k	26	184k
3.10	15k	4.5	25k	11.2	94k	14.00	100k	25.5	141k
3.05	7k	4.4	16.7k	11.0	70.1k	13.75	75k	25	112k
3.00	1.7k	4.3	10.7k	10.8	54.3k	13.50	58.3k	24.5	91.4k
		4.2	6.3k	10.6	42.9k	13.25	46.4k	24	76k
		4.1	2.8k	10.4	34.4k	13.00	37.5k	23.5	64k
		4.0	0	10.2	27.8k	12.75	30.6k	23	54.4k
				10.0	22.5k	12.50	25k	22.5	46.6k
				9.8	18.2k	12.25	20.5k		
				9.6	14.6k	12.00	16.7k		

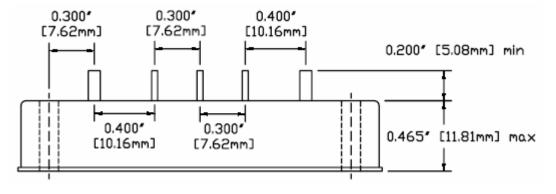




PACKAGE SPECIFICATIONS



TOP VIEW



PIN	FUNCTION
1	IN COM
2	SYNC OUT
3	SHARE
4	INH / SYNC IN
5	270V IN
6	+V OUT
7	+S
8	TRIM
9	-S
10	OUT COM

SIDE VIEW

Figure 7 – Package and Pinout (Dimensional Limits are ±0.005" Unless Otherwise Stated)



PACKAGE PIN DESCRIPTION

Pin	Function	Description
1	IN COM	Input Common Connection
2	SYNC OUT	Output Synchronization Signal
3	SHARE	Current Share
4	INH / SYNC IN	Logic Low = Disabled Output. Unconnected or open collector TTL or Square-wave Synchronization Signal = Enabled Output.
5	270V IN	Positive Input Voltage Connection
6	+V OUT	Positive Output Voltage Connection
7	+S	Positive Sense
8	TRIM	Trim Output Voltage to +10%, -20% of Nominal Value
9	-S	Return Sense
10	OUT COM	Output Common Connection

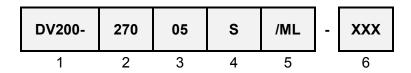
ENVIRONMENTAL SCREENING

Screening	MIL-STD-883	Standard (No Suffix)	Military /ML
Pre-Cap Inspection	IPC-A-610 Class III	•	•
Temperature Cycling	-55°C, 100°C, 10 cycles		•
Burn-In	96 hours at +100°C 12 hours at +100°C	•	•
Final Electrical	100% at -55°C, 25°C, 100°C ¹ 100% at 25°C	•	•
Final Inspection	Method 2009	•	•

Note: 1. 100% R&R testing at –55°C, +25°C, and +100°C with all test data included in product shipment.



ORDERING INFORMATION



(1) (2)

Product Series	Nominal Input Voltage		Output	Voltage
DV200-	270	270 Volts	3R3 05 12 15 28	3.3 Volts 5 Volts 12 Volts 15 Volts 28 Volts

(4) (5)

Number o	of Outputs	Screeni	ng Code	Additional Screening Code	
S	Single	None /ML	Standard Military	Contact Sales	

Please contact your sales representative or the VPT Inc. Sales Department for more information concerning additional environmental screening and testing, different input voltage, output voltage, power requirement, source inspection, and/or special element evaluation for space or other higher quality applications.

CONTACT INFORMATION

To request a quotation or place orders please contact your sales representative or the VPT Inc. Sales Department at:

Phone: (425) 487-4850 Fax: (425) 487-4802 E-mail: sales@vpt-inc.com

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