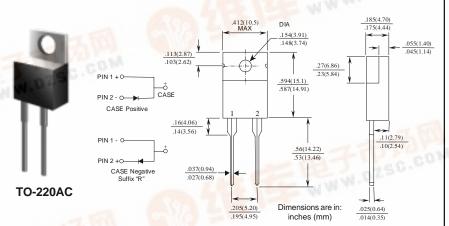


Discrete POWER & Signal Technologies

FES16AT - FES16JT

Features

- Low forward voltage drop.
- High surge current capacity.
- High current capability.
- · High reliability.



16 Ampere Glass Passivated Super Fast Rectifiers

Absolute Maximum Ratings*

 $T_A = 25$ °C unless otherwise noted

Symbol	Parameter	Value	Units		
l _o	Average Rectified Current .375 " lead length @ T _A = 100°C	16	A		
i _{f(surge)}	Peak Forward Surge Current 8.3 ms single half-sine-wave Superimposed on rated load (JEDEC method)	250	W.07A		
P _D	Total Device Dissipation Derate above 25°C	7.81 62	W mW/°C		
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient	16	°C/W		
$R_{\theta JL}$	Thermal Resistance, Junction to Lead	1.2	°C/W		
T _{stg}	Storage Temperature Range	-65 to +150	°C		
TJ	Operating Junction Temperature	-65 to +150	°C		

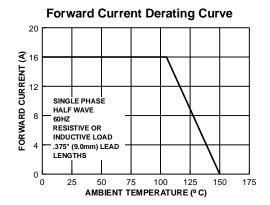
^{*}These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

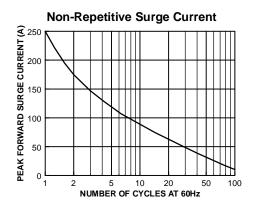
Electrical Characteristics

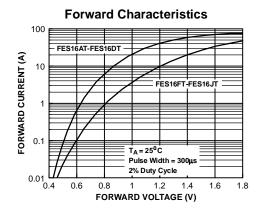
T_A = 25°C unless otherwise noted

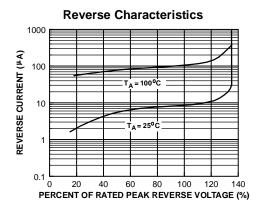
Parameter	Device Device						Units		
	16AT	16BT	16CT	16DT	16FT	16GT	16HT	16JT	
Peak Repetitive Reverse Voltage	50	100	150	200	300	400	500	600	V
Maximum RMS Voltage	35	70	105	140	210	280	350	420	V
DC Blocking Voltage (Rated V _R)	50	100	150	200	300	400	500	600	V
Maximum Reverse Current @ rated V_R $T_A = 25^{\circ}C$ $T_A = 100^{\circ}C$	10 500							μΑ μΑ	
Maximum Reverse Recovery Time $I_F = 0.5 \text{ A}, I_R = 1.0 \text{ A}, I_{RR} = 0.25 \text{ A}$	35 50					50		nS	
Maximum Forward Voltage @ 16.0A	0.975				1.3		.5	V	
Typical Junction Capacitance $V_R = 4.0$. $f = 1.0$ MHz	170 145					pF			

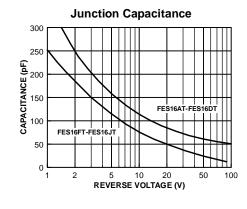
Typical Characteristics

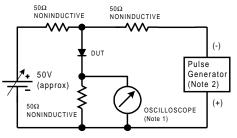


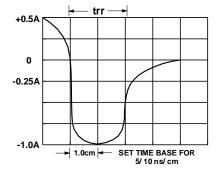












Reverse Recovery Time Characterstic and Test Circuit Diagram

TRADEMARKS

The following are registered and unregistered trademarks Fairchild Semiconductor owns or is authorized to use and is not intended to be an exhaustive list of all such trademarks.

ACEXTM ISOPLANARTM COOIFETTM MICROWIRETM

CROSSVOLTTM POPTM

E²CMOS[™] PowerTrench[™]

FACTTM QSTM

 $\begin{array}{lll} \mathsf{FACT} \ \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} & \mathsf{Quiet} \ \mathsf{Series^{\mathsf{TM}}} \\ \mathsf{FAST}^{\circledcirc} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-}3 \\ \mathsf{FASTr^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-}6 \\ \mathsf{GTO^{\mathsf{TM}}} & \mathsf{SuperSOT^{\mathsf{TM}}}\text{-}8 \\ \mathsf{HiSeC^{\mathsf{TM}}} & \mathsf{TinyLogic^{\mathsf{TM}}} \end{array}$

DISCLAIMER

FAIRCHILD SEMICONDUCTOR RESERVES THE RIGHT TO MAKE CHANGES WITHOUT FURTHER NOTICE TO ANY PRODUCTS HEREIN TO IMPROVE RELIABILITY, FUNCTION OR DESIGN. FAIRCHILD DOES NOT ASSUME ANY LIABILITY ARISING OUT OF THE APPLICATION OR USE OF ANY PRODUCT OR CIRCUIT DESCRIBED HEREIN; NEITHER DOES IT CONVEY ANY LICENSE UNDER ITS PATENT RIGHTS, NOR THE RIGHTS OF OTHERS.

LIFE SUPPORT POLICY

FAIRCHILD'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS WRITTEN APPROVAL OF FAIRCHILD SEMICONDUCTOR CORPORATION. As used herein:

- 1. Life support devices or systems are devices or systems which, (a) are intended for surgical implant into the body, or (b) support or sustain life, or (c) whose failure to perform when properly used in accordance with instructions for use provided in the labeling, can be reasonably expected to result in significant injury to the user.
- 2. A critical component is any component of a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system, or to affect its safety or effectiveness.

PRODUCT STATUS DEFINITIONS

Definition of Terms

Datasheet Identification	Product Status	Definition				
Advance Information	Formative or In Design	This datasheet contains the design specifications for product development. Specifications may change in any manner without notice.				
Preliminary	First Production	This datasheet contains preliminary data, and supplementary data will be published at a later date. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.				
No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.				
Obsolete	Not In Production	This datasheet contains specifications on a product that has been discontinued by Fairchild semiconductor. The datasheet is printed for reference information only.				