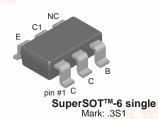


FMBS5551

NPN General Purpose Amplifier

• This device is designed for general purpose high voltage amplifiers WWW.DZSG.GOM and gas discharge display drivers.



Absolute Maximum Ratings* T_a =25°C unless otherwise noted

Symbol	Parameter	Value	Units
V _{CEO}	Collector-Emitter Voltage	160	V
V _{CBO}	Collector-Base Voltage	180	V
V _{EBO}	Emitter-Base Voltage	6.0	V
I _C	Collector Current - Continuous	600	mA
T _J , T _{STG}	Operating and Storage Junction Temperature Range	- 55 ~ 150	°C

^{*} These ratings are limiting values above which the serviceability of any semiconductor device may be impaired.

Electrical Characteristics T_a=25°C unless otherwise noted

Symbol	Parameter	Test Condition	Min.	Max.	Units
Off Characte	eristics	ATTACKED BY	W		
V _{(BR)CEO}	Collector-Emitter Sustaining Voltage *	$I_{\rm C} = 1.0 \rm mA, I_{\rm B} = 0$	160		V
V _{(BR)CBO}	Collector-Base Breakdown Voltage	$I_{C} = 100 \mu A, I_{E} = 0$	180		V
V _{(BR)EBO}	Emitter-Base Breakdown Voltage	$I_E = 10\mu A, I_C = 0$	6.0		V
ГСВО	Collector Cutoff Current	$V_{CB} = 120V, I_{E} = 0$ $V_{CB} = 120V, I_{E} = 0, T_{a} = 100^{\circ}C$		50 50	nA μA
I _{EBO}	Emitter Cut-off Current	$V_{EB} = 4.0 \text{V}, I_{C} = 0$		50	nA
On Characte	eristics		•	•	
h _{FE}	DC Current Gain	I _C = 1.0mA, V _{CE} = 5.0V I _C = 10mA, V _{CE} = 5.0V I _C = 50mA, V _{CE} = 5.0V	80 80 30	250	AN C.C
V _{CE(sat)}	Collector-Emitter Saturation Voltage	$I_C = 10\text{mA}, I_B = 1.0\text{mA}$ $I_C = 50\text{mA}, I_B = 5.0\text{mA}$	WINTS	0.15 0.2	V
V _{BE(sat)}	Base-Emitter Saturation Voltage	I _C = 10mA, I _B = 1.0mA I _C = 50mA, I _B = 5.0mA		1.0 1.0	V
Small Signa	I Characteristics		•		
f _T	Current Gain Bandwidth Product	$I_C = 10 \text{mA}, V_{CE} = 10, f = 100 \text{MHz}$	100	300	MHz
C _{obo}	Output Capacitance	$V_{CE} = 10V, I_{C} = 0, f = 1.0MHz$		6.0	pF
C _{ibo}	Input Capacitance	$V_{BE} = 0.5V, I_{C} = 0, f = 1.0MHz$		20	pF
h _{fe}	Small Single Current Gain	$I_C = 1.0 \text{mA}, V_{CE} = 10 \text{V}, f = 1.0 \text{KHz}$	50	250	
N _F	Noise Figure	$I_C = 250\mu A$, $V_{CE} = 5.0V$, $R_S = 1.0KΩ$, $f = 10$ Hz to 15.7KHz		8.0	dB

^{*} Pulse Test: Pulse Width ≤ 300μs, Duty Cycle ≤ 2.0%

These ratings are based on a maximum junction temperature of 150 degrees C.
These are steady state limits. The factory should be consulted on applications involving pulsed or low duty cycle operations.

Symbol	Parameter	Max.	Units
P_{D}	Total Device Dissipation *	700	mW
$R_{\theta JA}$	Thermal Resistance, Junction to Ambient, total	180	°C/W

^{*} Device mounted on a 1 in 2 pad of 2 oz copper.

Typical Characteristics

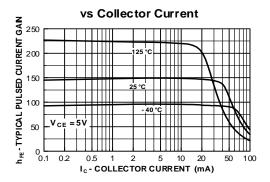


Figure 1. Typical Pulsed Current Gain vs Collector Current

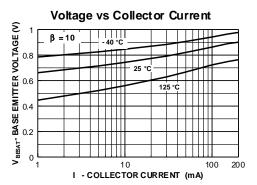


Figure 3. Base-Emitter Saturation Voltage vs Collector Current

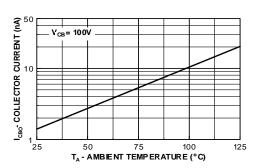


Figure 5. Collector Cutoff Current vs Ambient Temperature

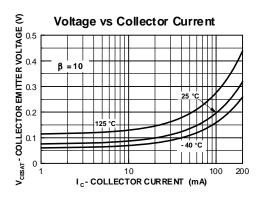


Figure 2. Collector-Emitter Saturation Voltage vs Collector Current

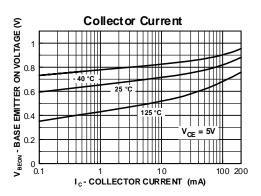


Figure 4. Base-Emitter On Voltage vs Collector Current

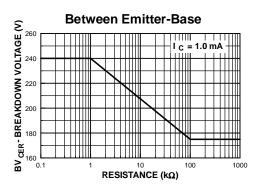


Figure 6. Collector-Emitter Breakdown Voltage with Resistance Between Emitter-Base

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Typical Characteristics (Continued)

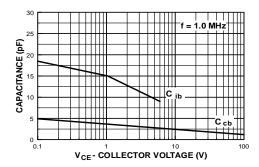


Figure 7. Input and Output Capacitance vs Reverse Voltage

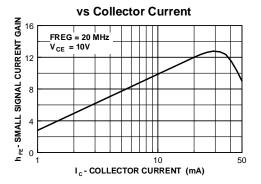
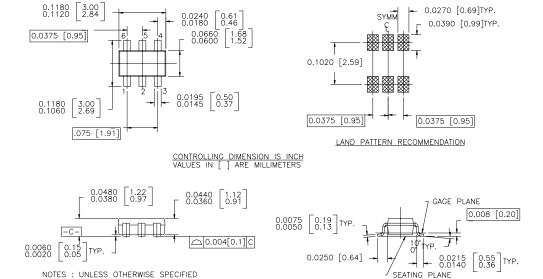


Figure 8. Small Signal current Gain vs Collector Current

Package Dimensions

SuperSOT™-6



SUPER SOT 6 LEADS

1.0 STANDARD LEAD FINISH: 150 MICROINCHES 93.81 MICROMETERS) MINIMUM TIN / LEAD (SOLDER) ON COPPER.

2.0 NO JEDEC REGISTRATION AS OF JULY 1996

Dimensions in Millimeters

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$CROSSVOLT^{TM}$	GlobalOptoisolator™	MicroPak™	QFET [®]	SuperSOT™-8
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EnSigna™	i-Lo™	OCX TM	RapidConfigure™	TruTranslation™
FACT™	ImpliedDisconnect™	OCXPro™	RapidConnect™	UHC™
FACT Quiet Series™	1	OPTOLOGIC [®]	μSerDes™	UltraFET [®]
Across the board. Around the world.™		OPTOPLANAR™	SILENT SWITCHER®	VCX TM
The Power Franchise®		PACMAN™	SMART START™	
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Definition of Terms

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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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