



QFET™

FQP10N20C/FQPF10N20C

200V N-Channel MOSFET

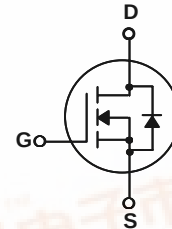
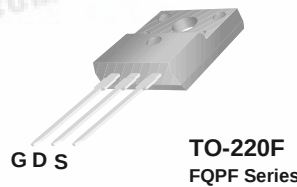
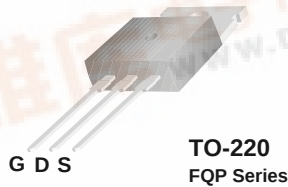
General Description

These N-Channel enhancement mode power field effect transistors are produced using Fairchild's proprietary, planar stripe, DMOS technology.

This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switching DC/DC converters, switch mode power supplies, DC-AC converters for uninterrupted power supplies and motor controls.

Features

- 9.5A, 200V, $R_{DS(on)} = 0.36\Omega$ @ $V_{GS} = 10V$
- Low gate charge (typical 20 nC)
- Low C_{rss} (typical 40.5 pF)
- Fast switching
- 100% avalanche tested
- Improved dv/dt capability



Absolute Maximum Ratings $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	FQP10N20C	FQPF10N20C	Units
V_{DSS}	Drain-Source Voltage	200		V
I_D	Drain Current - Continuous ($T_C = 25^\circ\text{C}$)	9.5	9.5 *	A
	- Continuous ($T_C = 100^\circ\text{C}$)	6.0	6.0 *	A
I_{DM}	Drain Current - Pulsed (Note 1)	38	38 *	A
V_{GSS}	Gate-Source Voltage	± 30		V
E_{AS}	Single Pulsed Avalanche Energy (Note 2)	210		mJ
I_{AR}	Avalanche Current (Note 1)	9.5		A
E_{AR}	Repetitive Avalanche Energy (Note 1)	7.2		mJ
dv/dt	Peak Diode Recovery dv/dt (Note 3)	5.5		V/ns
P_D	Power Dissipation ($T_C = 25^\circ\text{C}$)	72	38	W
	- Derate above 25°C	0.57	0.3	W/ $^\circ\text{C}$
T_J, T_{STG}	Operating and Storage Temperature Range	-55 to +150		$^\circ\text{C}$
T_L	Maximum lead temperature for soldering purposes, 1/8" from case for 5 seconds	300		$^\circ\text{C}$

* Drain current limited by maximum junction temperature.

Thermal Characteristics

Symbol	Parameter	FQP10N20C	FQPF10N20C	Units
$R_{\theta JC}$	Thermal Resistance, Junction-to-Case	1.74	3.33	$^\circ\text{C/W}$
$R_{\theta JS}$	Thermal Resistance, Case-to-Sink Typ.	0.5	--	$^\circ\text{C/W}$
$R_{\theta JA}$	Thermal Resistance, Junction-to-Ambient	62.5	62.5	$^\circ\text{C/W}$



Electrical Characteristics $T_C = 25^\circ\text{C}$ unless otherwise noted

Symbol	Parameter	Test Conditions	Min	Typ	Max	Units
Off Characteristics						
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} = 0 V, I _D = 250 μA	200	--	--	V
ΔBV _{DSS} / ΔT _J	Breakdown Voltage Temperature Coefficient	I _D = 250 μA, Referenced to 25°C	--	0.28	--	V/°C
I _{DSS}	Zero Gate Voltage Drain Current	V _{DS} = 200 V, V _{GS} = 0 V V _{DS} = 160 V, T _C = 125°C	--	--	10 100	μA
I _{GSSF}	Gate-Body Leakage Current, Forward	V _{GS} = 30 V, V _{DS} = 0 V	--	--	100	nA
I _{GSSR}	Gate-Body Leakage Current, Reverse	V _{GS} = -30 V, V _{DS} = 0 V	--	--	-100	nA
On Characteristics						
V _{GS(th)}	Gate Threshold Voltage	V _{DS} = V _{GS} , I _D = 250 μA	2.0	--	4.0	V
R _{DS(on)}	Static Drain-Source On-Resistance	V _{GS} = 10 V, I _D = 4.75 A	--	0.29	0.36	Ω
g _{FS}	Forward Transconductance	V _{DS} = 40 V, I _D = 4.75 A (Note 4)	--	5.5	--	S
Dynamic Characteristics						
C _{iss}	Input Capacitance	V _{DS} = 25 V, V _{GS} = 0 V, f = 1.0 MHz	--	395	510	pF
C _{oss}	Output Capacitance		--	97	125	pF
C _{rss}	Reverse Transfer Capacitance		--	40.5	53	pF
Switching Characteristics						
t _{d(on)}	Turn-On Delay Time	V _{DD} = 100 V, I _D = 9.5 A, R _G = 25 Ω (Note 4, 5)	--	11	30	ns
t _r	Turn-On Rise Time		--	92	190	ns
t _{d(off)}	Turn-Off Delay Time		--	70	150	ns
t _f	Turn-Off Fall Time		--	72	160	ns
Q _g	Total Gate Charge	V _{DS} = 160 V, I _D = 9.5 A, V _{GS} = 10 V (Note 4, 5)	--	20	26	nC
Q _{gs}	Gate-Source Charge		--	3.1	--	nC
Q _{gd}	Gate-Drain Charge		--	10.5	--	nC
Drain-Source Diode Characteristics and Maximum Ratings						
I _S	Maximum Continuous Drain-Source Diode Forward Current		--	--	9.5	A
I _{SM}	Maximum Pulsed Drain-Source Diode Forward Current		--	--	38	A
V _{SD}	Drain-Source Diode Forward Voltage	V _{GS} = 0 V, I _S = 9.5 A	--	--	1.5	V
t _{rr}	Reverse Recovery Time	V _{GS} = 0 V, I _S = 9.5 A, dI _F / dt = 100 A/μs (Note 4)	--	158	--	ns
Q _{rr}	Reverse Recovery Charge		--	0.97	--	μC

Notes:

1. Repetitive Rating : Pulse width limited by maximum junction temperature
2. $L = 3.5\text{ mH}, I_{AS} = 9.5\text{ A}, V_{DD} = 50\text{ V}, R_G = 25\text{ }\Omega$, Starting $T_J = 25^\circ\text{C}$
3. $I_{SD} \leq 9.5\text{ A}, di/dt \leq 300\text{ A}/\mu\text{s}, V_{DD} \leq BV_{DSS}$, Starting $T_J = 25^\circ\text{C}$
4. Pulse Test : Pulse width $\leq 300\mu\text{s}$, Duty cycle $\leq 2\%$
5. Essentially independent of operating temperature

Typical Characteristics

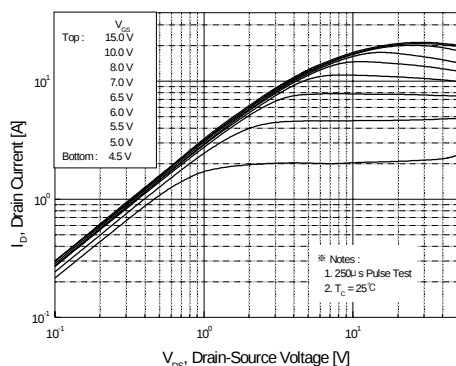


Figure 1. On-Region Characteristics

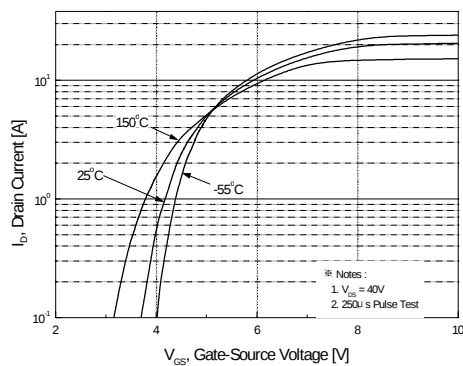


Figure 2. Transfer Characteristics

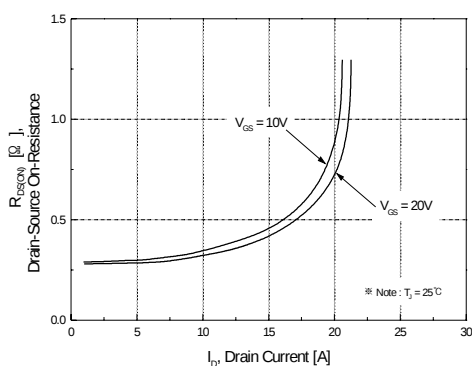


Figure 3. On-Resistance Variation vs Drain Current and Gate Voltage

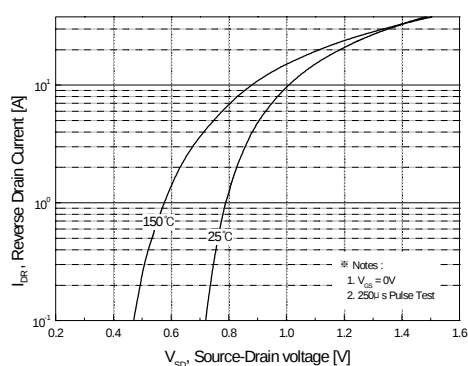


Figure 4. Body Diode Forward Voltage Variation with Source Current and Temperature

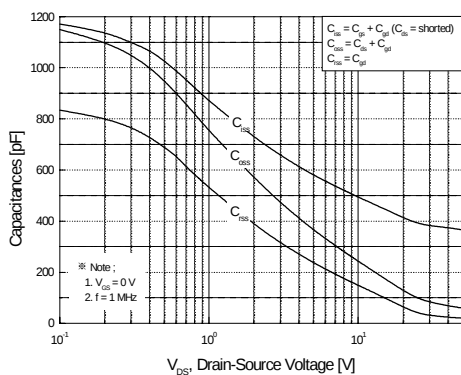


Figure 5. Capacitance Characteristics

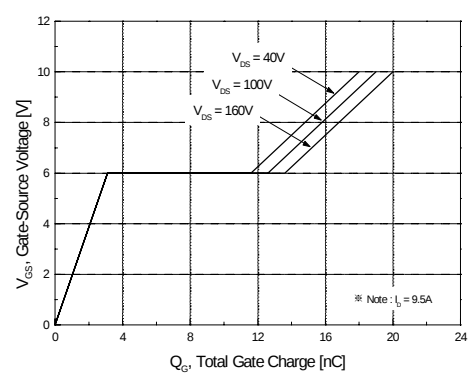


Figure 6. Gate Charge Characteristics

Typical Characteristics (Continued)

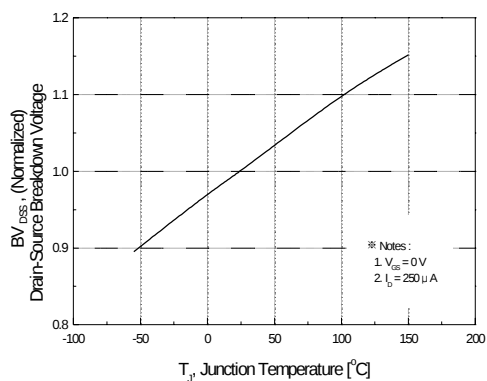


Figure 7. Breakdown Voltage Variation vs Temperature

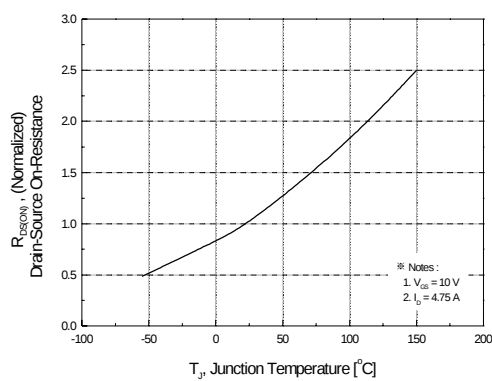


Figure 8. On-Resistance Variation vs Temperature

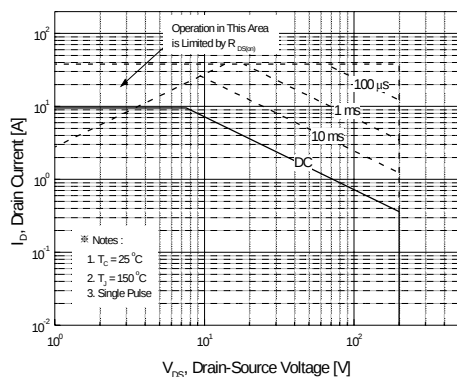


Figure 9-1. Maximum Safe Operating Area for FQP10N20C

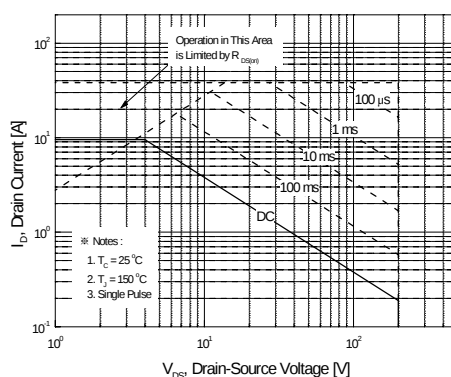


Figure 9-2. Maximum Safe Operating Area for FQPF10N20C

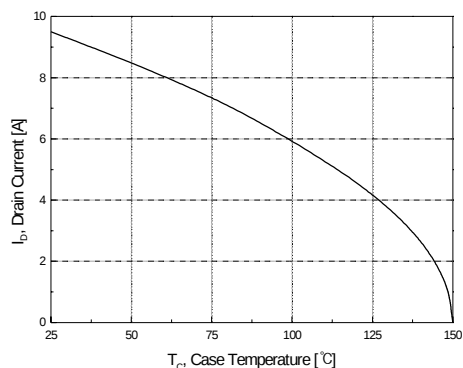


Figure 10. Maximum Drain Current vs Case Temperature

Typical Characteristics (Continued)

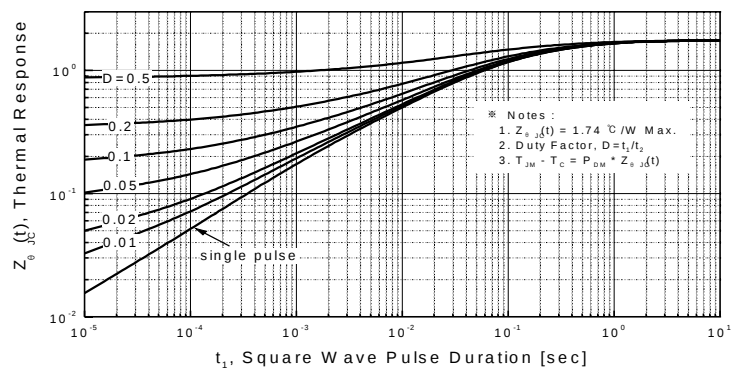


Figure 11-1. Transient Thermal Response Curve for FQP10N20C

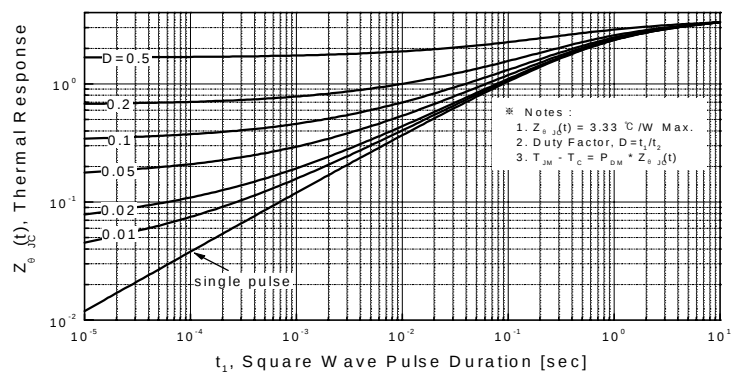
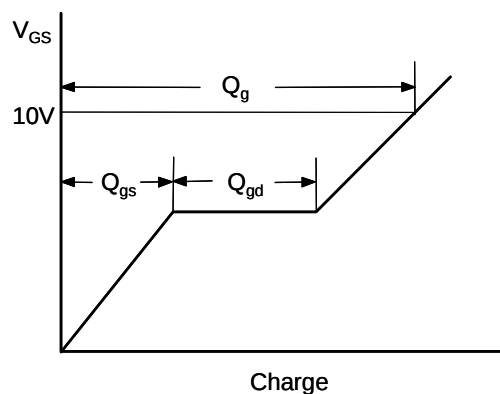
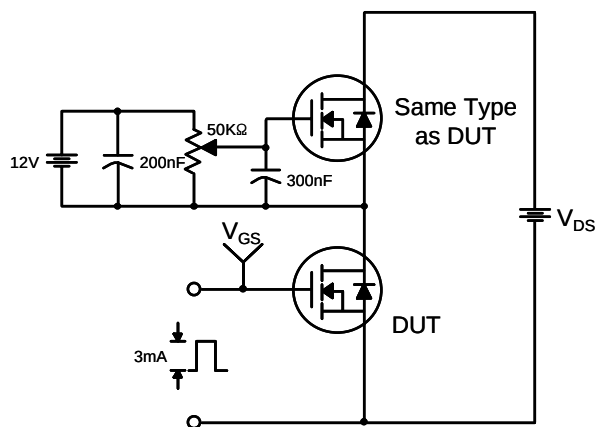
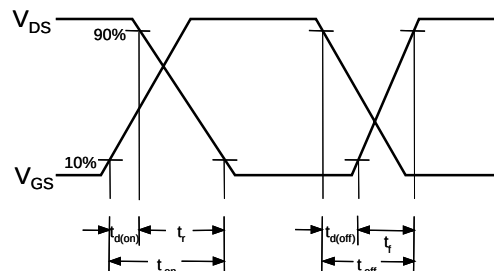
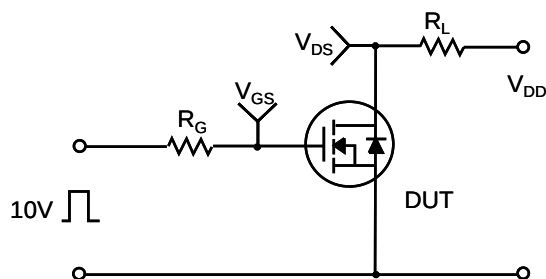


Figure 11-2. Transient Thermal Response Curve for FQPF10N20C

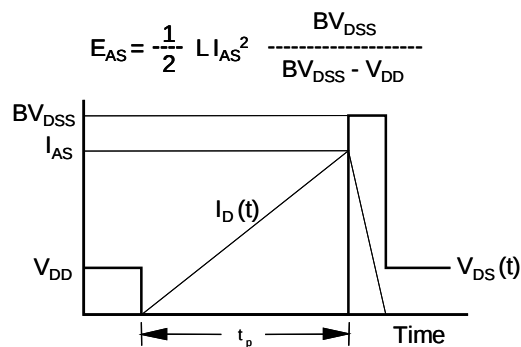
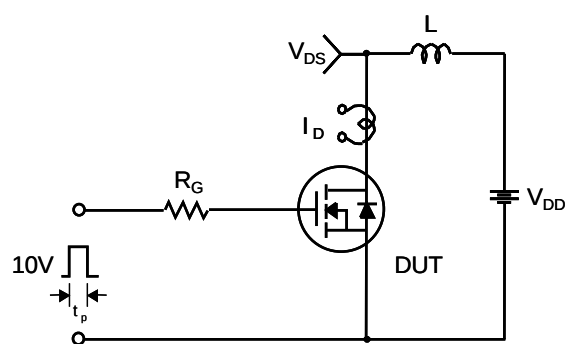
Gate Charge Test Circuit & Waveform



Resistive Switching Test Circuit & Waveforms

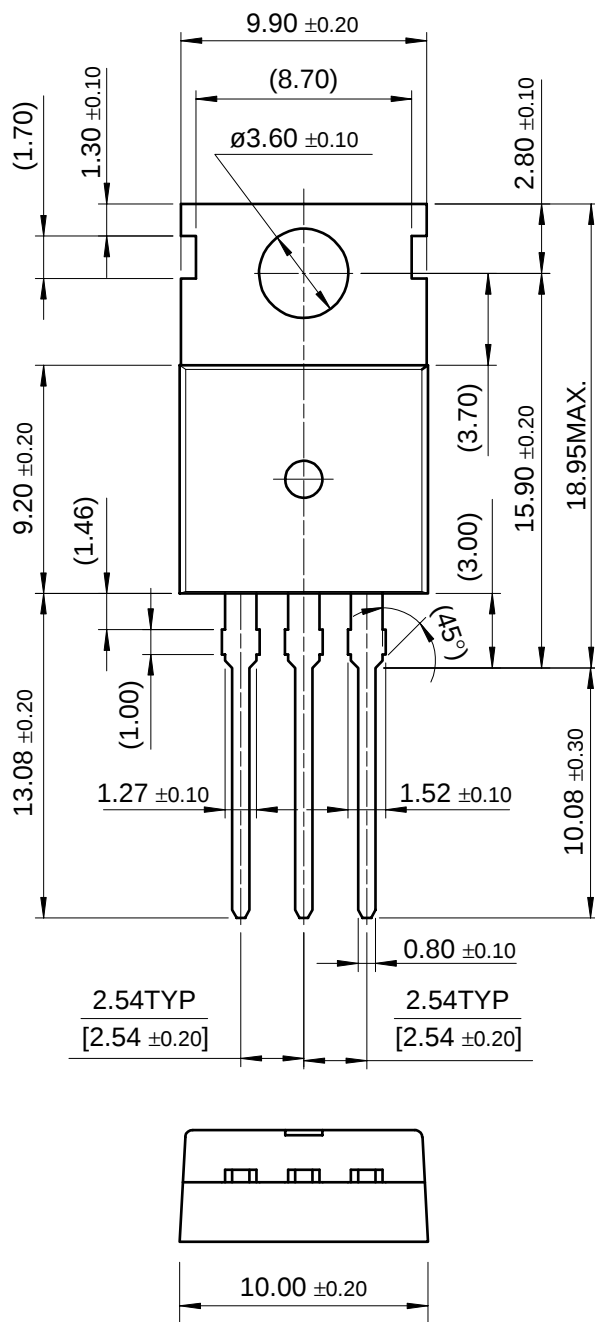


Unclamped Inductive Switching Test Circuit & Waveforms



Package Dimensions

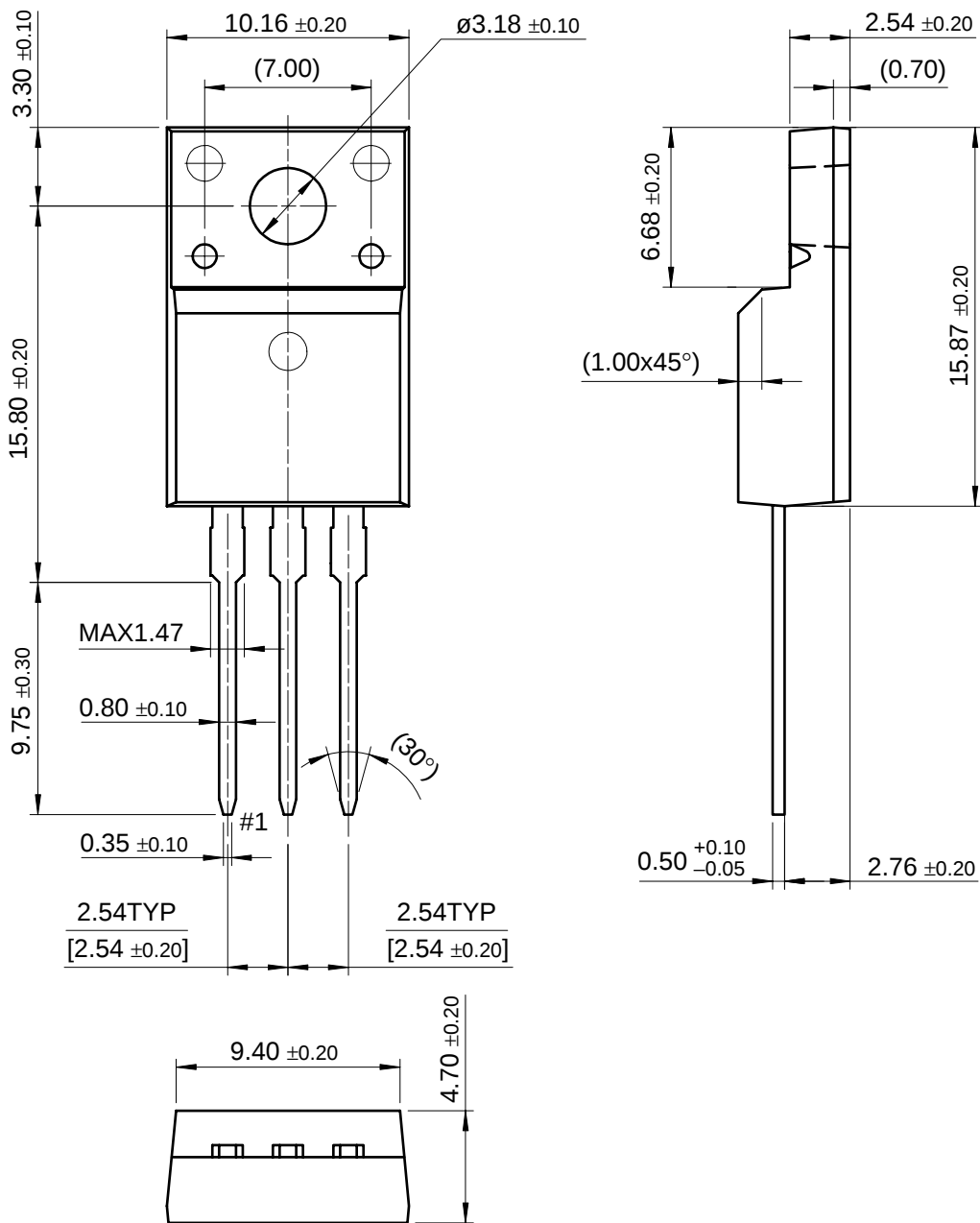
TO-220



Dimensions in Millimeters

Package Dimensions (Continued)

TO-220F



Dimensions in Millimeters

FQP10N20C/FQPF10N20C

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CoolFET™	FASTr™	MicroFET™	PowerTrench®	SuperSOT™-6
CROSSVOLT™	FRFET™	MicroPak™	QFET™	SuperSOT™-8
DOME™	GlobalOptoisolator™	MICROWIRE™	QS™	SyncFET™
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No Identification Needed	Full Production	This datasheet contains final specifications. Fairchild Semiconductor reserves the right to make changes at any time without notice in order to improve design.
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