



FR101SG THRU FR107SG

1.0 AMP. Glass Passivated Fast Recovery Rectifiers



Voltage Range
50 to 1000 Volts
Current
1.0 Ampere

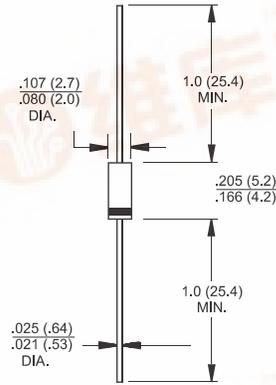
Features

- ✧ Low forward voltage drop
- ✧ High current capability
- ✧ High reliability
- ✧ High surge current capability

Mechanical Data

- ✧ Cases: Molded plastic
- ✧ Epoxy: UL 94V-0 rate flame retardant
- ✧ Lead: Axial leads, solderable per MIL-STD-202, Method 208 guaranteed
- ✧ Polarity: Color band denotes cathode end
- ✧ High temperature soldering guaranteed: 260°C/10 seconds/.375", (9.5mm) lead lengths at 5lbs., (2.3kg) tension
- ✧ Mounting position: Any
- ✧ Weight: 0.22gram

A-405



Dimensions in inches and (millimeters)

Maximum Ratings and Electrical Characteristics

Rating at 25°C ambient temperature unless otherwise specified.

Single phase, half wave, 60 Hz, resistive or inductive load.

For capacitive load, derate current by 20%

Type Number	Symbol	FR 101SG	FR 102SG	FR 103SG	FR 104SG	FR 105SG	FR 106SG	FR 107SG	Units
Maximum Recurrent Peak Reverse Voltage	V_{RRM}	50	100	200	400	600	800	1000	V
Maximum RMS Voltage	V_{RMS}	35	70	140	280	420	560	700	V
Maximum DC Blocking Voltage	V_{DC}	50	100	200	400	600	800	1000	V
Maximum Average Forward Rectified Current .375" (9.5mm) Lead Length @ $T_A = 55^\circ C$	$I_{(AV)}$	1.0							V
Peak Forward Surge Current, 8.3 ms Single Half Sine-wave Superimposed on Rated Load (JEDEC method)	I_{FSM}	30							A
Maximum Instantaneous Forward Voltage @ 1.0A	V_F	1.3							V
Maximum DC Reverse Current @ $T_A=25^\circ C$ at Rated DC Blocking Voltage @ $T_A=125^\circ C$	I_R	5.0 100							μA μA
Maximum Reverse Recovery Time (Note 1)	T_{rr}	150				250	500		nS
Typical Junction Capacitance (Note 2)	C_j	15							pF
Operating Temperature Range	T_J	-65 to +150							$^\circ C$
Storage Temperature Range	T_{STG}	-65 to +150							$^\circ C$

Notes: 1. Reverse Recovery Test Conditions: $I_F=0.5A$, $I_R=1.0A$, $I_{RR}=0.25A$

2. Measured at 1 MHz and Applied Reverse Voltage of 4.0 Volts D.C.



RATINGS AND CHARACTERISTIC CURVES (FR101SG THRU FR107SG)

FIG. 1- REVERSE RECOVERY TIME CHARACTERISTIC AND TEST CIRCUIT DIAGRAM

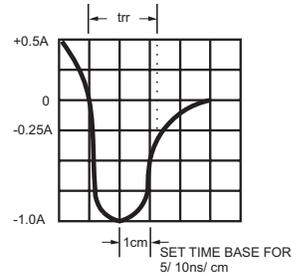
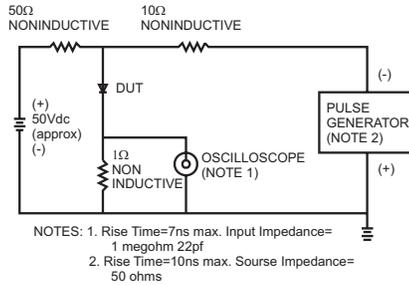


FIG. 2- MAXIMUM FORWARD CURRENT DERATING CURVE

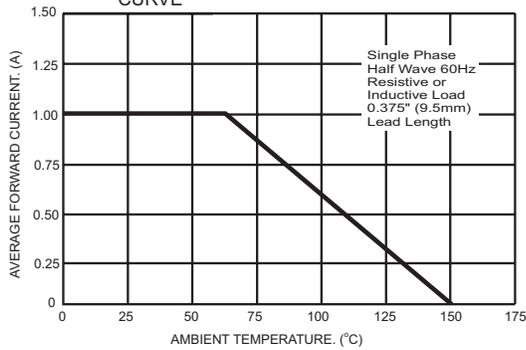


FIG. 3- MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

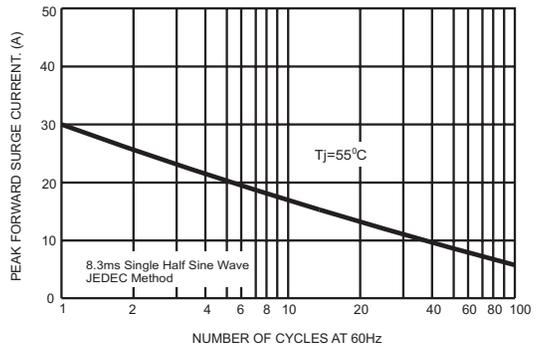


FIG. 4- TYPICAL JUNCTION CAPACITANCE

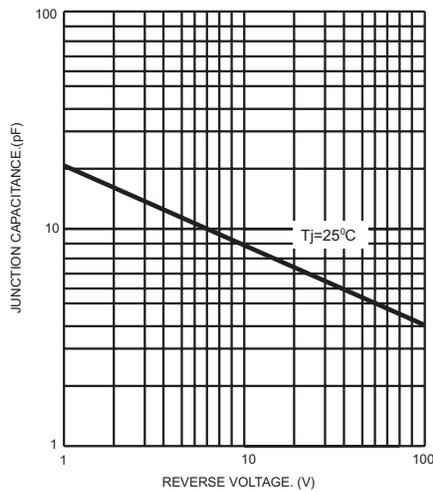


FIG. 5- TYPICAL FORWARD CHARACTERISTICS

