HMC439QS16G

HBT DIGITAL PHASE-FREQUENCY DETECTOR, 10 - 1300 MHz

Typical Applications

This Phase Frequency Detector is a key component in low phase noise frequency synthesis applications such as:

- Pt - Pt Radios
- Satellite Communication Systems
- Military Applications
- Sonet Clock Generation

Features

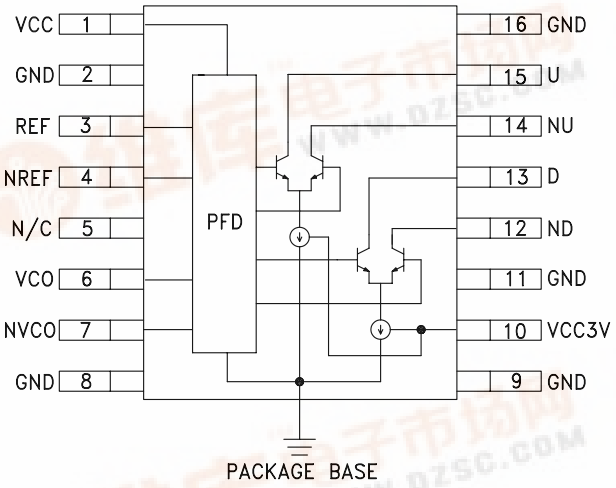
Ultra Low SSB Phase Noise Floor:
-153 dBc/Hz@10 kHz offset @ 100 MHz
Input up to 1300 MHz Fin.

Differential Input/Single Ended Output

Open Collector Output Buffer Amplifiers

QSOP16G SMT Package: 29.4 mm^2

Functional Diagram



General Description

The HMC439QS16G is a digital phase-frequency detector intended for use in low noise phase-locked loop applications for inputs from 10 to 1300 MHz. Its combination of high frequency of operation along with its ultra low phase noise floor make possible synthesizers with wide loop bandwidth and low N resulting in fast switching and very low phase noise. When used in conjunction with a differential loop amplifier, the HMC439QS16G generates an output voltage that can be used to phase lock a VCO to a reference oscillator. The device is packaged in a low cost, surface mount 16 lead QSOP package with an exposed base for improved RF and thermal performance.

Electrical Specifications, T_A = +25° C, Vcc= 5V

Parameter	Conditions	Min.	Typ.	Max.	Units
Maximum Input Frequency		1300			MHz
Minimum Input Frequency	Sine Wave Input			10	MHz
Input Power Range	Fin= 10 to 1300 MHz	-10		+10	dBm
Output Voltage			2000		mV, Pk - Pk
SSB Phase Noise	@ 10 kHz Offset with 100 MHz Input & Pin= 0 dBm		-153		dBc/Hz
Supply Current (Icc)			96		mA

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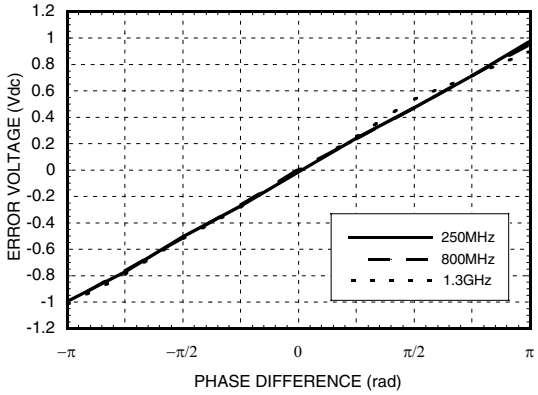
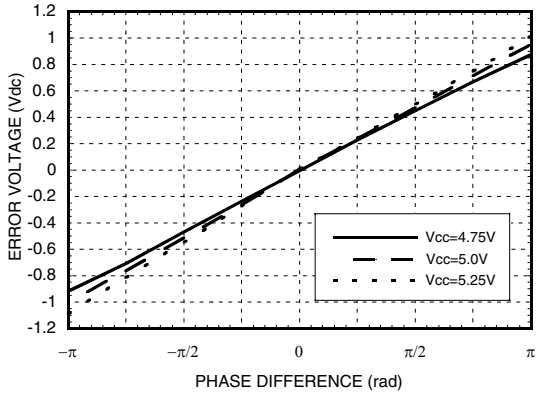
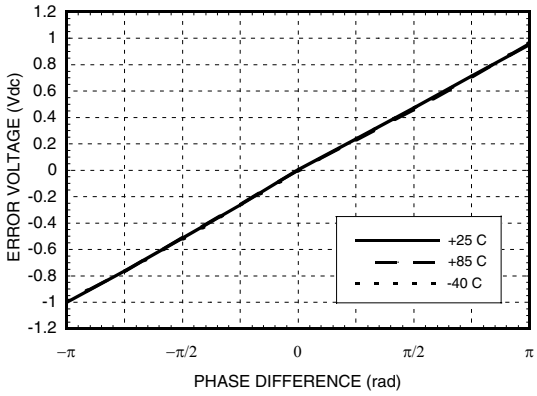
Error Voltage vs. Frequency, Pin= 0 dBm*



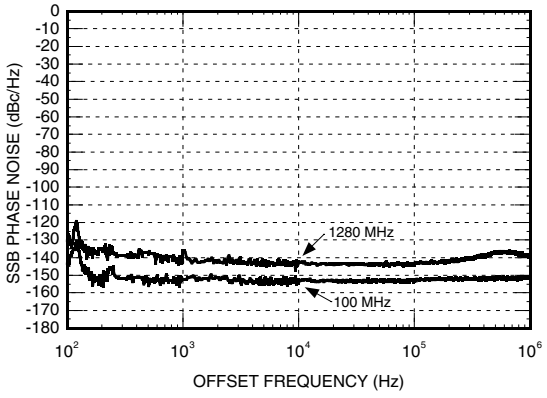
Error Voltage vs. Supply Voltage, Pin= 0 dBm, Fin= 250 MHz*



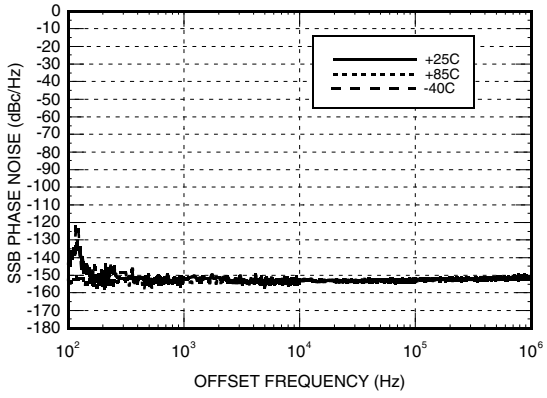
Error Voltage vs. Temperature, Pin= 0 dBm, Fin= 250 MHz*



SSB Phase Noise Performance, Pin= 0 dBm, T= 25 °C



SSB Phase Noise Performance, Pin= 0 dBm, Fin= 100 MHz



** See Gain & Error Voltage Test Circuit herein.*

v01.0503

HMC439QS16G

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Absolute Maximum Ratings

RF Input (Vcc= +5V)	+13 dBm
Supply Voltage (Vcc)	+5.5V
Channel Temperature (Tc)	135 °C
Continuous Pdiss (T = 85 °C) (derate 47.2 mW/° C above 85 °C)	4.25 W
Storage Temperature	-65 to +150 °C
Operating Temperature	-40 to +85 °C

Typical Supply Current vs. Vcc

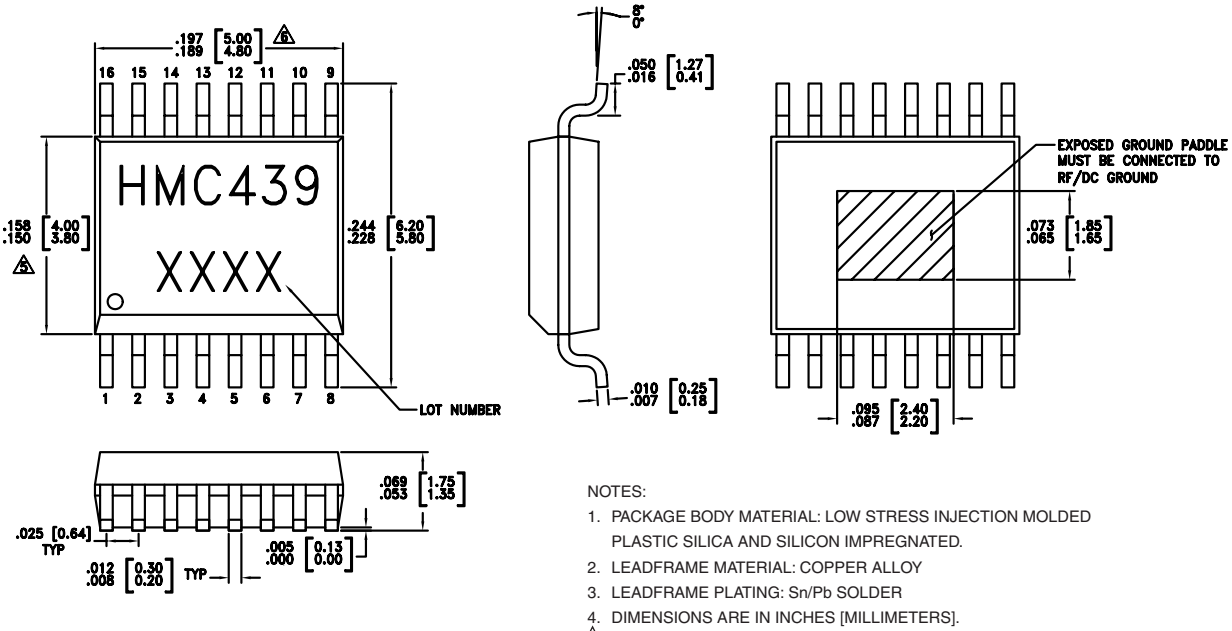
Vcc (Vdc)	Icc (mA)
4.8	90
5.0	96
5.2	102

Note: Detector will work over full voltage range above.

Typical DC Characteristics @ Vcc = +5V

Symbol	Characteristics	+25C			Units
		Min.	Typ.	Max.	
Icc	Power Supply Current	90	96	102	mA
Voh	Output High Voltage	5.0	5.0	5.0	V
Vol	Output Low Voltage	2.9	3	3.1	V

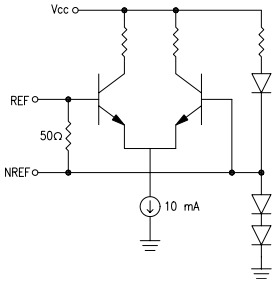
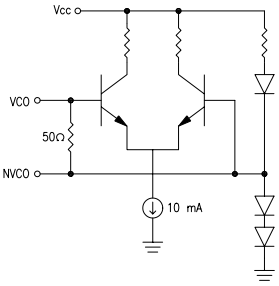
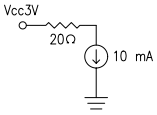
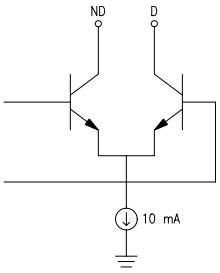
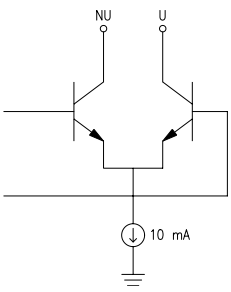
Outline Drawing



NOTES:

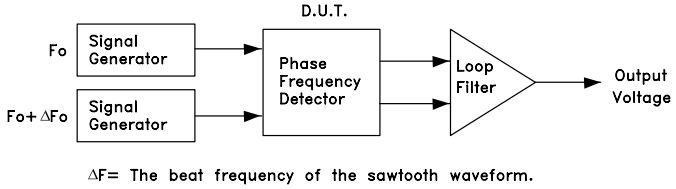
1. PACKAGE BODY MATERIAL: LOW STRESS INJECTION MOLDED PLASTIC SILICA AND SILICON IMPREGNATED.
2. LEADFRAME MATERIAL: COPPER ALLOY
3. LEADFRAME PLATING: Sn/Pb SOLDER
4. DIMENSIONS ARE IN INCHES [MILLIMETERS].
5. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.15mm PER SIDE.
6. DIMENSION DOES NOT INCLUDE MOLDFLASH OF 0.25mm PER SIDE.
7. ALL GROUND LEADS AND GROUND PADDLE MUST BE SOLDERED TO PCB RF GROUND.

Pin Description

Pin Number	Function	Description	Interface Schematic
1	Vcc	Supply voltage 5V ± 0.2V	
2, 8, 9, 11, 16	GND	All ground leads and ground paddle must be connected to PCB RF/DC ground.	
3	REF	(These pins are AC coupled and must be DC blocked externally.) Reference Input	
4	NREF	Reference Input Compliment	
5	N/C	Not Connected	
6	VCO	(These pins are AC coupled and must be DC blocked externally.) VCO Input	
7	NVCO	VCO Input Compliment	
10	Vcc3V	3.0 Volt Reference Voltage for Internal 10mA Current Source	
12	ND	Down Output Compliment	
13	D	Down Output	
14	NU	Up Output Compliment	
15	U	Up Output	

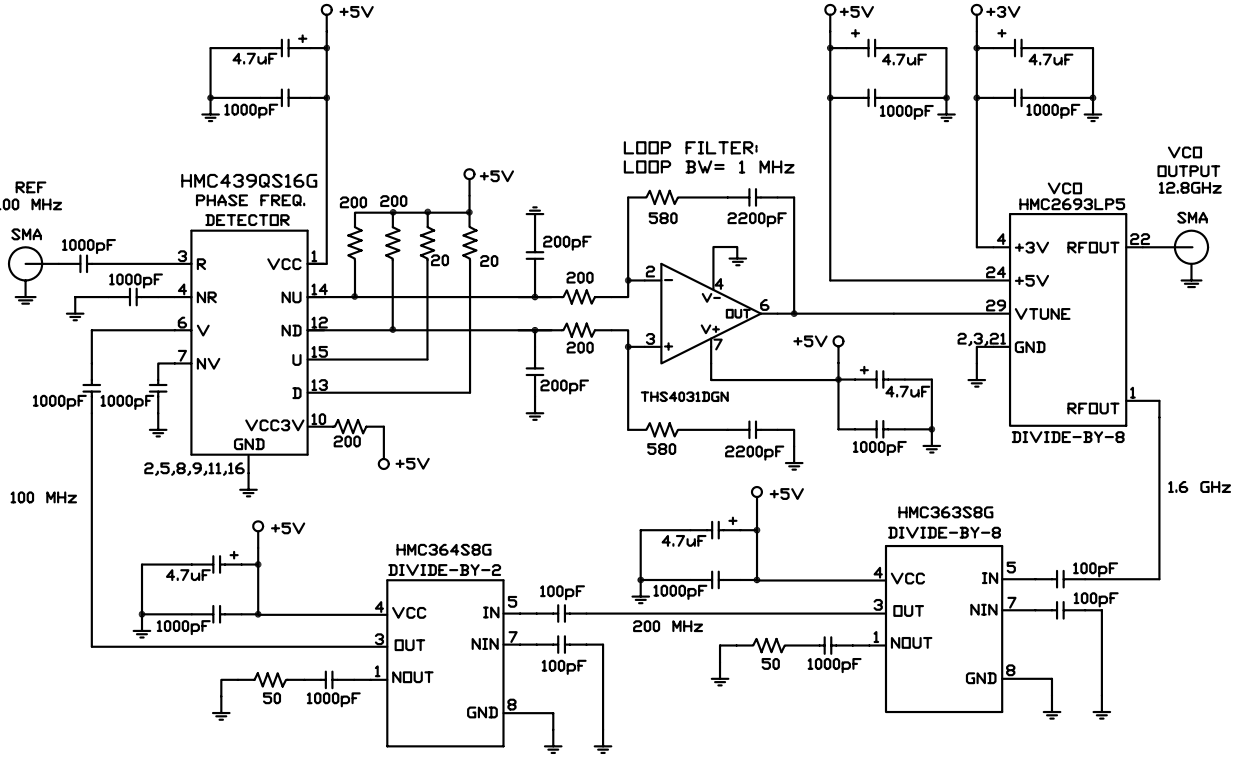
Gain & Error Voltage Test Circuit:

Gain & Error Voltage data taken using test circuit below. Loop filter gain has been subtracted from the result.

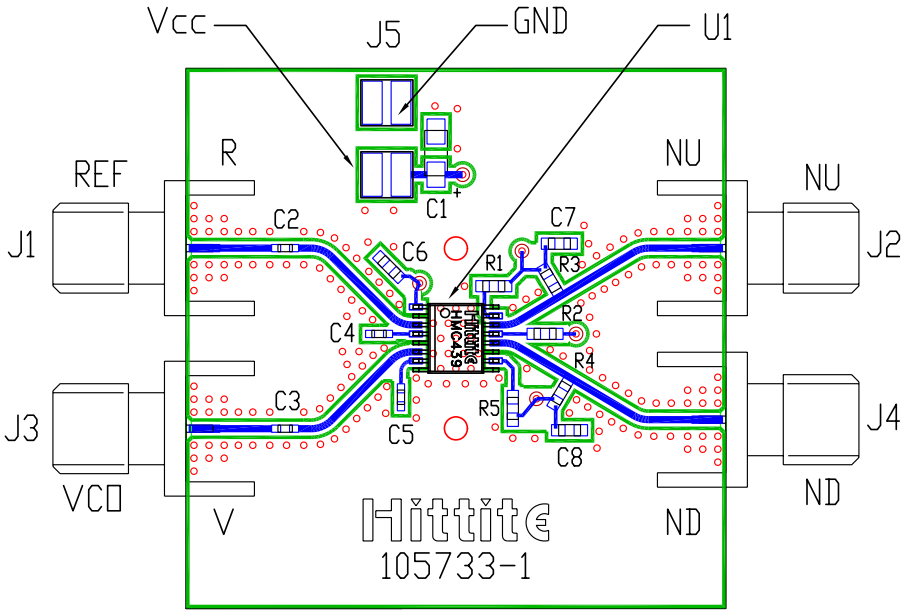


Typical PLL Application Circuit using HMC439QS16G

PLL application shown for a 12.8 GHz Fout. Contact HMC to discuss your specific application.



Evaluation PCB

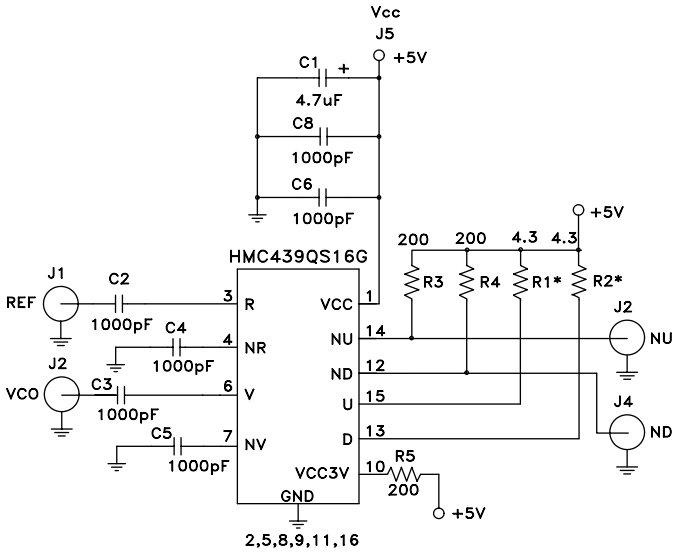


The circuit board used in the final application should use RF circuit design techniques. Signal lines should have 50 ohm impedance while the package ground leads and backside ground slug should be connected directly to the ground plane similar to that shown. A sufficient number of via holes should be used to connect the top and bottom ground planes. The evaluation circuit board shown is available from Hittite upon request.

List of Material

Item	Description
J1 - J4	PC Mount SMA RF Connector
J5	2 mm DC Header
C1	4.7 µF Capacitor
C2, C5 - C7	100 pF Capacitor, 0402 Pkg.
C3, C4, C8	1000 pF Capacitor, 0603 Pkg.
R1*, R2*	4.3 Ohm Resistor, 0603 Pkg.
R3, R4, R5	200 Ohm Resistor, 0603 Pkg.
U1	HMC439QS16G
PCB**	105733 Eval Board
** Circuit Board Material: Rogers 4350	

Evaluation PCB Circuit



** Choose values of R1 & R2 between 4.3 and 20 Ohms for best noise performance.*