



PC Card (PCMCIA) Interface Switch

FEATURES

- Single SO-16 Package
- CMOS Inputs with Hysteresis
- Extremely Low R_{ON}
- Reverse Blocking Switches
- HiZ Outputs in the Off-State
- Low Power Consumption
- Safe Power-Up

DESCRIPTION

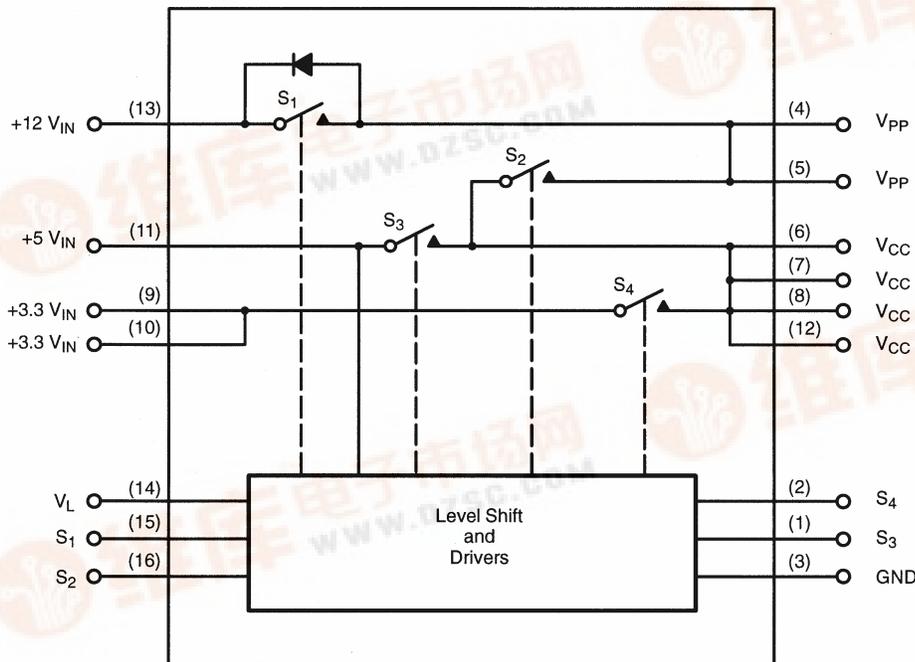
The Si9711CY is a monolithic switch designed to meet the needs of the PC Card interface. The inputs are fully CMOS compatible and incorporate all the level shift and interface required to be driven by any CMOS driver. The external inputs can be driven to 3.3-V or 5-V by setting V_L at the appropriate level. The switches are low R_{ON} and can carry the maximum currents found on the PC Card interface.

The 5-V and 3.3-V switches do not have the parasitic diode found in vertical DMOS power switches.

Low R_{ON} is achieved by using MOSFETs driven off the +12- V_{IN} input. All level shifting is built into the Si9711CY.

The Si9711CY is packaged in an SO-16 package and is rated over the commercial temperature range 0 to 70°C.

FUNCTIONAL BLOCK DIAGRAM



TRUTH TABLE: S ₁ - S ₄	
Logic	Switch
0	OFF
1	ON





SPECIFICATIONS							
Parameter	Symbol	Test Conditions Unless Otherwise Specified +5 V _{IN} = 5 V, +3.3 V _{IN} = 3.3 V +12 V _{IN} = 12 V, V _L = 5.0 V, GND = 0 V		Limits C Suffix, 0 to 70°C			Unit
				Min ^a	Typ	Max ^a	
Switch 4							
On-Resistance	R _{ON}	I = 500 mA, +12 V _{IN} = 10.8 V S ₄ = V _L	T _A = 25°C			150	mΩ
			T _A = 70°C			185	
Off Current	I _{OFF}	+3.3 V _{IN} = 3.6 V, V _{CC} = 0 V S ₂ = S ₃ = S ₄ = GND	T _A = 25°C			1	μA
			T _A = 70°C			10	
Switching Time	t _{d(on)}	+3.3 V _{IN} = 3.3 V, C _L = 0.1 μF, S ₃ = GND R _L = 100 Ω, See Figure 2.		1			μs
	t _{ramp(on)}			200			
	t _{S4(off)}			0.5		4	
Power Supply							
+12 V _{IN} Current	I _{+12VIN(1)}	S ₁ = S ₄ = GND, S ₂ = S ₃ = V _L				10	μA
	I _{+12VIN(2)}	S ₁ = S ₄ = V _L , S ₂ = S ₃ = GND				10	
V _L Current	I _{VL(1)}	S ₁ = S ₄ = GND, S ₂ = S ₃ = V _L				10	
	I _{VL(2)}	S ₁ = S ₄ = V _L , S ₂ = S ₃ = GND				10	
Switch Control Inputs							
Input Voltage High	V _{I(H)}		V _L = 3.3 V	2.8	2.4		V
			V _L = 5 V	4.0	3.3		
Input Voltage Low	V _{I(L)}		V _L = 3.3 V		1.1	0.4	
			V _L = 5 V		1.5	0.8	
Input Hysteresis ^b	V _{I(H)} - V _{I(L)}		V _L = 3.3 V	0.5	1.3		
			V _L = 5 V	0.8	1.8		
Input Current High	I _{I(H)}	S ₁ through S ₄ = V _L , V _L = 5 V				1.0	μA
Input Current Low	I _{I(L)}	S ₁ through S ₄ = GND, V _L = 5 V	-1.0				

Notes

- a. The algebraic convention whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- b. Guaranteed by design, not subject to production testing.

Si9711CY

Vishay Siliconix



TIMING WAVEFORMS

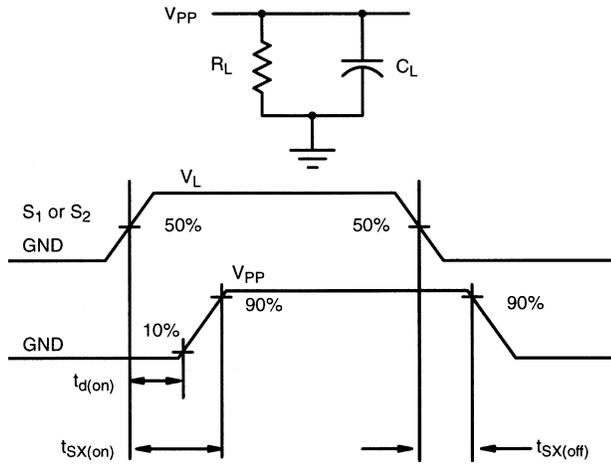


FIGURE 1. $t_{d(on)}$ and $t_{SX(on)}$

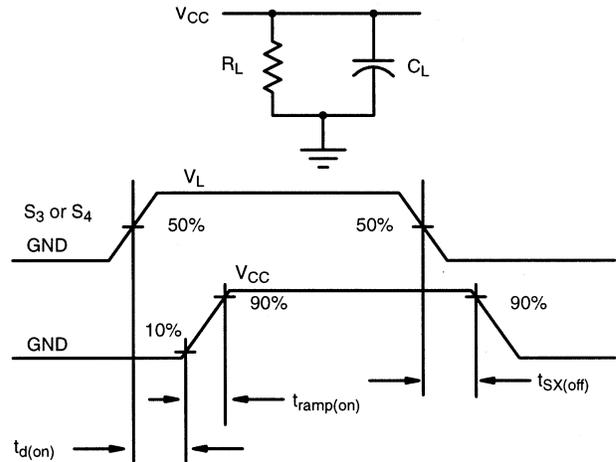
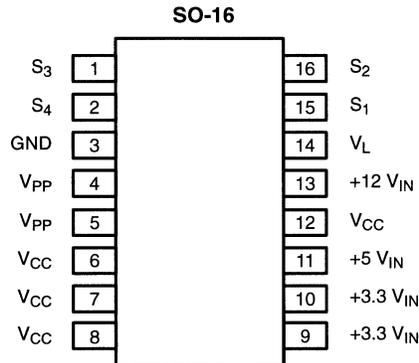


FIGURE 2. $t_{ramp(on)}$

PIN CONFIGURATION



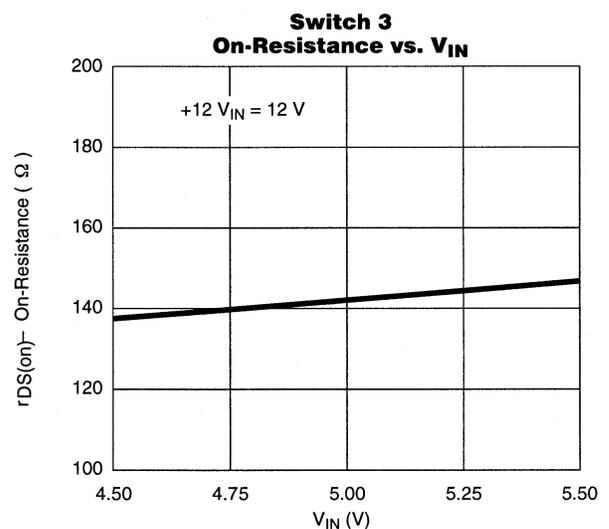
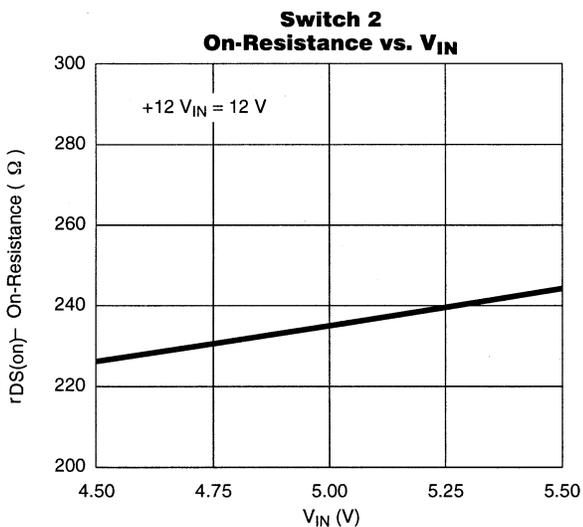
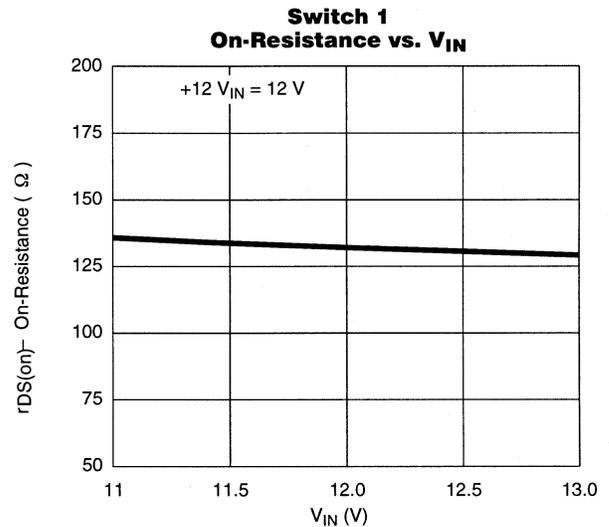
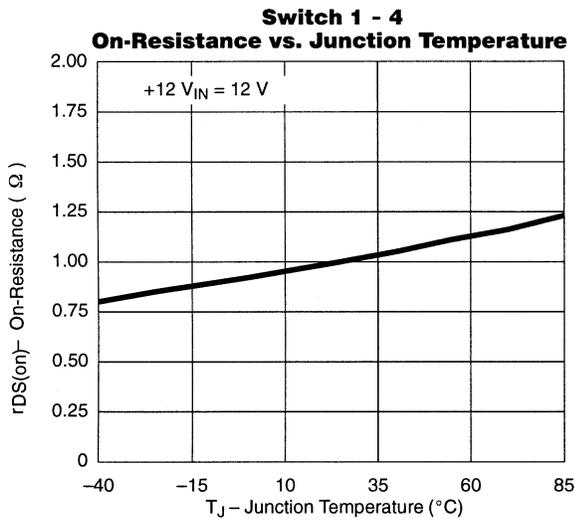
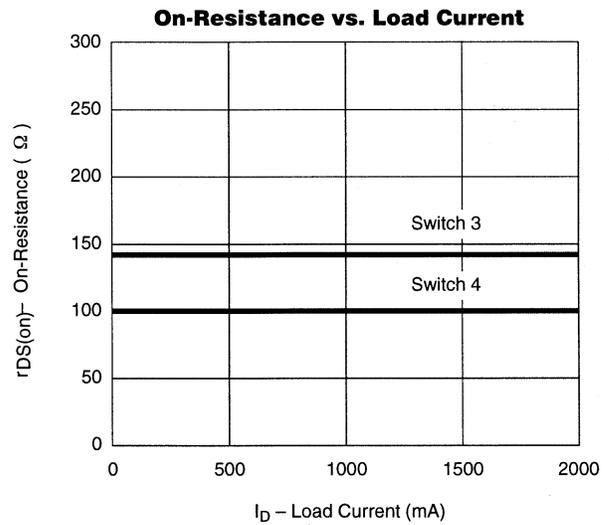
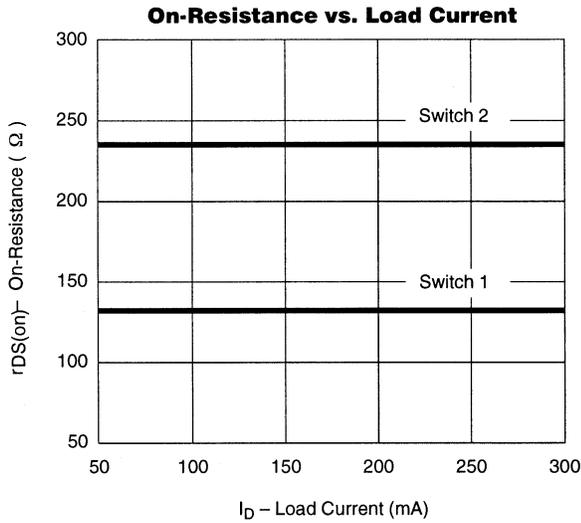
Top View

Order Number: Si9711CY

PIN DESCRIPTION		
Pin Number	Symbol	Description
1	S_3	Control input for selecting +5 V_{IN} to V_{CC} . The PC Card terminology for this pin is V_{CC_EN1} .
2	S_4	Control input for selecting +3.3 V_{IN} to V_{CC} . The PC Card terminology for this pin is V_{CC_EN0} .
3	GND	Ground connection.
4, 5	V_{PP}	Program and peripheral voltage to PC Card slot.
6, 7, 8, 12	V_{CC}	Supply voltage to slot.
9, 10	+3.3 V_{IN}	+3.3-V supply.
11	+5 V_{IN}	+5-V supply.
13	+12 V_{IN}	+12-V supply.
14	V_L	Rail voltage for switch control inputs, selectable to 5-V or 3.3-V.
15	S_1	Control input for selecting +12 V_{IN} to V_{PP} . The PC Card terminology for this pin is V_{PP_EN1} .
16	S_2	Control input for selecting V_{CC} to V_{PP} . The PC Card terminology for this pin is V_{PP_EN0} .



TYPICAL CHARACTERISTICS (25°C UNLESS OTHERWISE NOTED)





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