



# LT6600-20

## Very Low Noise, Differential Amplifier and 20MHz Lowpass Filter

### FEATURES

- Programmable Differential Gain via Two External Resistors
- Adjustable Output Common Mode Voltage
- Operates and Specified with 3V, 5V, ±5V Supplies
- 0.5dB Ripple 4th Order Lowpass Filter with 20MHz Cutoff
- 76dB S/N with 3V Supply and 2V<sub>P-P</sub> Output
- Low Distortion, 2V<sub>P-P</sub>, 800Ω Load  
2.5MHz: 83dBc 2nd, 88dBc 3rd  
20MHz: 63dBc 2nd, 64dBc 3rd
- Fully Differential Inputs and Outputs
- SO-8 Package
- Compatible with Popular Differential Amplifier Pinouts

### APPLICATIONS

- High Speed ADC Antialiasing and DAC Smoothing in Networking or Cellular Base Station Applications
- High Speed Test and Measurement Equipment
- Medical Imaging
- Drop-in Replacement for Differential Amplifiers

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### DESCRIPTION

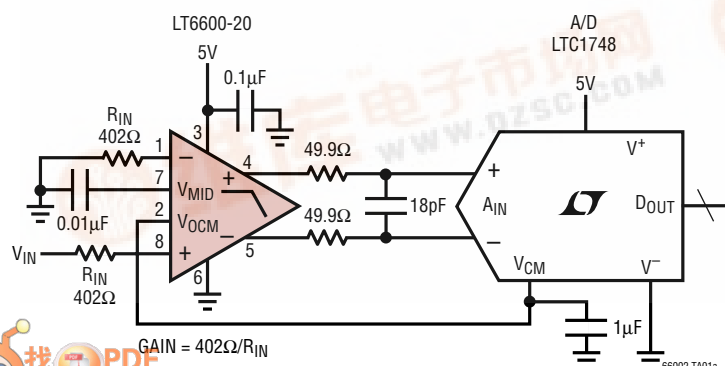
The LT<sup>®</sup>6600-20 combines a fully differential amplifier with a 4th order 20MHz lowpass filter approximating a Chebyshev frequency response. Most differential amplifiers require many precision external components to tailor gain and bandwidth. In contrast, with the LT6600-20, two external resistors program differential gain, and the filter's 20MHz cutoff frequency and passband ripple are internally set. The LT6600-20 also provides the necessary level shifting to set its output common mode voltage to accommodate the reference voltage requirements of A/Ds.

Using a proprietary internal architecture, the LT6600-20 integrates an antialiasing filter and a differential amplifier/driver without compromising distortion or low noise performance. At unity gain the measured in band signal-to-noise ratio is an impressive 76dB. At higher gains the input referred noise decreases so the part can process smaller input differential signals without significantly degrading the output signal-to-noise ratio.

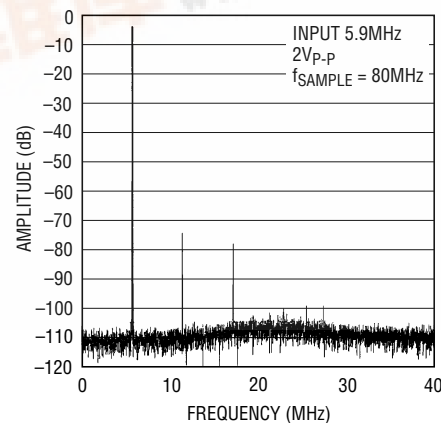
The LT6600-20 also features low voltage operation. The differential design provides outstanding performance for a 2V<sub>P-P</sub> signal level while the part operates with a single 3V supply.

The LT6600-20 is packaged in an SO-8 and is pin compatible with stand alone differential amplifiers.

### TYPICAL APPLICATION



An 8192 Point FFT Spectrum



# LT6600-20

## ABSOLUTE MAXIMUM RATINGS

(Note 1)

Total Supply Voltage .....	11V
Operating Temperature Range (Note 6) ...	-40°C to 85°C
Specified Temperature Range (Note 7) ....	-40°C to 85°C
Junction Temperature .....	150°C
Storage Temperature Range .....	-65°C to 150°C
Lead Temperature (Soldering, 10 sec) .....	300°C

## PACKAGE/ORDER INFORMATION

<p>TOP VIEW</p> <p>S8 PACKAGE 8-LEAD PLASTIC SO</p> <p><math>T_{JMAX} = 150^{\circ}C</math>, <math>\theta_{JA} = 100^{\circ}C/W</math></p>	ORDER PART NUMBER
	LT6600CS8-20 LT6600IS8-20
	S8 PART MARKING
	660020 600I20

Consult LTC Marketing for parts specified with wider operating temperature ranges.

## ELECTRICAL CHARACTERISTICS

The ● denotes specifications that apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^{\circ}C$ . Unless otherwise specified  $V_S = 5V$  ( $V^+ = 5V$ ,  $V^- = 0V$ ),  $R_{IN} = 402\Omega$ , and  $R_{LOAD} = 1k$ .

PARAMETER	CONDITIONS	MIN	TYP	MAX	UNITS
Filter Gain, $V_S = 3V$	$V_{IN} = 2V_{P-P}$ , $f_{IN} = DC$ to 260kHz	-0.4	0.1	0.5	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 2MHz$ (Gain Relative to 260kHz)	● -0.1	0	0.1	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 10MHz$ (Gain Relative to 260kHz)	● -0.2	0.1	0.5	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 16MHz$ (Gain Relative to 260kHz)	● -0.1	0.5	1.9	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 20MHz$ (Gain Relative to 260kHz)	● -0.8	0	1	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 60MHz$ (Gain Relative to 260kHz)	●	-33	-28	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 100MHz$ (Gain Relative to 260kHz)	●	-50		dB
Filter Gain, $V_S = 5V$	$V_{IN} = 2V_{P-P}$ , $f_{IN} = DC$ to 260kHz	-0.5	0	0.5	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 2MHz$ (Gain Relative to 260kHz)	● -0.1	0	0.1	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 10MHz$ (Gain Relative to 260kHz)	● -0.2	0.1	0.4	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 16MHz$ (Gain Relative to 260kHz)	● -0.3	0.4	1.6	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 20MHz$ (Gain Relative to 260kHz)	● -1.3	-0.4	0.6	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 60MHz$ (Gain Relative to 260kHz)	●	-33	-28	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = 100MHz$ (Gain Relative to 260kHz)	●	-50		dB
Filter Gain, $V_S = \pm 5V$	$V_{IN} = 2V_{P-P}$ , $f_{IN} = DC$ to 260kHz	-0.6	-0.1	0.4	dB
Filter Gain, $R_{IN} = 100\Omega$	$V_{IN} = 2V_{P-P}$ , $f_{IN} = DC$ to 260kHz, $V_S = 3V$	11.6	12.1	12.6	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = DC$ to 260kHz, $V_S = 5V$	11.5	12.0	12.5	dB
	$V_{IN} = 2V_{P-P}$ , $f_{IN} = DC$ to 260kHz, $V_S = \pm 5V$	11.4	11.9	12.4	dB
Filter Gain Temperature Coefficient (Note 2)	$f_{IN} = 250kHz$ , $V_{IN} = 2V_{P-P}$		780		ppm/C
Noise	Noise BW = 10kHz to 20MHz		118		$\mu V_{RMS}$
Distortion (Note 4)	2.5MHz, $2V_{P-P}$ , $R_L = 800\Omega$	2nd Harmonic		83	dBc
		3rd Harmonic		88	dBc
	20MHz, $2V_{P-P}$ , $R_L = 800\Omega$	2nd Harmonic		63	dBc
		3rd Harmonic		64	dBc
Differential Output Swing	Measured Between Pins 4 and 5	$V_S = 5V$	● 3.80	4.75	$V_{P-P DIFF}$
		$V_S = 3V$	● 3.75	4.50	$V_{P-P DIFF}$
Input Bias Current	Average of Pin 1 and Pin 8	● -95	-50		$\mu A$

**ELECTRICAL CHARACTERISTICS** The ● denotes specifications that apply over the full operating temperature range, otherwise specifications are at  $T_A = 25^\circ\text{C}$ . Unless otherwise specified  $V_S = 5\text{V}$  ( $V^+ = 5\text{V}$ ,  $V^- = 0\text{V}$ ),  $R_{IN} = 402\Omega$ , and  $R_{LOAD} = 1\text{k}$ .

PARAMETER	CONDITIONS			MIN	TYP	MAX	UNITS	
Input Referred Differential Offset	$R_{IN} = 402\Omega$	$V_S = 3\text{V}$	●		5	25	mV	
		$V_S = 5\text{V}$	●		10	30	mV	
		$V_S = \pm 5\text{V}$	●		10	35	mV	
	$R_{IN} = 100\Omega$	$V_S = 3\text{V}$	●		5	15	mV	
		$V_S = 5\text{V}$	●		5	17	mV	
		$V_S = \pm 5\text{V}$	●		5	20	mV	
Differential Offset Drift					10		$\mu\text{V}/^\circ\text{C}$	
Input Common Mode Voltage (Note 3)	Differential Input = 500mV <sub>P-P</sub> , $R_{IN} = 100\Omega$	$V_S = 3\text{V}$	●	0.0		1.5	V	
		$V_S = 5\text{V}$	●	0.0		3.0	V	
		$V_S = \pm 5\text{V}$	●	-2.5		1.0	V	
Output Common Mode Voltage (Note 5)	Differential Input = 2V <sub>P-P</sub> , Pin 7 at Mid-Supply Common Mode Voltage at Pin 2	$V_S = 3\text{V}$	●	1.0		1.5	V	
		$V_S = 5\text{V}$	●	1.5		3.0	V	
		$V_S = \pm 5\text{V}$	●	-1.0		2.0	V	
Output Common Mode Offset (with Respect to Pin 2)		$V_S = 3\text{V}$	●	-35	5	40	mV	
		$V_S = 5\text{V}$	●	-40	0	40	mV	
		$V_S = \pm 5\text{V}$	●	-55	-5	35	mV	
Common Mode Rejection Ratio					66		dB	
Voltage at $V_{MID}$ (Pin 7)		$V_S = 5$	●	2.46	2.51	2.55	V	
		$V_S = 3$			1.5		V	
$V_{MID}$ Input Resistance				●	4.35	5.7	7.65	k $\Omega$
$V_{OCM}$ Bias Current	$V_{OCM} = V_{MID} = V_S/2$	$V_S = 5\text{V}$	●	-15	-3		$\mu\text{A}$	
		$V_S = 3\text{V}$	●	-10	-3		$\mu\text{A}$	
Power Supply Current		$V_S = 3\text{V}$ , $V_S = 5$	●		42	46	mA	
		$V_S = 3\text{V}$ , $V_S = 5$	●			53	mA	
		$V_S = \pm 5\text{V}$	●		46	56	mA	

**Note 1:** Absolute Maximum Ratings are those values beyond which the life of a device may be impaired.

**Note 2:** This is the temperature coefficient of the internal feedback resistors assuming a temperature independent external resistor ( $R_{IN}$ ).

**Note 3:** The input common mode voltage is the average of the voltages applied to the external resistors ( $R_{IN}$ ). Specification guaranteed for  $R_{IN} \geq 100\Omega$ .

**Note 4:** Distortion is measured differentially using a differential stimulus. The input common mode voltage, the voltage at Pin 2, and the voltage at Pin 7 are equal to one half of the total power supply voltage.

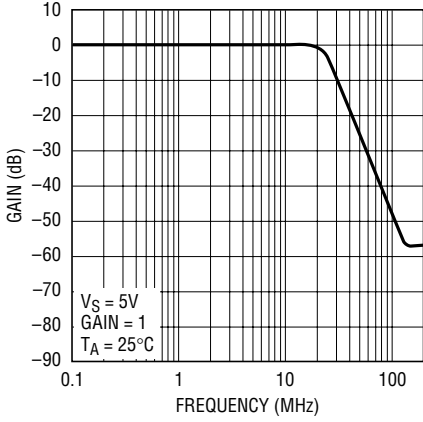
**Note 5:** Output common mode voltage is the average of the voltages at Pins 4 and 5. The output common mode voltage is equal to the voltage applied to Pin 2.

**Note 6:** The LT6600C-20 is guaranteed functional over the operating temperature range  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .

**Note 7:** The LT6600C-20 is guaranteed to meet  $0^\circ\text{C}$  to  $70^\circ\text{C}$  specifications and is designed, characterized and expected to meet the extended temperature limits, but is not tested at  $-40^\circ\text{C}$  and  $85^\circ\text{C}$ . The LT6600I-20 is guaranteed to meet specified performance from  $-40^\circ\text{C}$  to  $85^\circ\text{C}$ .

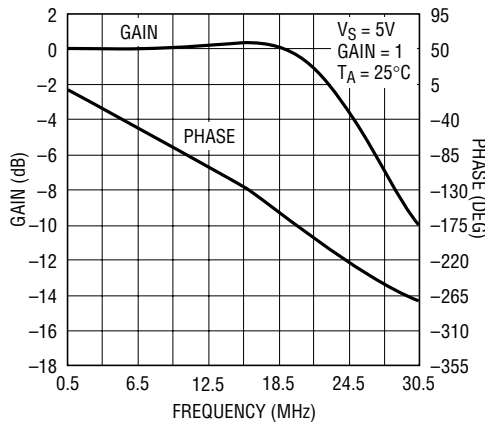
TYPICAL PERFORMANCE CHARACTERISTICS

Amplitude Response



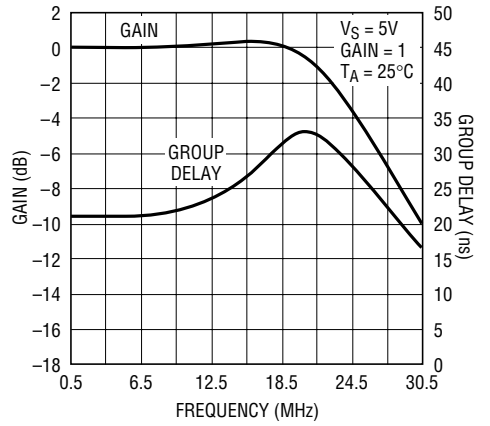
66002 G01

Passband Gain and Phase



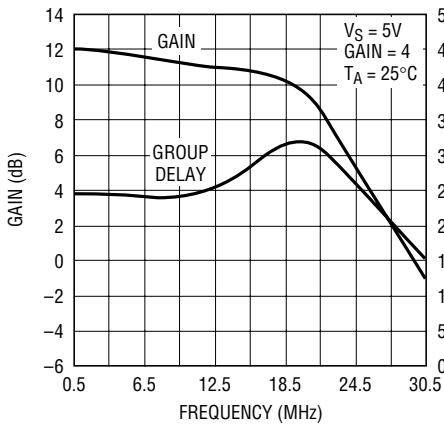
66002 G02

Passband Gain and Group Delay



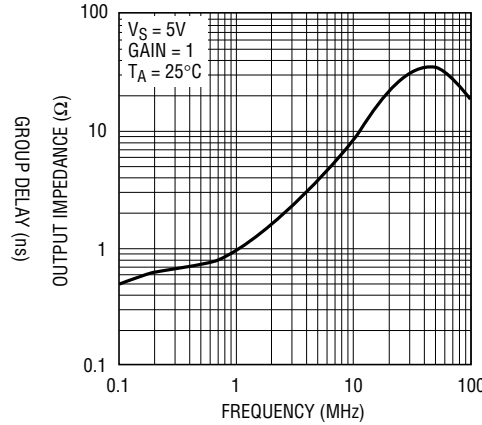
66002 G03

Passband Gain and Group Delay



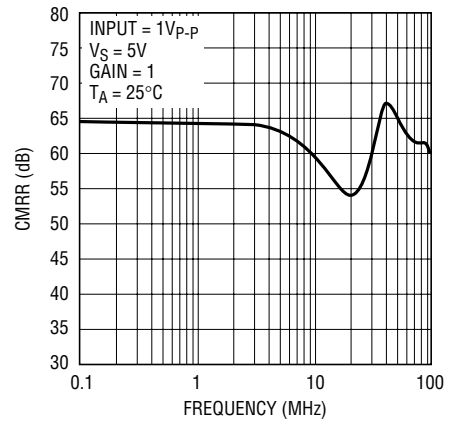
66002 G04

Output Impedance



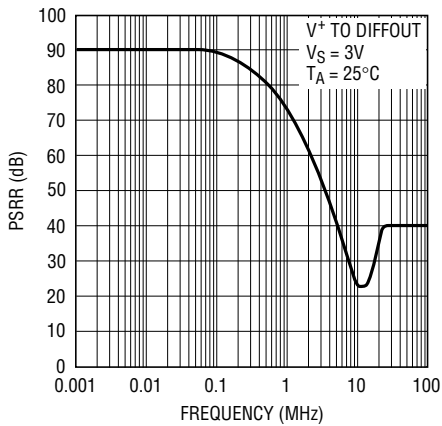
66002 G05

Common Mode Rejection Ratio



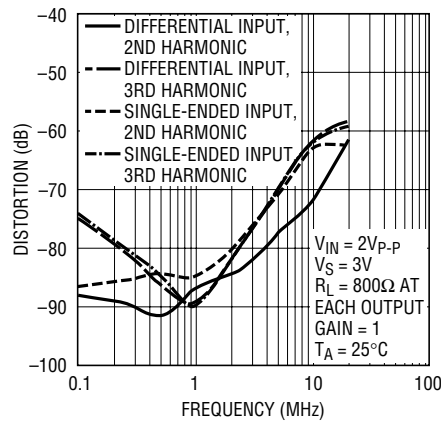
66002 G06

Power Supply Rejection Ratio



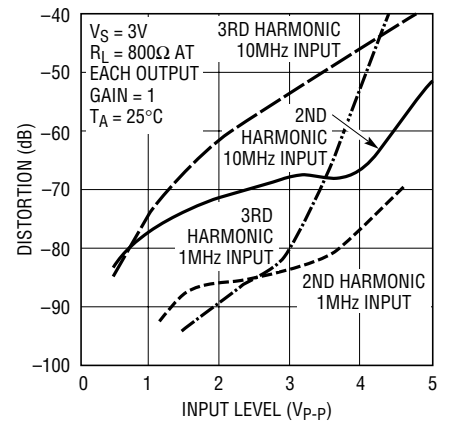
66002 G07

Distortion vs Frequency



66002 G08

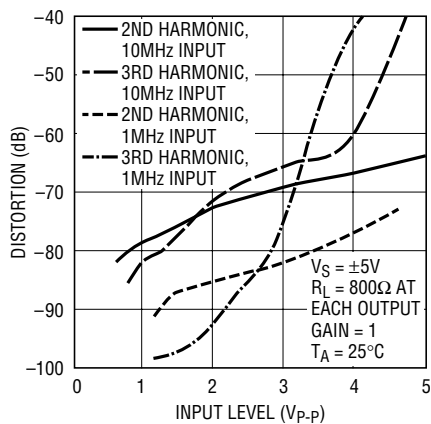
Distortion vs Signal Level,  $V_S = 3V$



66002 G09

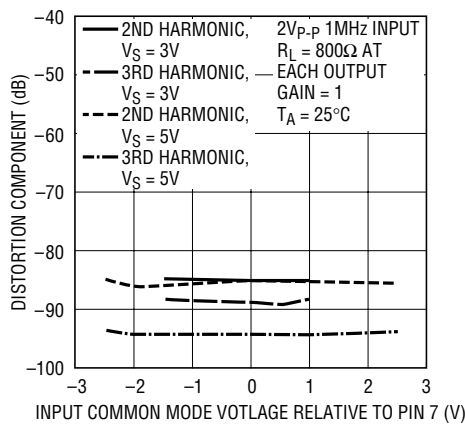
# TYPICAL PERFORMANCE CHARACTERISTICS

**Distortion vs Signal Level,  
 $V_S = \pm 5V$**



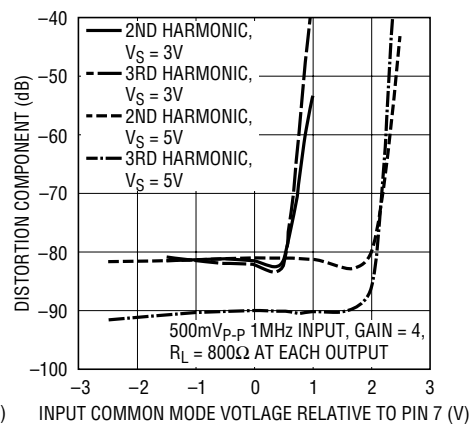
66002 G10

**Distortion vs Input Common Mode Level**



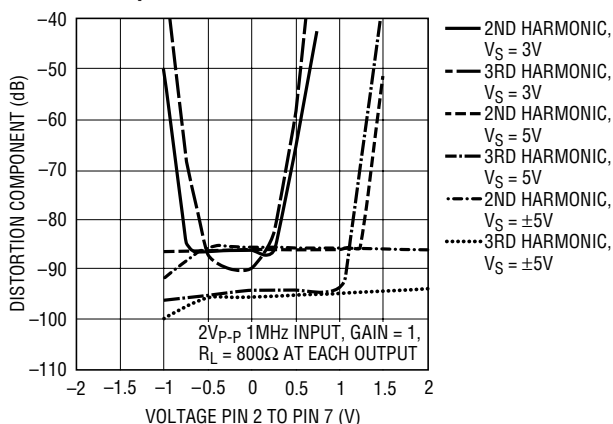
66002 G11

**Distortion vs Input Common Mode Level**



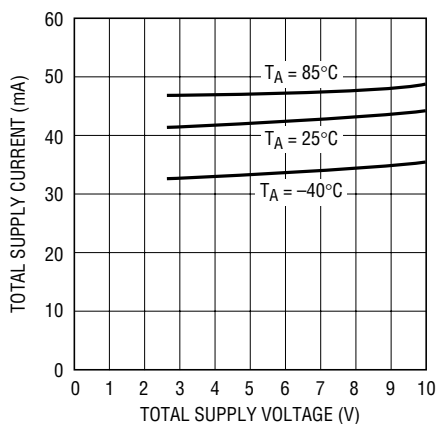
66002 G12

**Distortion vs Output Common Mode**



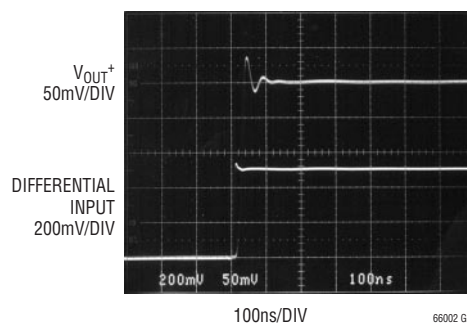
66002 G13

**Total Supply Current vs Total Supply Voltage**



66002 G14

**Transient Response, Gain = 1**



66002 G15

## PIN FUNCTIONS

**IN<sup>-</sup> and IN<sup>+</sup> (Pins 1, 8):** Input Pins. Signals can be applied to either or both input pins through identical external resistors,  $R_{IN}$ . The DC gain from differential inputs to the differential outputs is  $402\Omega/R_{IN}$ .

**V<sub>OCM</sub> (Pin 2):** Is the DC Common Mode Reference Voltage for the 2nd Filter Stage. Its value programs the common mode voltage of the differential output of the filter. Pin 2 is a high impedance input, which can be driven from an external voltage reference, or Pin 2 can be tied to Pin 7 on the PC board. Pin 2 should be bypassed with a  $0.01\mu\text{F}$  ceramic capacitor unless it is connected to a ground plane.

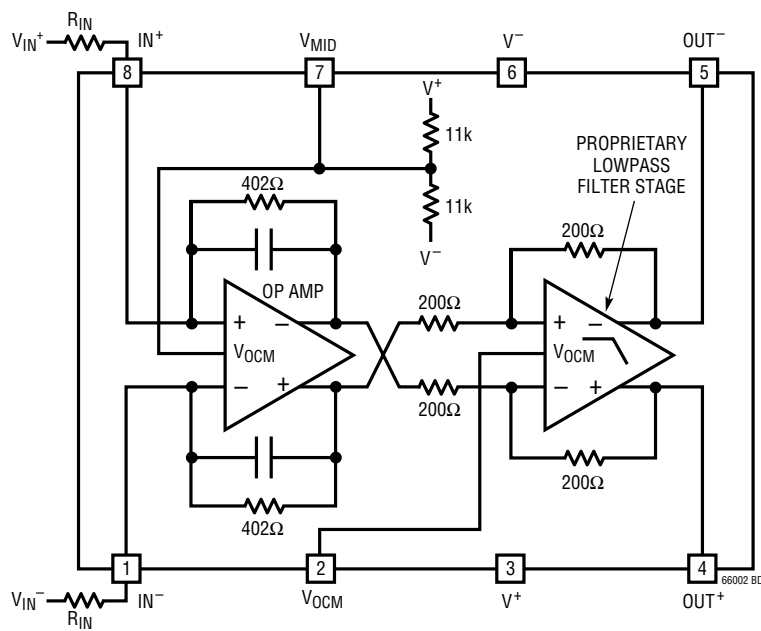
**V<sup>+</sup> and V<sup>-</sup> (Pins 3, 6):** Power Supply Pins. For a single 3.3V or 5V supply (Pin 6 grounded) a quality  $0.1\mu\text{F}$  ceramic bypass capacitor is required from the positive supply pin (Pin 3) to the negative supply pin (Pin 6). The bypass should be as close as possible to the IC. For dual

supply applications, bypass Pin 3 to ground and Pin 6 to ground with a quality  $0.1\mu\text{F}$  ceramic capacitor.

**OUT<sup>+</sup> and OUT<sup>-</sup> (Pins 4, 5):** Output Pins. Pins 4 and 5 are the filter differential outputs. Each pin can drive a  $100\Omega$  and/or  $50\text{pF}$  load.

**V<sub>MID</sub> (Pin 7):** The  $V_{MID}$  pin is internally biased at mid-supply, see block diagram. For single supply operation, the  $V_{MID}$  pin should be bypassed with a quality  $0.01\mu\text{F}$  ceramic capacitor to Pin 6. For dual supply operation, Pin 7 can be bypassed or connected to a high quality DC ground. A ground plane should be used. A poor ground will increase noise and distortion. Pin 7 sets the output common mode voltage of the 1st stage of the filter. It has a  $5.5\text{k}\Omega$  impedance, and it can be overridden with an external low impedance voltage source.

## BLOCK DIAGRAM



## APPLICATIONS INFORMATION

### Interfacing to the LT6600-20

The LT6600-20 requires two equal external resistors,  $R_{IN}$ , to set the differential gain to  $402\Omega/R_{IN}$ . The inputs to the filter are the voltages  $V_{IN}^+$  and  $V_{IN}^-$  presented to these external components, Figure 1. The difference between  $V_{IN}^+$  and  $V_{IN}^-$  is the differential input voltage. The average of  $V_{IN}^+$  and  $V_{IN}^-$  is the common mode input voltage. Similarly, the voltages  $V_{OUT}^+$  and  $V_{OUT}^-$  appearing at Pins 4 and 5 of the LT6600-20 are the filter outputs. The difference between  $V_{OUT}^+$  and  $V_{OUT}^-$  is the differential output voltage. The average of  $V_{OUT}^+$  and  $V_{OUT}^-$  is the common mode output voltage.

Figure 1 illustrates the LT6600-20 operating with a single 3.3V supply and unity passband gain; the input signal is DC coupled. The common mode input voltage is 0.5V, and the differential input voltage is  $2V_{P-P}$ . The common mode

output voltage is 1.65V, and the differential output voltage is  $2V_{P-P}$  for frequencies below 20MHz. The common mode output voltage is determined by the voltage at pin 2. Since pin 2 is shorted to pin 7, the output common mode is the mid-supply voltage. In addition, the common mode input voltage can be equal to the mid-supply voltage of Pin 7 (see the Distortion vs Input Common Mode Level graphs in the Typical Performance Characteristics).

Figure 2 shows how to AC couple signals into the LT6600-20. In this instance, the input is a single-ended signal. AC coupling allows the processing of single-ended or differential signals with arbitrary common mode levels. The  $0.1\mu F$  coupling capacitor and the  $402\Omega$  gain setting resistor form a high pass filter, attenuating signals below 4kHz. Larger values of coupling capacitors will proportionally reduce this highpass 3dB frequency.

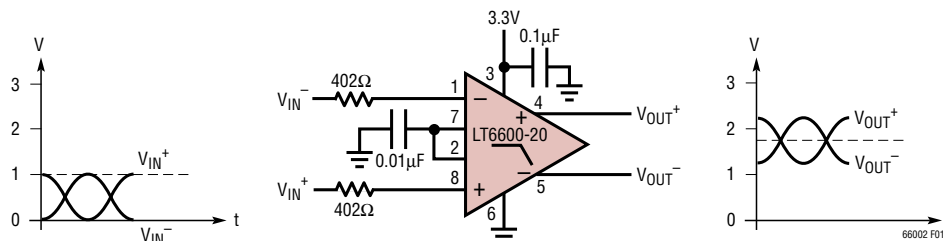


Figure 1

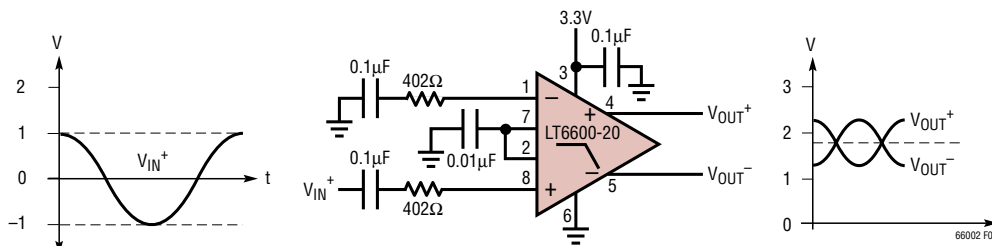


Figure 2

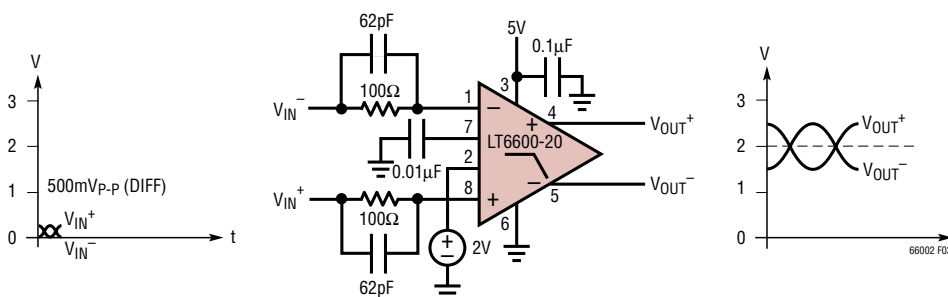


Figure 3

## APPLICATIONS INFORMATION

In Figure 3 the LT6600-20 is providing 12dB of gain. The gain resistor has an optional 62pF in parallel to improve the passband flatness near 20MHz. The common mode output voltage is set to 2V.

Use Figure 4 to determine the interface between the LT6600-20 and a current output DAC. The gain, or “transimpedance,” is defined as  $A = V_{OUT}/I_{IN}$ . To compute the transimpedance, use the following equation:

$$A = \frac{402 \cdot R1}{R1 + R2} (\Omega)$$

By setting  $R1 + R2 = 402\Omega$ , the gain equation reduces to  $A = R1(\Omega)$ .

The voltage at the pins of the DAC is determined by R1, R2, the voltage on Pin 7 and the DAC output current. Consider Figure 4 with  $R1 = 49.9\Omega$  and  $R2 = 348\Omega$ . The voltage at Pin 7 is 1.65V. The voltage at the DAC pins is given by:

$$\begin{aligned} V_{DAC} &= V_{PIN7} \cdot \frac{R1}{R1 + R2 + 402} + I_{IN} \cdot \frac{R1 \cdot R2}{R1 + R2} \\ &= 26mV + I_{IN} \cdot 48.3\Omega \end{aligned}$$

$I_{IN}$  is  $I_{IN}^+$  or  $I_{IN}^-$ . The transimpedance in this example is  $50.4\Omega$ .

### Evaluating the LT6600-20

The low impedance levels and high frequency operation of the LT6600-20 require some attention to the matching networks between the LT6600-20 and other devices. The previous examples assume an ideal ( $0\Omega$ ) source impedance and a large ( $1k\Omega$ ) load resistance. Among practical

examples where impedance must be considered is the evaluation of the LT6600-20 with a network analyzer. Figure 5 is a laboratory setup that can be used to characterize the LT6600-20 using single-ended instruments with  $50\Omega$  source impedance and  $50\Omega$  input impedance. For a unity gain configuration the LT6600-20 requires a  $402\Omega$  source resistance yet the network analyzer output is calibrated for a  $50\Omega$  load resistance. The 1:1 transformer,  $53.6\Omega$  and  $388\Omega$  resistors satisfy the two constraints above. The transformer converts the single-ended source into a differential stimulus. Similarly, the output of the LT6600-20 will have lower distortion with larger load resistance yet the analyzer input is typically  $50\Omega$ . The 4:1 turns (16:1 impedance) transformer and the two  $402\Omega$  resistors of Figure 5, present the output of the LT6600-20 with a  $1600\Omega$  differential load, or the equivalent of  $800\Omega$  to ground at each output. The impedance seen by the network analyzer input is still  $50\Omega$ , reducing reflections in the cabling between the transformer and analyzer input.

### Differential and Common Mode Voltage Ranges

The differential amplifiers inside the LT6600-20 contain circuitry to limit the maximum peak-to-peak differential voltage through the filter. This limiting function prevents excessive power dissipation in the internal circuitry and provides output short-circuit protection. The limiting function begins to take effect at output signal levels above  $2V_{P-P}$  and it becomes noticeable above  $3.5V_{P-P}$ . This is illustrated in Figure 6; the LT6600-20 was configured with unity passband gain and the input of the filter was driven with a 1MHz signal. Because this voltage limiting takes place well before the output stage of the filter reaches the

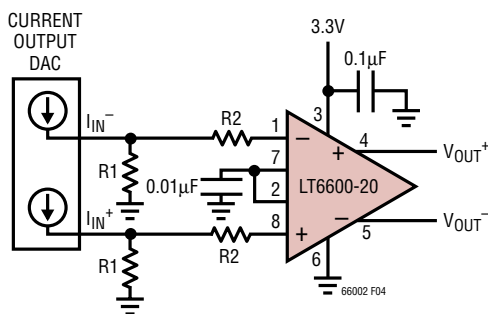


Figure 4

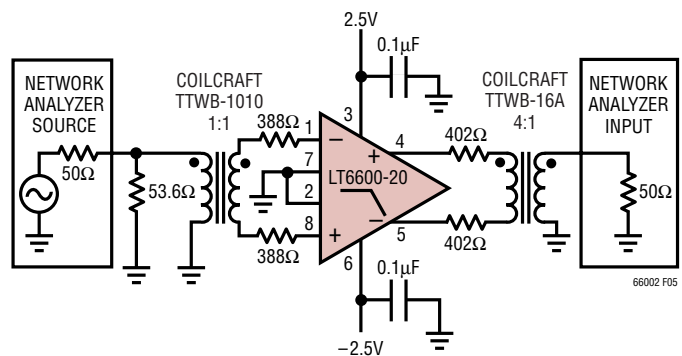
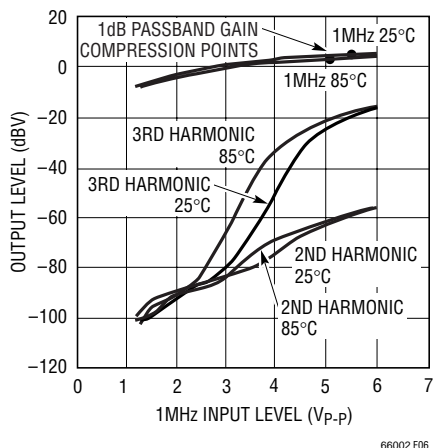


Figure 5

## APPLICATIONS INFORMATION



**Figure 6. Output Level vs Input Level, Differential 1MHz Input, Gain = 1**

supply rails, the input/output behavior of the IC shown in Figure 6 is relatively independent of the power supply voltage.

The two amplifiers inside the LT6600-20 have independent control of their output common mode voltage (see the “block diagram” section). The following guidelines will optimize the performance of the filter.

Pin 7 must be bypassed to an AC ground with a 0.01 $\mu$ F or larger capacitor. Pin 7 can be driven from a low impedance source, provided it remains at least 1.5V above  $V^-$  and at least 1.5V below  $V^+$ . An internal resistor divider sets the voltage of Pin 7. While the internal 11k resistors are well matched, their absolute value can vary by  $\pm 20\%$ . This should be taken into consideration when connecting an external resistor network to alter the voltage of Pin 7.

Pin 2 can be shorted to Pin 7 for simplicity. If a different common mode output voltage is required, connect Pin 2 to a voltage source or resistor network. For 3V and 3.3V supplies the voltage at Pin 2 must be less than or equal to the mid supply level. For example, voltage (Pin 2)  $\leq 1.65V$  on a single 3.3V supply. For power supply voltages higher than 3.3V the voltage at Pin 2 should be within the voltage of Pin 7 – 1V to the voltage of Pin 7 + 2V. Pin 2 is a high impedance input.

The LT6600-20 was designed to process a variety of input signals including signals centered around the mid-supply voltage and signals that swing between ground and a positive voltage in a single supply system (Figure 1). The range of allowable input common mode voltage (the average of  $V_{IN}^+$  and  $V_{IN}^-$  in Figure 1) is determined by the power supply level and gain setting (see Distortion vs Input Common Mode Level in the Typical Performance Characteristics).

### Common Mode DC Currents

In applications like Figure 1 and Figure 3 where the LT6600-20 not only provides lowpass filtering but also level shifts the common mode voltage of the input signal, DC currents will be generated through the DC path between input and output terminals. Minimize these currents to decrease power dissipation and distortion.

Consider the application in Figure 3. Pin 7 sets the output common mode voltage of the 1st differential amplifier inside the LT6600-20 (see the “Block Diagram” section) at 2.5V. Since the input common mode voltage is near 0V, there will be approximately a total of 2.5V drop across the series combination of the internal 402 $\Omega$  feedback resistor and the external 100 $\Omega$  input resistor. The resulting 5mA common mode DC current in each input path, must be absorbed by the sources  $V_{IN}^+$  and  $V_{IN}^-$ . Pin 2 sets the common mode output voltage of the 2nd differential amplifier inside the LT6600-20, and therefore sets the common mode output voltage of the filter. Since, in the example of Figure 3, Pin 2 differs from Pin 7 by 0.5V, an additional 2.5mA (1.25mA per side) of DC current will flow in the resistors coupling the 1st differential amplifier output stage to filter output. Thus, a total of 12.5mA is used to translate the common mode voltages.

A simple modification to Figure 3 will reduce the DC common mode currents by 36%. If Pin 7 is shorted to Pin 2 the common mode output voltage of both op amp stages will be 2V and the resulting DC current will be 8mA. Of course, by AC coupling the inputs of Figure 3, the common mode DC current can be reduced to 2.5mA.

## APPLICATIONS INFORMATION

### Noise

The noise performance of the LT6600-20 can be evaluated with the circuit of Figure 7.

Given the low noise output of the LT6600-20 and the 6dB attenuation of the transformer coupling network, it is necessary to measure the noise floor of the spectrum analyzer and subtract the instrument noise from the filter noise measurement.

Example: With the IC removed and the 25Ω resistors grounded, Figure 7, measure the total integrated noise ( $e_s$ ) of the spectrum analyzer from 10kHz to 20MHz. With the IC inserted, the signal source ( $V_{IN}$ ) disconnected, and the input resistors grounded, measure the total integrated noise out of the filter ( $e_o$ ). With the signal source connected, set the frequency to 1MHz and adjust the amplitude until  $V_{IN}$  measures 100mV<sub>P-P</sub>. Measure the output amplitude,  $V_{OUT}$ , and compute the passband gain  $A = V_{OUT}/V_{IN}$ . Now compute the input referred integrated noise ( $e_{IN}$ ) as:

$$e_{IN} = \frac{\sqrt{(e_o)^2 - (e_s)^2}}{A}$$

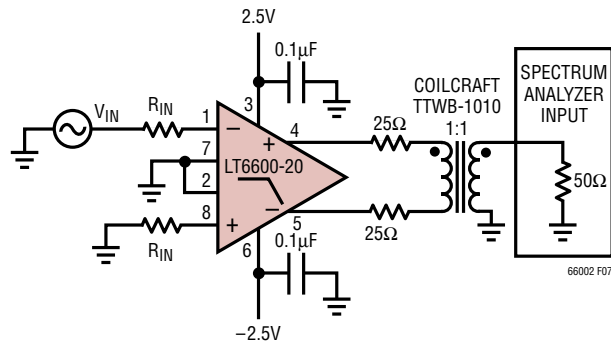
Table 1 lists the typical input referred integrated noise for various values of  $R_{IN}$ .

Figure 8 is plot of the noise spectral density as a function of frequency for an LT6600-20 with  $R_{IN} = 402\Omega$  using the fixture of Figure 7 (the instrument noise has been subtracted from the results).

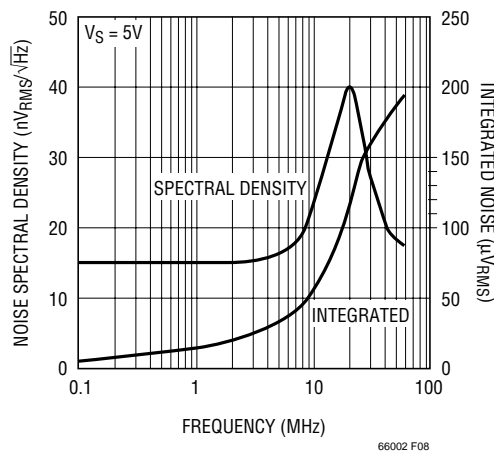
**Table 1. Noise Performance**

PASSBAND GAIN (V/V)	$R_{IN}$	INPUT REFERRED INTEGRATED NOISE 10kHz TO 20MHz	INPUT REFERRED NOISE dBm/Hz
4	100Ω	42μV <sub>RMS</sub>	-148
2	200Ω	67μV <sub>RMS</sub>	-143
1	402Ω	118μV <sub>RMS</sub>	-139

The noise at each output is comprised of a differential component and a common mode component. Using a transformer or combiner to convert the differential outputs to single-ended signal rejects the common mode noise and gives a true measure of the S/N achievable in the system. Conversely, if each output is measured individually and the



**Figure 7**



**Figure 8. Input Referred Noise, Gain = 1**

noise power added together, the resulting calculated noise level will be higher than the true differential noise.

### Power Dissipation

The LT6600-20 amplifiers combine high speed with large-signal currents in a small package. There is a need to ensure that the die junction temperature does not exceed 150°C. The LT6600-20 package has Pin 6 fused to the lead frame to enhance thermal conduction when connecting to a ground plane or a large metal trace. Metal trace and plated through-holes can be used to spread the heat generated by the device to the backside of the PC board. For example, on a 3/32" FR-4 board with 2oz copper, a total of 660 square millimeters connected to Pin 6 of the LT6600-20 (330 square millimeters on each side of the PC board) will result in a thermal resistance,  $\theta_{JA}$ , of about 85°C/W. Without the extra metal trace connected to the V<sup>-</sup> pin to provide a heat sink, the thermal resistance will be around 105°C/W. Table 2 can be used as a guide when considering thermal resistance.

## APPLICATIONS INFORMATION

Table 2. LT6600-20 SO-8 Package Thermal Resistance

COPPER AREA		BOARD AREA (mm <sup>2</sup> )	THERMAL RESISTANCE (JUNCTION-TO-AMBIENT)
TOPSIDE (mm <sup>2</sup> )	BACKSIDE (mm <sup>2</sup> )		
1100	1100	2500	65°C/W
330	330	2500	85°C/W
35	35	2500	95°C/W
35	0	2500	100°C/W
0	0	2500	105°C/W

Junction temperature,  $T_J$ , is calculated from the ambient temperature,  $T_A$ , and power dissipation,  $P_D$ . The power dissipation is the product of supply voltage,  $V_S$ , and supply current,  $I_S$ . Therefore, the junction temperature is given by:

$$T_J = T_A + (P_D \cdot \theta_{JA}) = T_A + (V_S \cdot I_S \cdot \theta_{JA})$$

where the supply current,  $I_S$ , is a function of signal level, load impedance, temperature and common mode voltages.

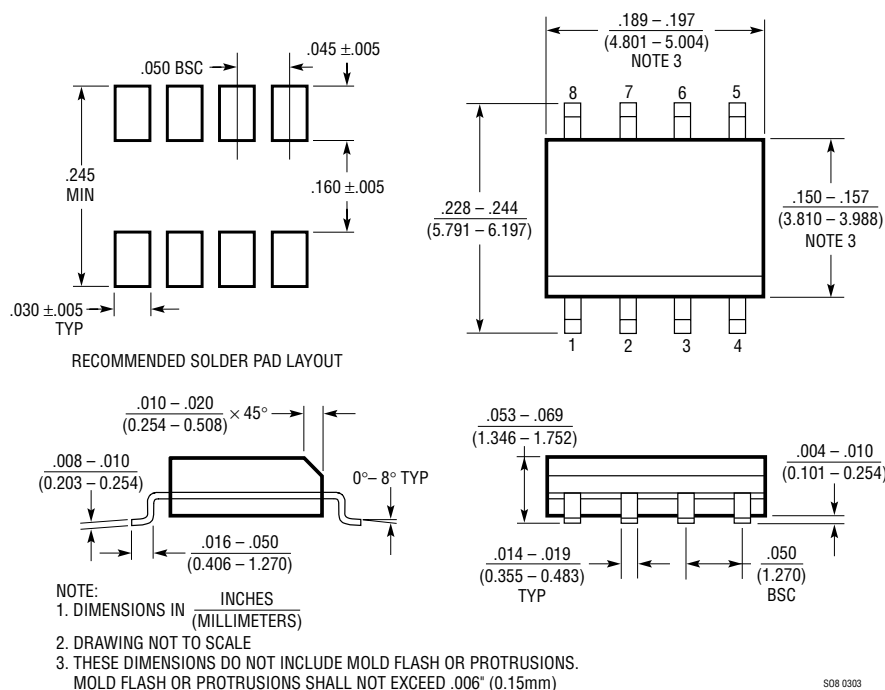
For a given supply voltage, the worst-case power dissipation occurs when the differential input signal is maximum, the common mode currents are maximum (see Applications Information regarding common mode DC currents), the load impedance is small and the ambient temperature is maximum. To compute the junction temperature, measure the supply current under these worst-case conditions, estimate the thermal resistance from Table 2, then apply the equation for  $T_J$ . For example, using the circuit in Figure 3 with a DC differential input voltage of 250mV, a differential output voltage of 1V, no load resistance and an ambient temperature of 85°C, the supply current (current into Pin 3) measures 55.5mA. Assuming a PC board layout with a 35mm<sup>2</sup> copper trace, the  $\theta_{JA}$  is 100°C/W. The resulting junction temperature is:

$$T_J = T_A + (P_D \cdot \theta_{JA}) = 85 + (5 \cdot 0.0555 \cdot 100) = 113^\circ\text{C}$$

When using higher supply voltages or when driving small impedances, more copper may be necessary to keep  $T_J$  below 150°C.

## PACKAGE DESCRIPTION

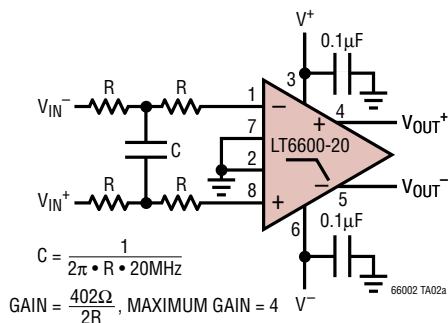
**S8 Package**  
**8-Lead Plastic Small Outline (Narrow .150 Inch)**  
 (Reference LTC DWG # 05-08-1610)



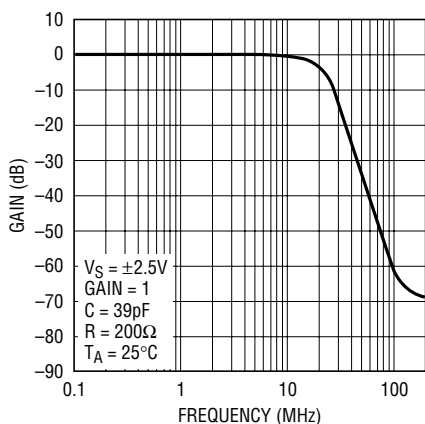
S08 0303

## TYPICAL APPLICATION

### A 5th Order, 20MHz Lowpass Filter

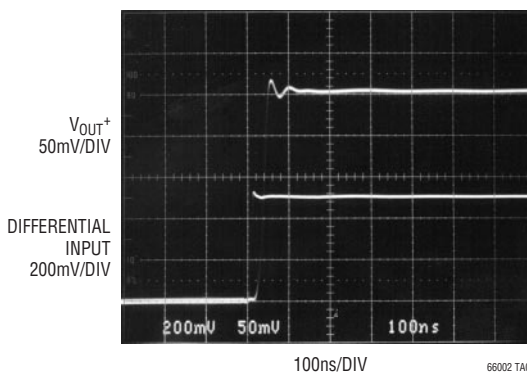


### Amplitude Response



66002 TA04

### Transient Response, Gain = 1



66002 TA03

## RELATED PARTS

PART NUMBER	DESCRIPTION	COMMENTS
LTC <sup>®</sup> 1565-31	650kHz Linear Phase Lowpass Filter	Continuous Time, SO8 Package, Fully Differential
LTC1566-1	Low Noise, 2.3MHz Lowpass Filter	Continuous Time, SO8 Package
LT1567	Very Low Noise, High Frequency Filter Building Block	1.4nV/√Hz Op Amp, MSOP Package, Fully Differential
LT1568	Very Low Noise, 4th Order Building Block	Lowpass and Bandpass Filter Designs Up to 10MHz, Differential Outputs
LT6600-2.5	Very Low Noise Differential Amplifier and 2.5MHz Lowpass Filter	86dB S/N with 3V Supply, SO-8
LT6600-10	Very Low Noise Differential Amplifier and 10MHz Lowpass Filter	82dB S/N with 3V Supply, SO-8