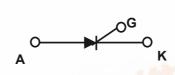
MCR100-3 ... MCR100-8





1. Cathode 2. Gate 3. Anode

TO-92 Plastic Package
Weight approx. 0.18g

MAXIMUM RATINGS (T_J=25°C unless otherwise noted.)

Rating	BY 6	Symbol	Value	Unit
Peak Repetitive Forward and Reverse Blocking	The same			
Voltage, Note 1 (T.=25 to 125°C, Rov=1KO)	MCR100-3		100	
$(T_J=25 \text{ to } 125^{\circ}\text{C}, R_{GK}=1\text{K}\Omega)$	MCR100-4	V_{DRM}	200	
	MCR100-5	and	300	Volts
	MCR100-6	V_{RRM}	400	2 100
	MCR100-7		500	COM
	MCR100-8	LITTE.	600	
Forward Current RMS	130 TA	F 1-4	0.8	Amps
(All Conduction Angles)	(A)(D)	I _{T(RMS)}		
Peak Forward Surge Current, T _{A=} 25°C		I _{TSM}	10	Amps
(1/2 Cycle, Sine Wave, 60Hz)		TSM		
Circuit Fusing (t=8.3ms)		l ² t	0.415	A^2s
Peak Gate Power - Forward, T _A =25°C		P_{GM}	0.1	Watts
Average Gate Power - Forward, T _A =25°C		P _{GF(AV)}	0.01	Watt
Peak Gate Current - Forward, T _A =25°C		- CE-1	IMM DIEC	Amp
(300µs,120PPS)	41	GFM		
Peak Gate Voltage - Reverse		V_{GRM}	5	Volts
Operating Junction Temperature Range @ Rated V _{RRM} and V _{DRM}		T_J	-40 to +125	°C
Storage Temperature Range		Ts	-40 to +150	°C

Note 1. Ratings apply for zero or negative gate voltage; however, positive gate voltage shall not be applied concurrent with negative potential on the anode.

GSP FORM A IS AVAILABLE



MCR100-3 ... MCR100-8

CHARACTERISTICS (Tc=25°C, RGK=1K Ω unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
Peak Forward or Reverse Blocking Current	I_{DRM}, I_{RRM}			
$(V_{AK}$ =Rated V_{DRM} or V_{RRM})		-	10	μΑ
Forward "On" Voltage	V_{TM}	-	1.7	Volts
(I _{TM} =1A Peak @T _A =25°C)				
Gate Trigger Current(Continuous dc),Note 1	I_{GT}	-	200	μΑ
(Anode Voltage=7Vdc,R _L =100 Ohms)				
Gate Trigger Voltage(Continuous dc)	V_{GT}	-	0.8	Volts
(Anode Voltage=7Vdc,R _L =100 Ohms)				
(Anode Voltage=Rated V _{DRM} ,R _L =100 Ohms)				
Holding Current	I _H	-	5	mA
(Anode Voltage=7Vdc,initiating current=20mA)				

Note 1. R_{GK} current is not included in measurement.

GSP FORM A IS AVAILABLE



MCR100-3 ... MCR100-8

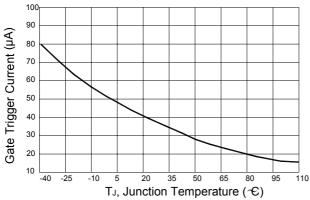


Figure 1. Typical Gate Trigger Curent Versus Junction Temperature

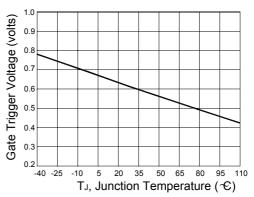


Figure 2. Typical Gate Trigger Voltage Versus Junction Temperature

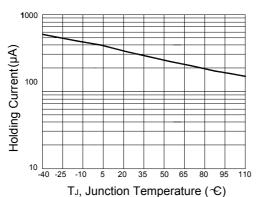


Figure 3. Typical Holding Curent Versus
Junction Temperature

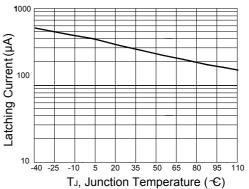


Figure 4. Typical Latching Curent Versus
Junction Temperature

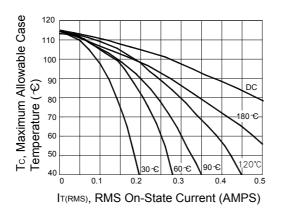


Figure 5. Typical RMS Current Derating

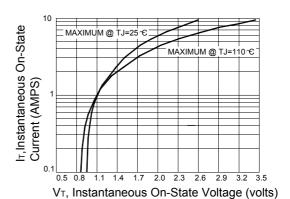


Figure 6. Typical On-State Characteristics

