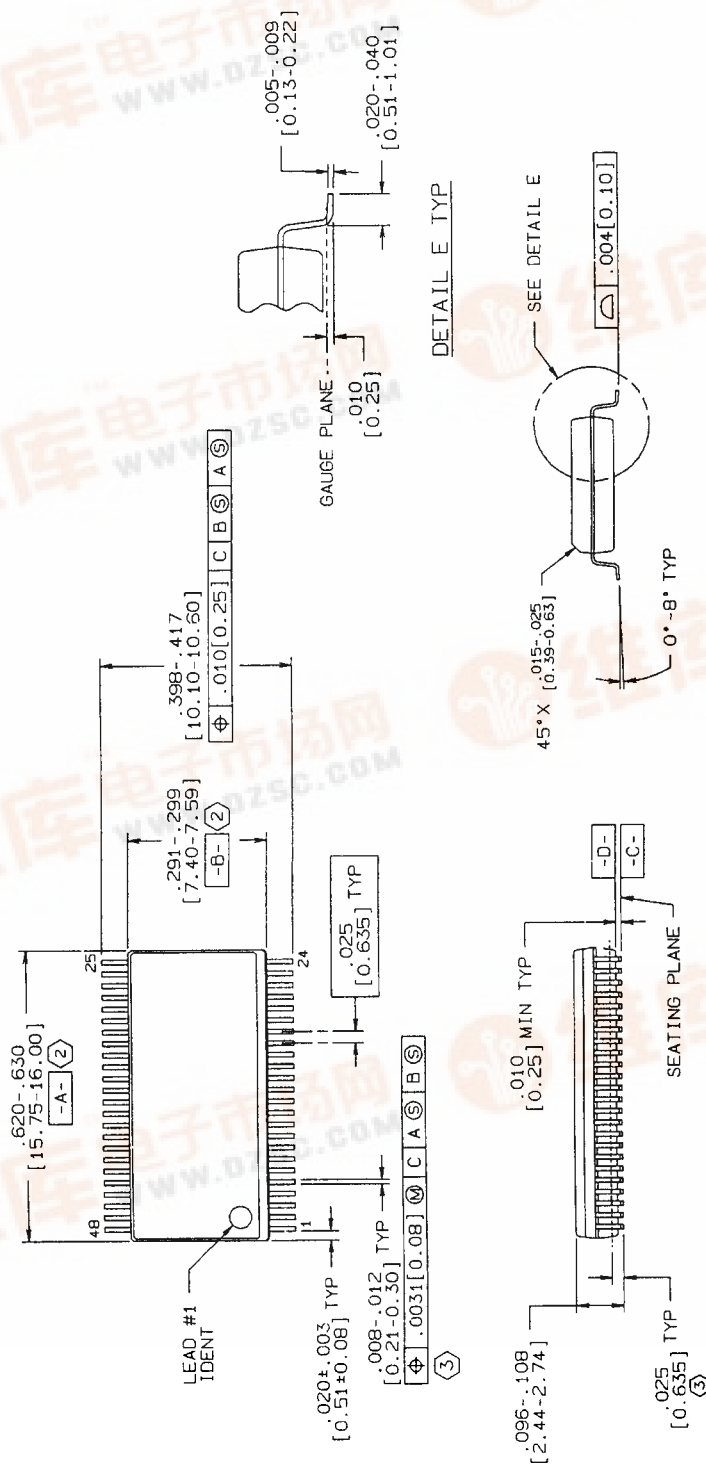


APPROVALS	DATE	 National Semiconductor 2900 Semiconductor dr, Santa Clara, CA 95052-8090
DRAWN	04/01/91	
DTG. CHK. <i>Therese Longmy</i>	11/10/95	
ENGR. CHK. <i>Charles Rhee</i>	11/09/95	
PROJECTION 		SCALE N/A
		SIZE C
INCH		DRAWING NUMBER MKT-MS48A
		REV E
		DO NOT SCALE DRAWING SHEET 1 OF 1



CONTROLLING DIMENSION IS INCH
VALUES IN [] ARE MILLIMETERS

NOTES: UNLESS OTHERWISE SPECIFIED

1. STANDARD LEAD FINISH:
200 MICRONS/CHES/508 MICROMETERS MINIMUM LEAD/TIN
(SOLDER) ON COPPER.
- ② DIMENSION DOES NOT INCLUDE MOLD FLASH.
- ③ MAXIMUM LEAD WIDTH ABOVE AREA SPECIFIED .018[0.45].
4. REFERENCE JEDEC REGISTRATION MO-418, VARIATION AA, DATED JUNE 1993.