

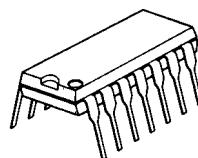
DUAL TIMER

■ GENERAL DESCRIPTION

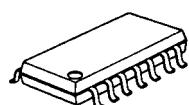
The **NJM556** dual monolithic timing circuit is a highly stable controller capable of producing accurate time delays or oscillation. In the time delay mode, delay time is precisely controlled by only two external parts : a resistor and a capacitor. For operation as an oscillator, both the free running frequency and the duty cycle are accurately controlled by two external resistors and capacitor.

Terminal are provided for triggering and resetting. The circuit will trigger and reset on falling waveforms. The output can source or sink up to 200mA or drive TTL circuits.

■ PACKAGE OUTLINE



N JM556D

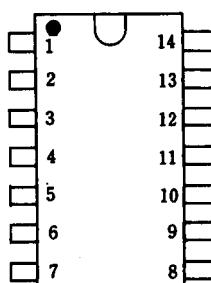


N JM556M

■ FEATURES

- Dual Timer Circuits
- Less number of External Components
- Package Outline DIP14, DMP14
- Bipolar Technology

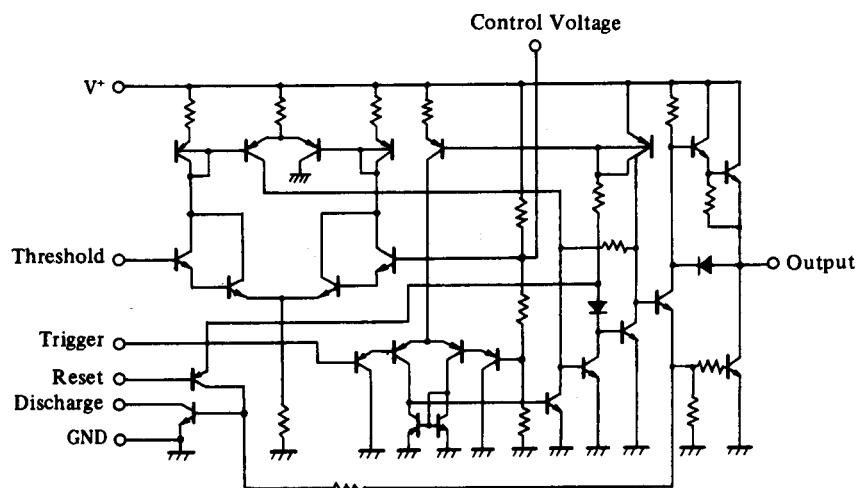
■ PIN CONFIGURATION

N JM556D
N JM556M

PIN FUNCTION

- | | |
|----------------|--------------------|
| 1. Discharge A | 8. Trigger B |
| 2. Threshold A | 9. Output B |
| 3. Control A | 10. Reset B |
| 4. Reset A | 11. Control B |
| 5. Output A | 12. Threshold B |
| 6. Trigger A | 13. Discharge B |
| 7. Ground | 14. V ⁺ |

■ EQUIVALENT CIRCUIT (1/2 Shown)



NJM556

■ ABSOLUTE MAXIMUM RATINGS

($T_a=25^\circ\text{C}$)

PARAMETER	SYMBOL	RATINGS	UNIT
Supply Voltage	V^+	18	V
Power Dissipation	P_D	(DIP14) 570 (DMP14) 700(note)	mW mW
Operating Temperature Range	T_{opr}	-40 to +85	°C
Storage Temperature Range	T_{stg}	-40 to +125	°C

(Note) At on PC board

■ ELECTRICAL CHARACTERISTICS

($V^+=+5$ to $+15$ V, $T_a=25^\circ\text{C}$)

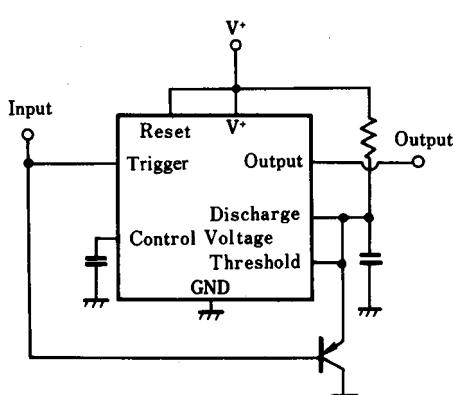
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Operating Voltage	V^+		4.5	-	16	V
Operating Current (Note 2)	I_{CC}	$V^+=5$ V, $R_L=\infty$ (Each Section)	-	3	6	mA
Operating Current (Note 2)	I_{CC}	$V^+=15$ V, $R_L=\infty$ (Each Section)	-	10	14	mA
Threshold Voltage	V_{TH}		-	2/3	-	$\times V^+$
Trigger Voltage	V_T	$V^+=15$ V	-	5	-	V
Trigger Voltage	V_T	$V^+=5$ V	-	1.67	-	V
Trigger Current	I_T		-	0.5	-	μA
Reset Voltage	V_R		0.4	0.7	1.0	V
Reset Current	I_R		-	0.1	-	mA
Threshold Current	I_T		-	0.03	0.1	μA
Control Voltage Level	V_{CL}	$V^+=15$ V	9	10	11	V
Control Voltage Level	V_{CL}	$V^+=5$ V	2.6	3.33	4	V
Output Voltage Drop (Low)	V_{OL}	$V^+=15$ V, $I_{SINK}=10$ mA	-	0.1	0.25	V
Output Voltage Drop (Low)	V_{OL}	$V^+=15$ V, $I_{SINK}=50$ mA	-	0.4	0.75	V
Output Voltage Drop (Low)	V_{OL}	$V^+=15$ V, $I_{SINK}=100$ mA	-	2	2.75	V
Output Voltage Drop (Low)	V_{OL}	$V^+=15$ V, $I_{SINK}=200$ mA	-	2.5	-	V
Output Voltage Drop (Low)	V_{OL}	$V^+=5$ V, $I_{SINK}=5$ mA	-	0.25	0.35	V
Output Voltage Drop (High)	V_{OH}	$V^+=15$ V, $I_{SOURCE}=200$ mA	-	12.5	-	V
Output Voltage Drop (High)	V_{OH}	$V^+=15$ V, $I_{SOURCE}=100$ mA	12.75	13.3	-	V
Output Voltage Drop (High)	V_{OH}	$V^+=15$ V, $I_{SOURCE}=40$ mA	-	13.5	-	V
Output Voltage Drop (High)	V_{OH}	$V^+=5$ V, $I_{SOURCE}=100$ mA	2.75	3.3	-	V

(Note 2) Operating Current when output high typically 2mA less.

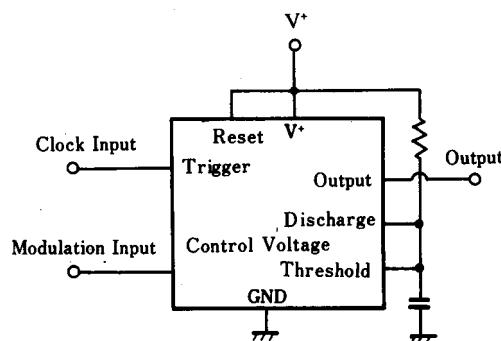
PARAMETER	SYMBOL	TEST CONDITION	MIN.	TYP.	MAX.	UNIT
Timing Error (Free Running) (Note 3)						
Initial Accuracy	E_{ta}	$R_A, R_B=2k$ to $100k\Omega$, $C=0.1\mu\text{F}$	-	2.25	-	%
vs. Temperature	E_{ta}	$R_A, R_B=2k$ to $100k\Omega$, $C=0.1\mu\text{F}$	-	150	-	ppm / °C
vs. Operating Voltage	E_{ta}	$R_A, R_B=2k$ to $100k\Omega$, $C=0.1\mu\text{F}$	-	0.3	-	% / Volt
Timing Error (Monostable) (Note 3)						
Initial Accuracy	E_{tm}	$R_A, R_B=2k$ to $100k\Omega$, $C=0.1\mu\text{F}$	-	0.75	-	%
vs. Temperature	E_{tm}	$R_A, R_B=2k$ to $100k\Omega$, $C=0.1\mu\text{F}$	-	50	-	ppm / °C
vs. Operating Voltage	E_{tm}	$R_A, R_B=2k$ to $100k\Omega$, $C=0.1\mu\text{F}$	-	0.1	-	% / Volt
Matching Characteristics Between Each Section						
Initial Accuracy			-	0.5	1	%
vs. Temperature			-	±10	-	ppm / °C
vs. Operating Voltage			-	0.2	0.5	% / Volt

(Note 3) : Tested at $V^+=+5$ V to $+15$ V

■ TYPICAL APPLICATION



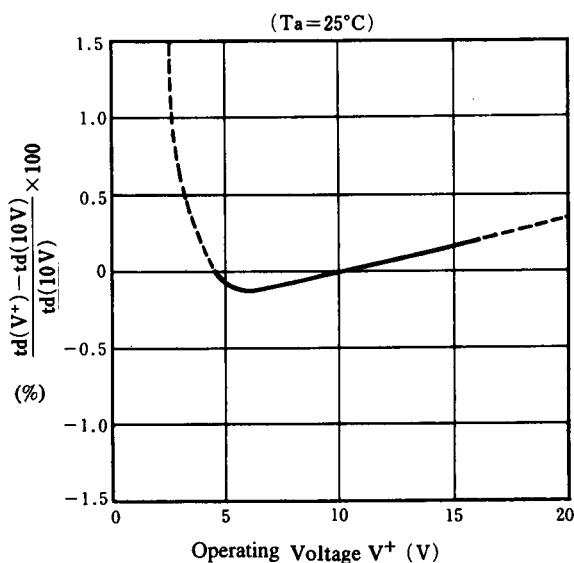
Missing pulse Dtection Circuit



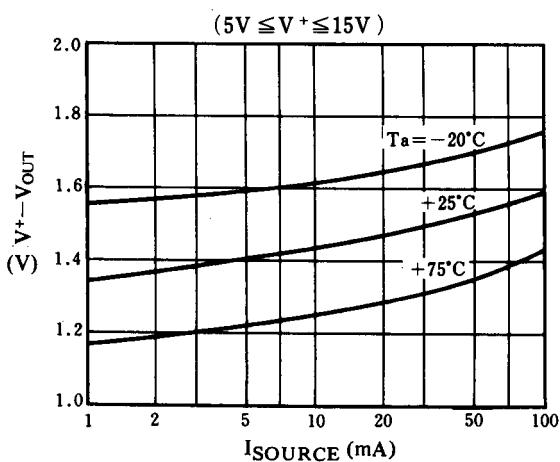
Pulse Width Moduration Circuit

■ TYPICAL CHARACTERISTICS

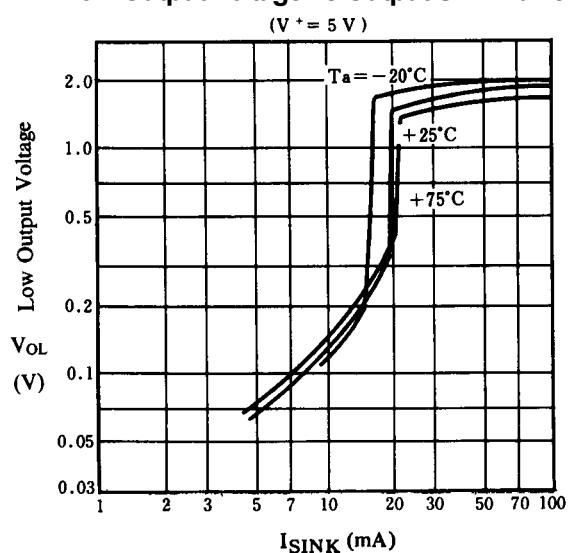
Delay Time vs. Operating Voltage



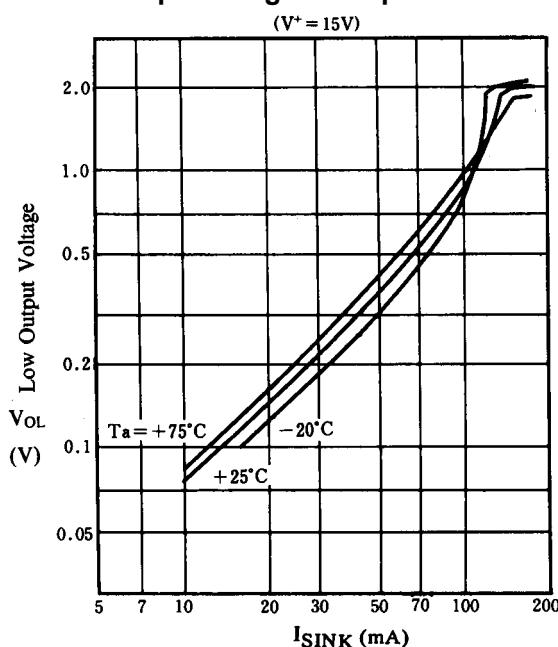
High Output Voltage Dsrop vs. Output Source Current



Low Output Voltage vs. Output Sink Current



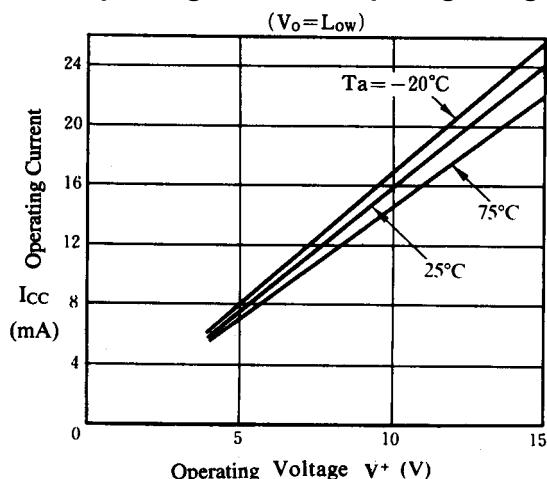
Low Output Voltage vs. Output Sink Current



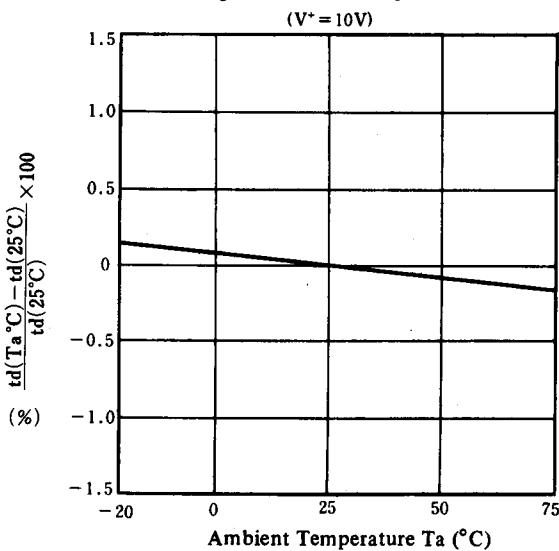
NJM556

■ TYPICAL CHARACTERISTICS

Operating Current vs. Operatig Voltage



Delay time vs. Temperature



[CAUTION]

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