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## NTE1535 Integrated Circuit TV Sync. Horizontal/Vertical OSC

**Features:**

- Single Chip for Vertical and Horizontal Deflection System
- Synchro Pulse Processing
- Vertical Deflection: Oscillator to Driver, Video Blanking
- Horizontal Deflection: AFC Comparator to Buffer, Video Blanking

**Absolute Maximum Ratings:** ( $T_A = +25^\circ\text{C}$  unless otherwise specified)

Maximum Supply Voltage,  $V_{12}, V_{24}$  ..... 14V  
 Allowable Power Dissipation ( $T_A \leq +65^\circ\text{C}$ ),  $P_D$  ..... 750mW  
 Operating Temperature Range,  $T_{opr}$  .....  $-20^\circ$  to  $+75^\circ\text{C}$   
 Storage Temperature Range,  $T_{stg}$  .....  $-55^\circ$  to  $+125^\circ\text{C}$

**Electrical Characteristics:** ( $T_A = +25^\circ\text{C}$ ,  $V_{12} = V_{24} = 12\text{V}$  unless otherwise specified)

Parameter	Test Conditions	Min	Typ	Max	Unit
Gain of Synchro Pulse Amp		-	6.8	-	
Burst Gate Pulse Peak		-	6.7	-	V
Delay Time		-	0.3	-	$\mu\text{s}$
Back Side Delay Time		-	3.7	-	$\mu\text{s}$
Vertical Frequency Holding Range		-	10.5	-	Hz
Pulling In Range		-	10.5	-	Hz
For $V_{CC}$ Varying	$V_{24} = 12\text{V} \pm 1\text{V}$			$\pm 1$	Hz
Oscillator		-	60	-	Hz
Vertical Retracing Period		-	700	-	$\mu\text{s}$
AFC Pulling In Range		-	750	-	Hz
AFC Center Error		-	-	$\pm 100$	Hz
Horizontal Frequency Oscillator		-	15.734	-	Hz
Oscillator Duty Cycle		-	50	-	%
For $V_{CC}$ Varying	$V_{12} = 12\text{V} \pm 1\text{V}$	-	-	$\pm 50$	Hz



### Pin Connection Diagram

