6 Channel EMI Pi-Filter Array with ESD Protection

This device is a 6 channel EMI filter array for data lines. Greater than -20 dB attenuation is obtained at frequencies from 800 MHz to 2.2 GHz. It also offers ESD protection – clamping transients from static discharges to protect delicate data line circuitry.

Features

- EMI Filtering and ESD Protection for Data Lines
- Integration of 30 Discretes Offers Cost and Space Savings
- Exceeds IEC61000-4-2 (Level 4) Specifications
- Low Profile Flip-Chip Packaging

Typical Applications

- EMI Filtering and ESD Protection for Data Lines
- Cell Phones
- Handheld Portables
- Notebook Computers
- MP3 Players

MAXIMUM RATINGS (T_A = 25°C)

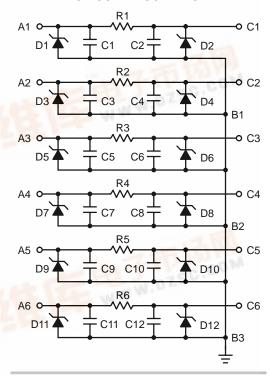
| Rating | Symbol | Value | Unit |
|--|------------------|------------------|------|
| ESD Discharge IEC61000–4–2, – Contact Discharge Human Body Model Machine Model | V _{PP} | 8.0 16 1.6 | kV |
| DC Power per Resistor | P _R | 100 | mW |
| DC Power per Package | P _T | 600 | mW |
| Junction Temperature | T_J | 150 | °C |
| Operating Temperature Range | T _{op} | -40 to +85 | °C |
| Storage Temperature Range | T _{stg} | -55 to +150 | °C |



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CIRCUIT DESCRIPTION





FLIP-CHIP CASE 499D **PLASTIC**

DEVICE MARKING

NUF6106YYWW

NUF6106= Specific Device Code

YY = Year WW = Work Week

ORDERING INFORMATION

| Device | Package | Shipping [†] | |
|-------------|-----------|-----------------------|--|
| NUF6106FCT1 | Flip-Chip | 3000/Tape & Reel | |

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.



ELECTRICAL CHARACTERISTICS (T_A = 25°C unless otherwise noted)

| Symbol | Characteristic | Min | Тур | Max | Unit |
|-------------------|--|-----|-----|-----|------|
| V _{BR} | I _Z = 10 mA | 6.0 | 7.0 | 8.0 | V |
| I _R | V _{RM} = 3.3 V per line | - | _ | 0.1 | μΑ |
| R _{I/O} | $I_R = 20 \text{ mA}$ | 80 | 100 | 120 | Ω |
| C _{line} | V _R = 2.5 V, f = 1 MHz (Note 1) | - | 21 | 23 | pF |

^{1.} Measured from Input/Output Pins to Ground

TYPICAL PERFORMANCE CURVES

(T_A = 25°C unless otherwise specified)

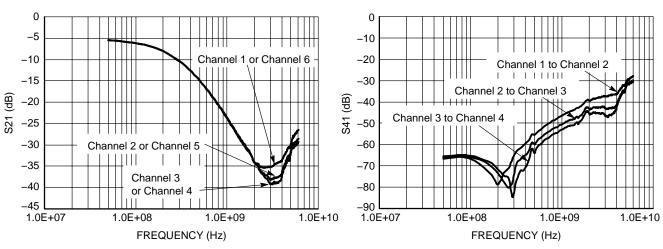


Figure 1. Insertion Loss Characteristics (S21 Measurement)

Figure 2. Analog Crosstalk Curve (S41 Measurement)

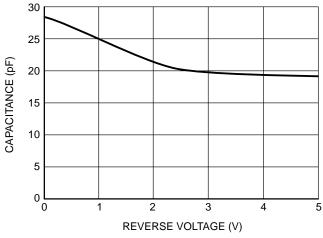


Figure 3. Typical Line Capacitance vs. Reverse Bias Voltage

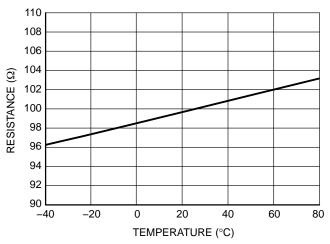


Figure 4. Typical Resistance Over Temperature

Printed Circuit Board Recommendations

| Parameter | 500 μm Pitch 300 μm Solder Ball | | |
|--------------------------|------------------------------------|--|--|
| PCB Pad Size | 250 μm +25 -0 | | |
| Pad Shape | Round | | |
| Pad Type | NSMD | | |
| Solder Mask Opening | 350 μm ±25 | | |
| Solder Stencil Thickness | 125 μm | | |
| Stencil Aperture | 250 x 250 μm sq. | | |
| Solder Flux Ratio | 50/50 | | |
| Solder Paste Type | No Clean Type 3 or Finer | | |
| Trace Finish | OSP Cu | | |
| Trace Width | 150 μm Max | | |

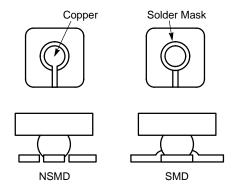


Figure 5. Solder Mask versus Non-Solder Mask Definition

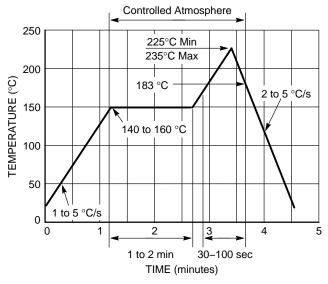


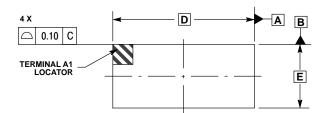
Figure 6. Solder Reflow Profile

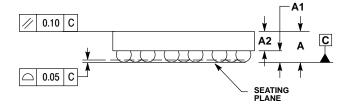
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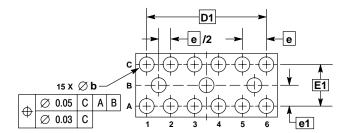
PACKAGE DIMENSIONS

15 PIN FLIP-CHIP CSP

CASE 499D-01 **ISSUE O**







- 1. DIMENSIONING AND TOLERANCING PER
- ASME Y14.5M, 1994.
 CONTROLLING DIMENSION: MILLIMETER.
 COPLANARITY APPLIES TO SPHERICAL

CROWNS OF SOLDER BALLS.

| MILLIMETERS | | |
|-------------|--|--|
| MIN | MAX | |
| | 0.700 | |
| 0.210 | 0.270 | |
| 0.380 | 0.430 | |
| 2.960 BSC | | |
| 1.330 BSC | | |
| 0.290 | 0.340 | |
| 0.500 BSC | | |
| 0.435 BSC | | |
| 2.500 BSC | | |
| | MIN 0.210 0.380 2.960 1.330 0.290 0.500 0.435 | |

0.870 BSC

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