



CLARE

	PLA140L	Units
Load Voltage	400	V
Load Current	200	mA
Max R _{ON}	13	Ω

Features

- Small 6 Pin DIP Package
- Low Drive Power Requirements (TTL/CMOS Compatible)
- No Moving Parts
- High Reliability
- Arc-Free With No Snubbing Circuits
- 3750V_{RMS} Input/Output Isolation
- FCC Compatible
- VDE Compatible
- No EMI/RFI Generation
- Machine Insertable, Wave Solderable
- Current Limiting, Surface Mount and Tape & Reel Versions Available

Applications

- Instrumentation
 - Multiplexers
 - Data Acquisition
 - Electronic Switching
 - I/O Subsystems
 - Meters (Watt-Hour, Water, Gas)
- Medical Equipment—Patient/Equipment Isolation
- Security
- Aerospace
- Industrial Controls
- Automotive

Description

PLA140L is a 1-Form-A solid state relay which uses optically coupled MOSFET technology to provide 3750V of input to output isolation. The efficient MOSFET switches and photovoltaic die use Clare's patented Optomos architecture. The optically-coupled input is controlled by a highly efficient GaAlAs infrared LED. The PLA140L also contains a built in load current limiting feature. This combined with a low on resistance and very high load current handling capabilities makes it suitable for a variety of high performance switching applications.

Approvals

- UL Recognized: File Number E76270
- CSA Certified: File Number LR 43639-10
- BSI Certified to:
 - BS EN 60950:1992 (BS7002:1992) Certificate #: 7344
 - BS EN 41003:1993 Certificate #: 7344

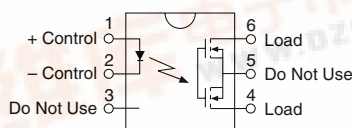
Ordering Information

Part #	Description
PLA140L	6 Pin DIP (50/Tube)
PLA140LS	6 Pin Surface Mount (50/Tube)
PLA140LSTR	6 Pin Surface Mount (1000/Reel)

Pin Configuration

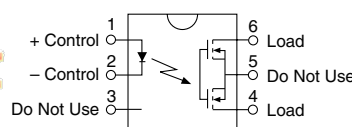
PLA140L Pinout

AC/DC Configuration

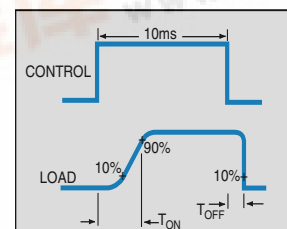


PLA140L Pinout

AC/DC Configuration



Switching Characteristics of Normally Open (Form A) Devices



**PLA140L****Absolute Maximum Ratings (@ 25° C)**

Parameter	Min	Typ	Max	Units
Input Power Dissipation	-	-	150 ¹	mW
Input Control Current	-	-	50	mA
Peak (10ms)	-	-	1	A
Reverse Input Voltage	-	-	5	V
Total Power Dissipation	-	-	800 ²	mW
Isolation Voltage Input to Output	3750	-	-	V _{RMS}
Operational Temperature	-40	-	+85	°C
Storage Temperature	-40	-	+125	°C
Soldering Temperature DIP Package	-	-	+260	°C
Surface Mount Package (10 Seconds Max.)	-	-	+220	°C

¹ Derate Linearly 1.33 mW/°C² Derate Linearly 6.67 mW/°C

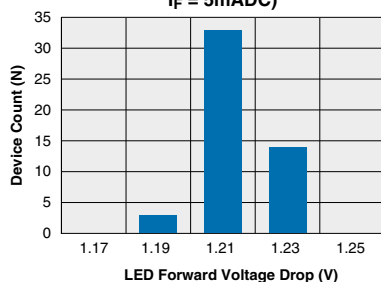
Absolute Maximum Ratings are stress ratings. Stresses in excess of these ratings can cause permanent damage to the device. Functional operation of the device at these or any other conditions beyond those indicated in the operational sections of this data sheet is not implied. Exposure of the device to the absolute maximum ratings for an extended period may degrade the device and effect its reliability.

Electrical Characteristics

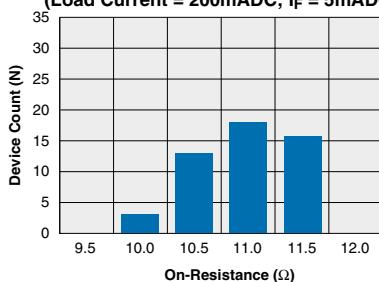
Parameter	Conditions	Symbol	Min	Typ	Max	Units
Output Characteristics @ 25°C						
Load Voltage (Peak)	-	V _L	-	-	400	V
Load Current (Continuous)	-	I _L	-	-	200	mA
AC/DC Configuration	-	I _L	-	-	350	mA
DC Configuration	-	I _L	-	-	350	mA
Peak Load Current	10ms	I _L	-	-	500	mA
On-Resistance	I _L =200mA	R _{ON}	-	10	13	Ω
AC/DC Configuration	I _L =350mA	R _{ON}	-	3	4	Ω
DC Configuration	I _L =350mA	R _{ON}	-	3	4	Ω
Off-State Leakage Current	V _L =400V	I _{LEAK}	-	-	1	μA
Switching Speeds	I _F =5mA, V _L =10V	T _{ON}	-	-	5	ms
Turn-On	I _F =5mA, V _L =10V	T _{OFF}	-	-	3	ms
Turn-Off	I _F =5mA, V _L =10V	T _{OFF}	-	-	3	ms
Load current limit	I _F =5mA, T=25°C	I _{CL}	240	-	380	mA
Output Capacitance	50V; f=1MHz	C _{OUT}	-	65	-	pF
Input Characteristics @ 25°C						
Input Control Current	I _L =200mA	I _F	5	-	50	mA
Input Dropout Current	-	I _F	0.4	0.7	-	mA
Input Voltage Drop	I _F =5mA	V _F	0.9	1.2	1.4	V
Reverse Input Voltage	-	V _R	-	-	5	V
Reverse Input Current	V _R =5V	I _R	-	-	10	μA
Common Characteristics @ 25°C						
Input to Output Capacitance	-	C _{I/O}	-	3	-	pF
Input to Output Isolation	-	V _{I/O}	3750	-	-	V _{RMS}

PERFORMANCE DATA*

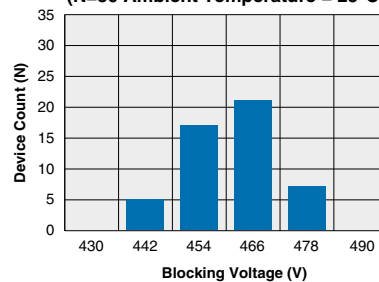
PLA140L
Typical LED Forward Voltage Drop
(N=50 Ambient Temperature = 25°C;
 $I_F = 5\text{mA}$)



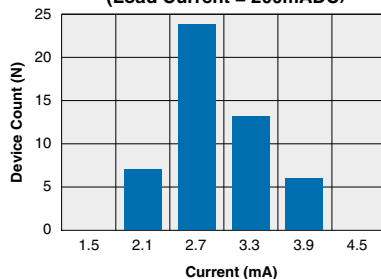
PLA140L
Typical On-Resistance Distribution
(N=50 Ambient Temperature = 25°C)
(Load Current = 200mA; $I_F = 5\text{mA}$)



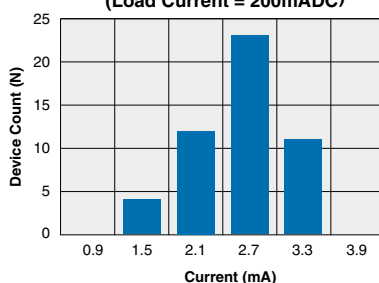
PLA140L
Typical Blocking Voltage Distribution
(N=50 Ambient Temperature = 25°C)



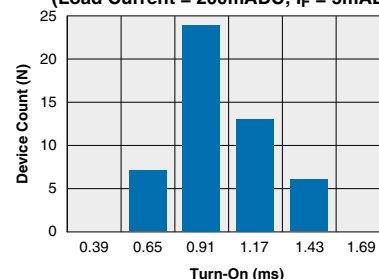
PLA140L
Typical I_F for Switch Operation
(N=50 Ambient Temperature = 25°C)
(Load Current = 200mA)



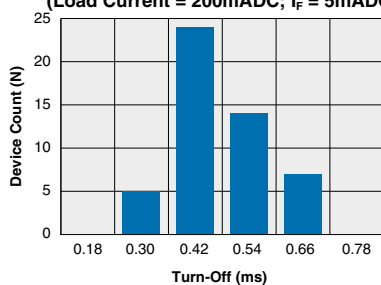
PLA140L
Typical I_F for Switch Dropout
(N=50 Ambient Temperature = 25°C)
(Load Current = 200mA)



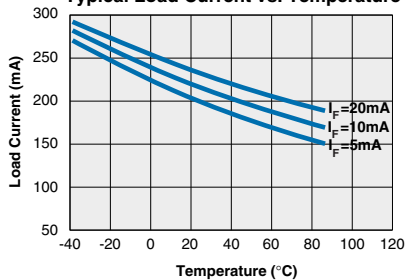
PLA140L
Typical Turn-On Time
(N=50 Ambient Temperature = 25°C)
(Load Current = 200mA; $I_F = 5\text{mA}$)



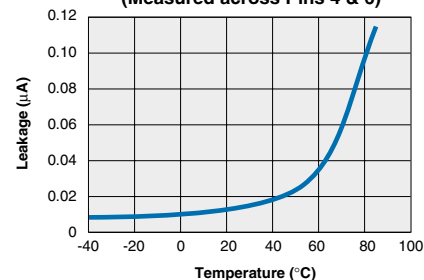
PLA140L
Typical Turn-Off Time
(N=50 Ambient Temperature = 25°C)
(Load Current = 200mA; $I_F = 5\text{mA}$)



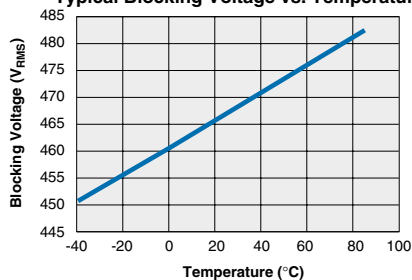
PLA140L
Typical Load Current vs. Temperature



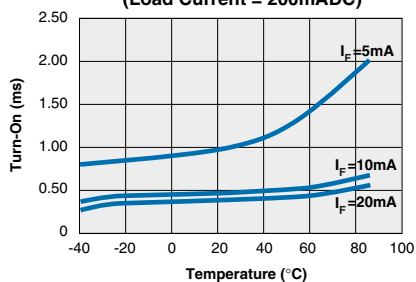
PLA140L
Typical Leakage vs. Temperature
(Measured across Pins 4 & 6)



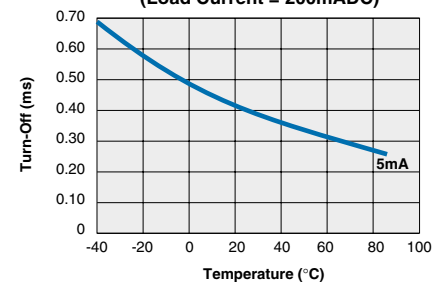
PLA140L
Typical Blocking Voltage vs. Temperature



PLA140L
Typical Turn-On vs. Temperature
(Load Current = 200mA)

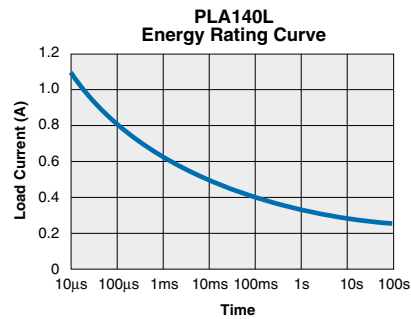
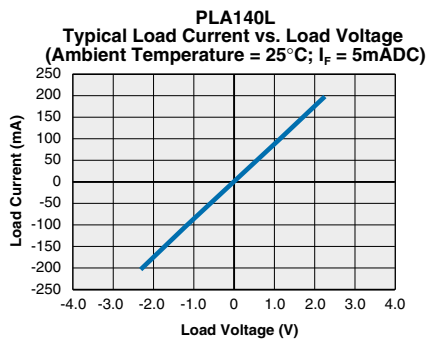
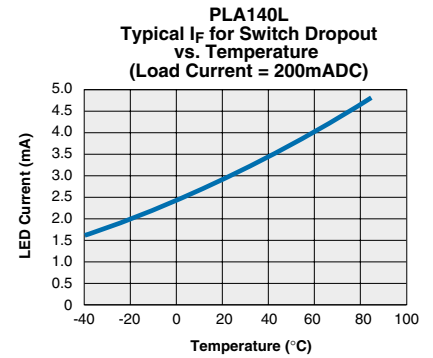
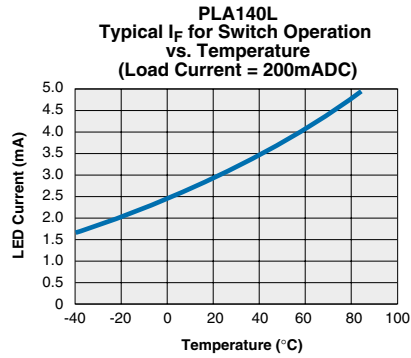
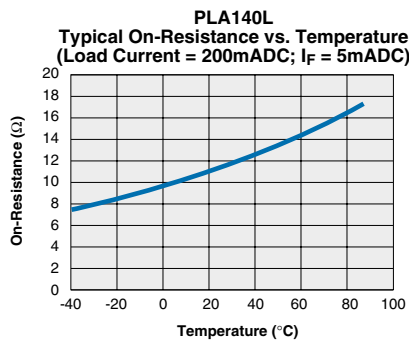
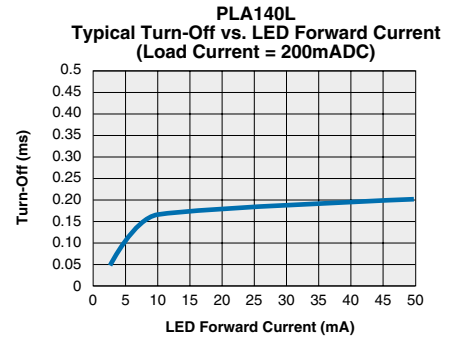
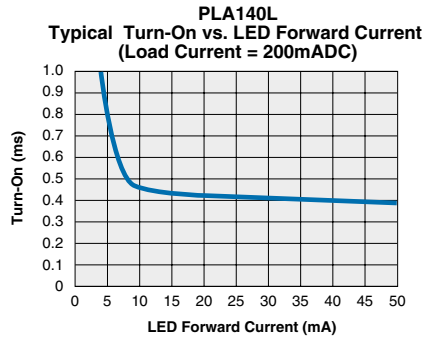
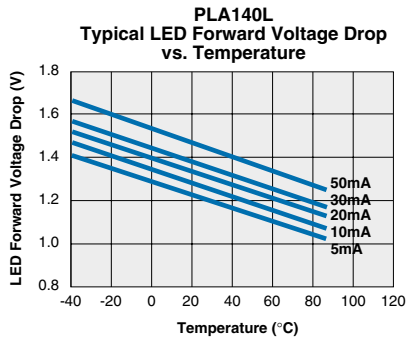


PLA140L
Typical Turn-Off vs. Temperature
(Load Current = 200mA)



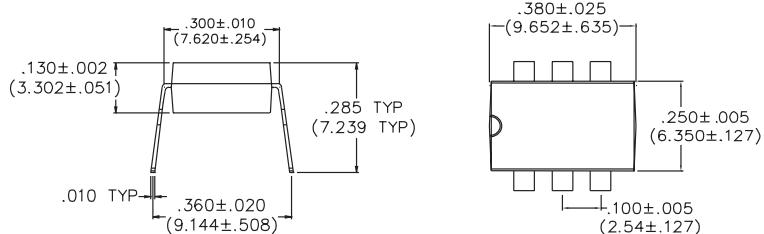
* The Performance data shown in the graphs above is typical of device performance. For guaranteed parameters not indicated in the written specifications, please contact our application department.

PERFORMANCE DATA*

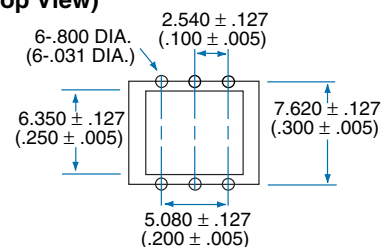


MECHANICAL DIMENSIONS

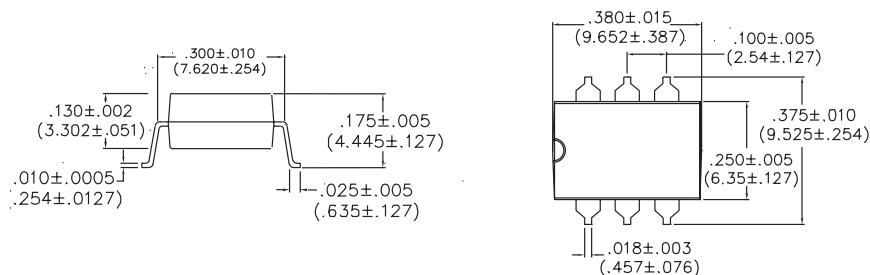
6Pin DIP Through Hole (Standard)



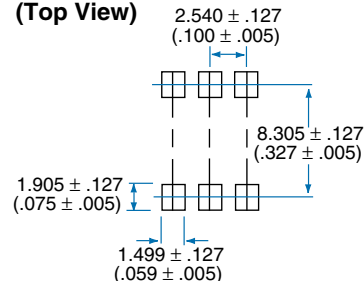
PC Board Pattern (Top View)



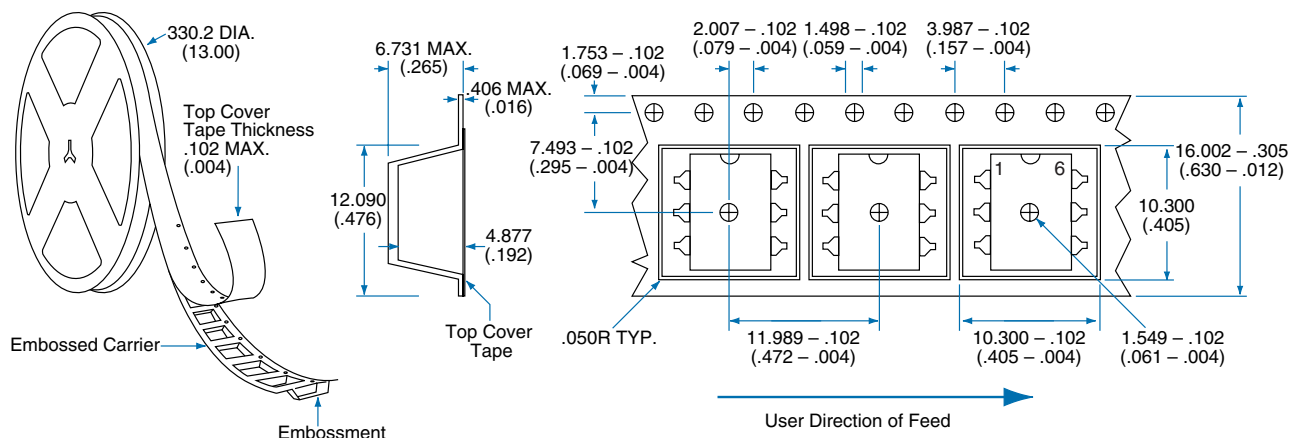
6Pin DIP Surface Mount ("S" Suffix)



PC Board Pattern (Top View)



Tape and Reel Packaging for 6 Pin Power DIP Surface Mount Package





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