

**SUD23N06-31L**

Vishay Siliconix

N-Channel 60-V (D-S), 175°C MOSFET, Logic Level**PRODUCT SUMMARY**

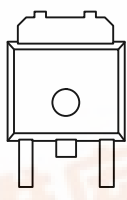
V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A) ^a
60	0.031 @ $V_{GS} = 10$ V	23
	0.045 @ $V_{GS} = 4.5$ V	19.5

FEATURES

- TrenchFET® Power MOSFET
- 175°C Junction Temperature

APPLICATIONS

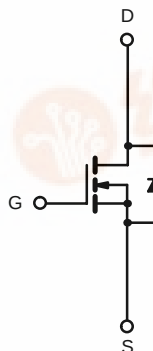
- Automotive
 - 12-V Systems

TO-252

Top View

Drain Connected to Tab

Ordering Information: SUD23N06-31L



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_C = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Limit	Unit
Gate-Source Voltage	V_{GS}	± 20	V
Continuous Drain Current ($T_J = 175^\circ\text{C}$) ^b	I_D	$T_C = 25^\circ\text{C}$	23
		$T_C = 100^\circ\text{C}$	16.5
Pulsed Drain Current	I_{DM}	50	A
Continuous Source Current (Diode Conduction)	I_S	23	
Avalanche Current	I_{AR}	20	
Repetitive Avalanche Energy (Duty Cycle $\leq 1\%$)	E_{AR}	20	mJ
Maximum Power Dissipation	P_D	$T_C = 25^\circ\text{C}$	100
		$T_A = 25^\circ\text{C}$	3 ^a
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Limit	Unit
Maximum Junction-to-Ambient ^a	R_{thJA}	$t \leq 10$ sec.	18	$^\circ\text{C/W}$
		Steady State	40	
Maximum Junction-to-Case	R_{thJC}	3.2	4	

Notes:

^a Surface mounted on 1" x 1" FR4 Board, $t \leq 10$ sec.

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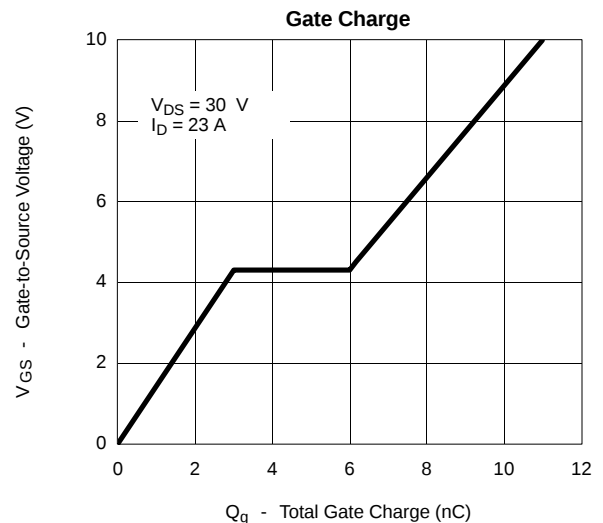
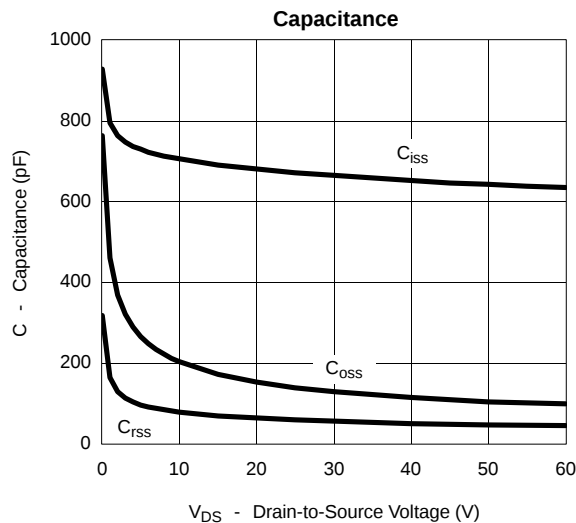
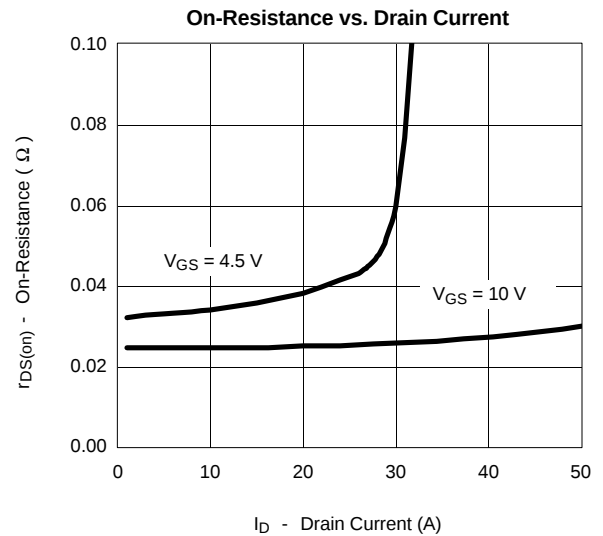
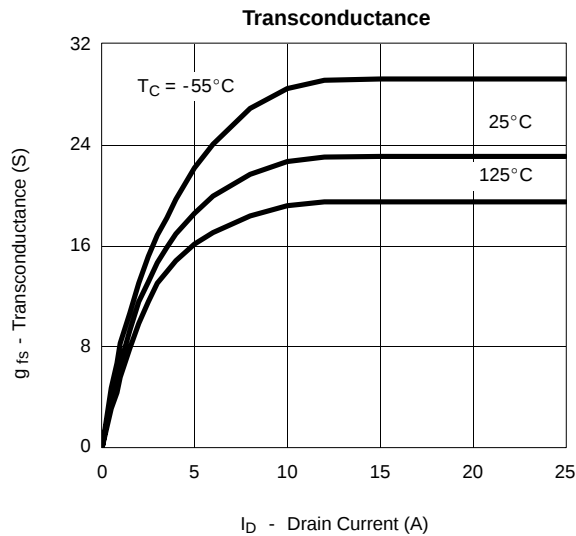
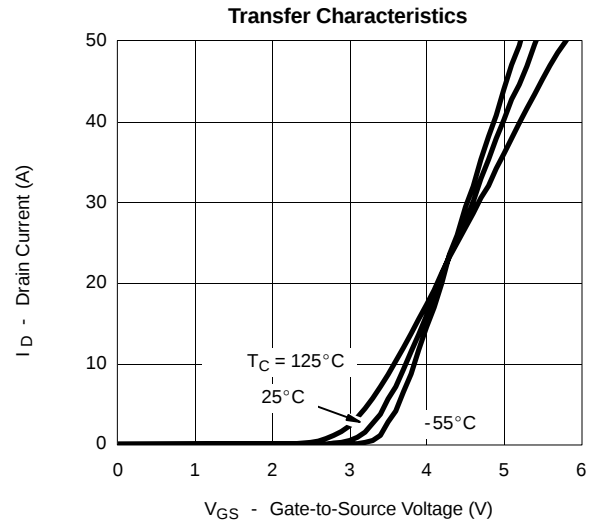
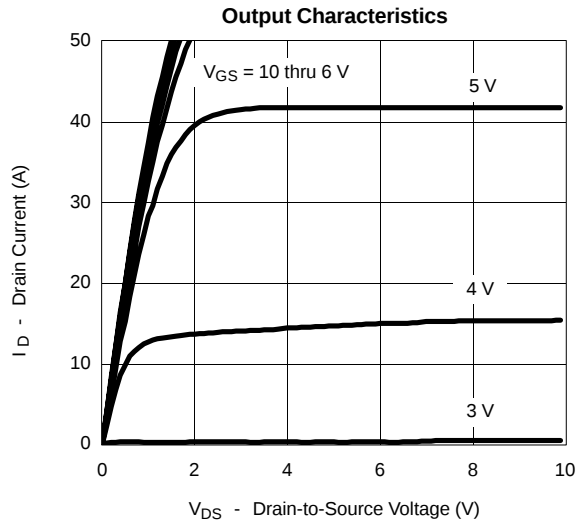
SPECIFICATIONS (T _J = 25 °C UNLESS OTHERWISE NOTED)						
Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0 V, I _D = 250 μA	60			V
Gate Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250 μA	1.0	2.0	3.0	
Gate-Body Leakage	I _{GSS}	V _{DS} = 0 V, V _{GS} = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 48 V, V _{GS} = 0 V			1	μA
		V _{DS} = 48 V, V _{GS} = 0 V, T _J = 125°C			50	
		V _{DS} = 48 V, V _{GS} = 0 V, T _J = 175°C			250	
On-State Drain Current ^b	I _{D(on)}	V _{DS} = 5 V, V _{GS} = 10 V	50			A
Drain-Source On-State Resistance ^b	r _{DS(on)}	V _{GS} = 10 V, I _D = 15 A		0.025	0.031	Ω
		V _{GS} = 10 V, I _D = 15 A, T _J = 125°C			0.055	
		V _{GS} = 10 V, I _D = 15 A, T _J = 175°C			0.069	
		V _{GS} = 4.5 V, I _D = 10 A		0.037	0.045	
Forward Transconductance ^b	g _{fs}	V _{DS} = 15 V, I _D = 15 A		20		S
Dynamic						
Input Capacitance	C _{iss}	V _{GS} = 0 V, V _{DS} = 25 V, f = 1 MHz		670		pF
Output Capacitance	C _{oss}			140		
Reverse Transfer Capacitance	C _{rss}			60		
Total Gate Charge ^c	Q _g	V _{DS} = 30 V, V _{GS} = 10 V, I _D = 23 A		11	17	nC
Gate-Source Charge ^c	Q _{gs}			3		
Gate-Drain Charge ^c	Q _{gd}			3		
Turn-On Delay Time ^c	t _{d(on)}	V _{DD} = 30 V, R _L = 1.3 Ω I _D ≅ 23 A, V _{GEN} = 10 V, R _G = 2.5 Ω		8	15	ns
Rise Time ^c	t _r			15	25	
Turn-Off Delay Time ^c	t _{d(off)}			30	45	
Fall Time ^c	t _f			25	40	
Source-Drain Diode Ratings and Characteristics (T _C = 25°C)						
Pulsed Current	I _{SM}				50	A
Diode Forward Voltage	V _{SD}	I _F = 15 A, V _{GS} = 0 V		1.0	1.5	V
Reverse Recovery Time	t _{rr}	I _F = 15 A, di/dt = 100 A/μs		30	60	ns

Notes:

- b. For design aid only; not subject to production testing.
- c. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- d. Independent of operating temperature.



TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

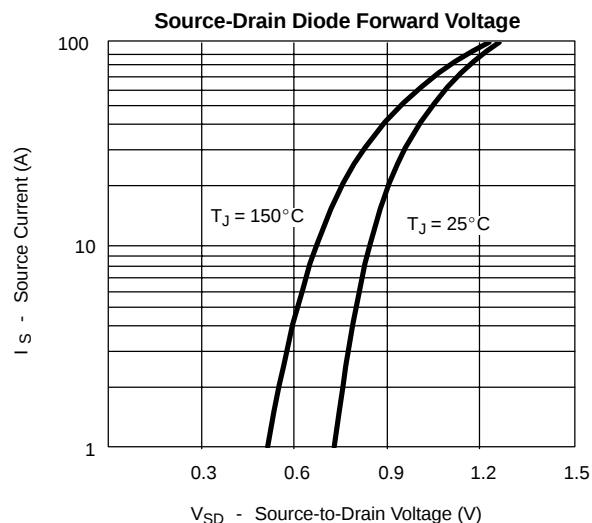
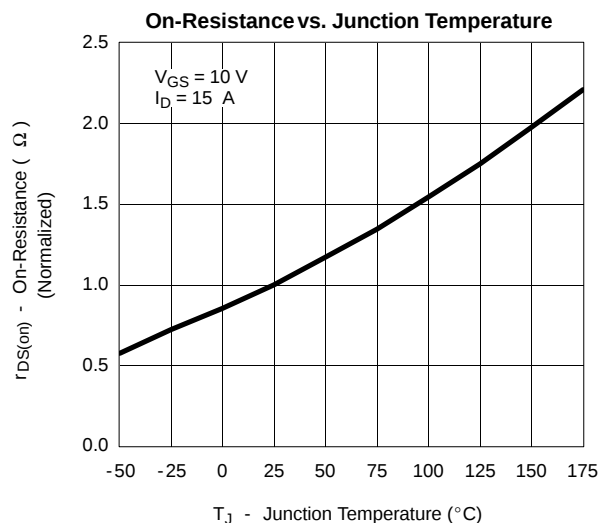


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TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



THERMAL RATINGS

