



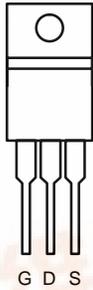
**SUP45N03-13L**  
Vishay Siliconix

**N-Channel 30-V (D-S), 175° C MOSFET**

PRODUCT SUMMARY		
$V_{(BR)DSS}$ (V)	$r_{DS(on)}$ ( $\Omega$ )	$I_D$ (A)
30	0.013 @ $V_{GS} = 10$ V	45 <sup>a</sup>
	0.02 @ $V_{GS} = 4.5$ V	45 <sup>a</sup>

**175° C Rated**  
Maximum Junction Temperature

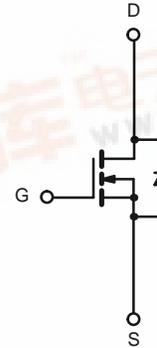
TO-220AB



Top View

SUP45N03-13L

DRAIN connected to TAB



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ( $T_C = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)				
Parameter	Symbol	Limit	Unit	
Drain-Source Voltage	$V_{DS}$	30	V	
Gate-Source Voltage	$V_{GS}$	$\pm 10$		
Continuous Drain Current ( $T_J = 175^\circ\text{C}$ )	$I_D$	$T_C = 25^\circ\text{C}$	45 <sup>a</sup>	
		$T_C = 125^\circ\text{C}$	34 <sup>a</sup>	
Pulsed Drain Current	$I_{DM}$	100	A	
Avalanche Current	$I_{AR}$	45		
Repetitive Avalanche Energy <sup>b</sup>	$E_{AR}$	L = 0.1 mH	100	
Maximum Power Dissipation <sup>b</sup>		$T_C = 25^\circ\text{C}$	88 <sup>c</sup>	
Operating Junction and Storage Temperature Range		$T_J, T_{stg}$	-55 to 175	$^\circ\text{C}$

THERMAL RESISTANCE RATINGS			
Parameter	Symbol	Limit	Unit
Junction-to-Ambient	$R_{thJA}$	85	$^\circ\text{C}/\text{W}$
Junction-to-Case			

Notes

- a. Package limited.
- b. Duty cycle  $\leq 1\%$ .
- c. See SOA curve for voltage derating.
- d. When mounted on 1" square PCB (FR-4 material).

For SPICE model information via the Worldwide Web: <http://www.vishay.com/www/product/spice.htm>



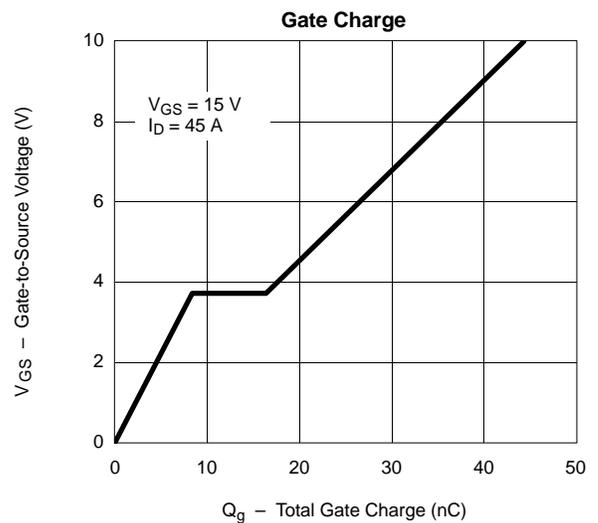
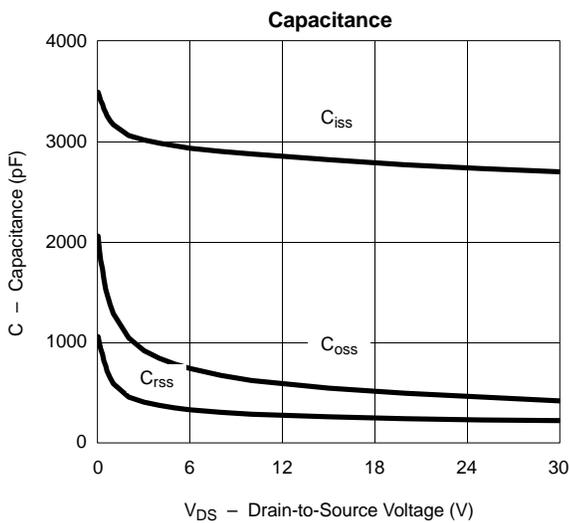
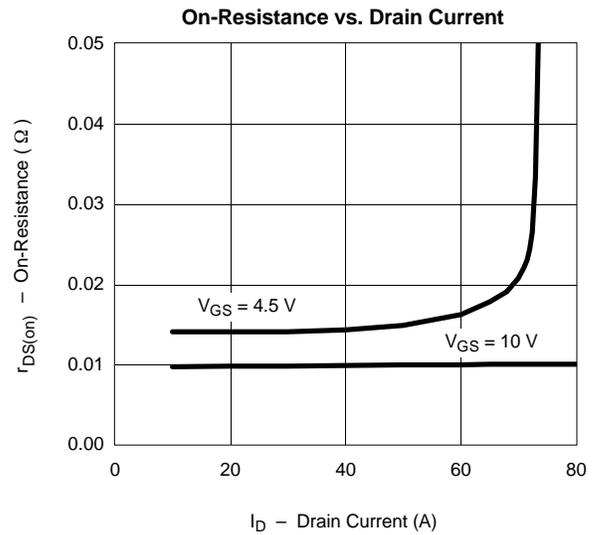
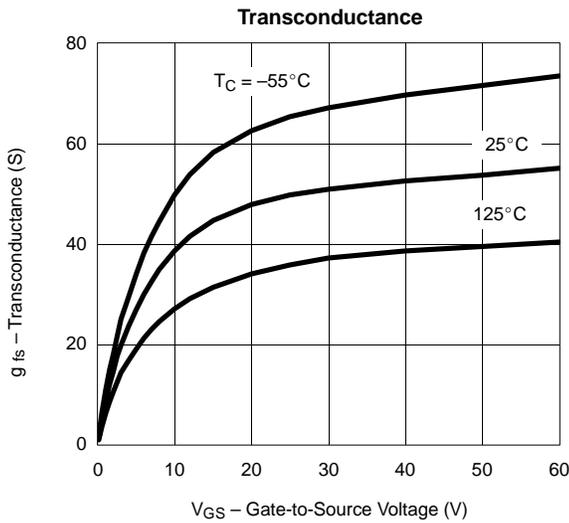
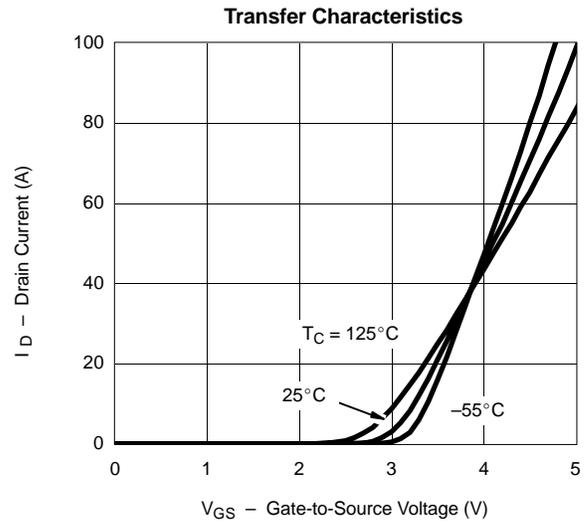
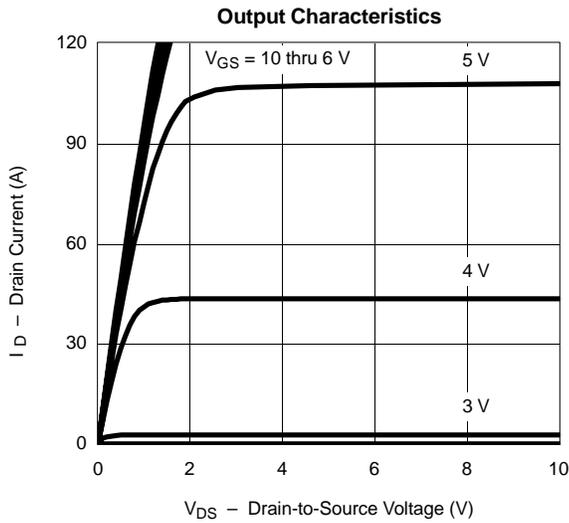
<b>MOSFET SPECIFICATIONS (T<sub>J</sub> = 25 °C UNLESS OTHERWISE NOTED)</b>						
Parameter	Symbol	Test Condition	Min	Typ	Max	Unit
<b>Static</b>						
Drain-Source Breakdown Voltage	V <sub>(BR)DSS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	30			V
Gate Threshold Voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>DS</sub> = 250 μA	1		3	
Gate-Body Leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V			± 100	nA
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V			1	μA
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 125 °C			50	
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 175 °C			150	
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	45			A
Drain-Source On-State Resistance <sup>a</sup>	r <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 45 A		0.01	0.013	Ω
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 45 A, T <sub>J</sub> = 125 °C		0.0155	0.02	
		V <sub>GS</sub> = 10 V, I <sub>D</sub> = 45 A, T <sub>J</sub> = 175 °C		0.02	0.026	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 20 A		0.0145	0.02	
Forward Transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 45 A	20			S
<b>Dynamic<sup>b</sup></b>						
Input Capacitance	C <sub>iSS</sub>	V <sub>GS</sub> = 0 V, V <sub>DS</sub> = 25 V, f = 1 MHz		2730		pF
Output Capacitance	C <sub>oss</sub>			450		
Reverse Transfer Capacitance	C <sub>rSS</sub>			220		
Total Gate Charge <sup>c</sup>	Q <sub>g</sub>	V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 45 A		45	70	nC
Gate-Source Charge <sup>c</sup>	Q <sub>gs</sub>			8.5		
Gate-Drain Charge <sup>c</sup>	Q <sub>gd</sub>			8		
Turn-On Delay Time <sup>c</sup>	t <sub>d(on)</sub>	V <sub>DD</sub> = 15 V, R <sub>L</sub> = 0.33 Ω I <sub>D</sub> = 45 A, V <sub>GEN</sub> = 10 V, R <sub>G</sub> = 2.5 Ω		11	20	ns
Rise Time <sup>c</sup>	t <sub>r</sub>			9	20	
Turn-Off Delay Time <sup>c</sup>	t <sub>d(off)</sub>			38	70	
Fall Time <sup>c</sup>	t <sub>f</sub>			11	20	
<b>Source-Drain Diode Ratings and Characteristics (T<sub>C</sub> = 25 °C)<sup>b</sup></b>						
Continuous Current	I <sub>S</sub>				45	A
Pulsed Current	I <sub>SM</sub>				100	
Forward Voltage <sup>a</sup>	V <sub>SD</sub>	I <sub>F</sub> = 45 A, V <sub>GS</sub> = 0 V		1	1.3	V
Reverse Recovery Time	t <sub>rr</sub>	I <sub>F</sub> = 45 A, di/dt = 100 A/μs		35	70	ns
Peak Reverse Recovery Current	I <sub>RM(REC)</sub>			1.7		A
Reverse Recovery Charge	Q <sub>rr</sub>				0.03	

Notes:

- a. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2%.
- e. Guaranteed by design, not subject to production testing.
- b. Independent of operating temperature.



**TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)**

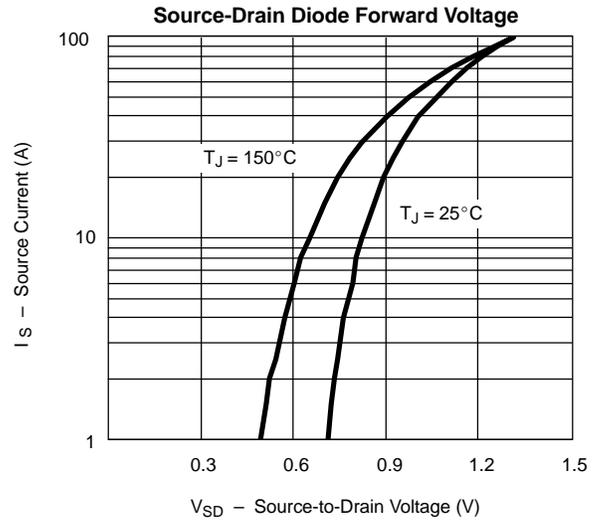
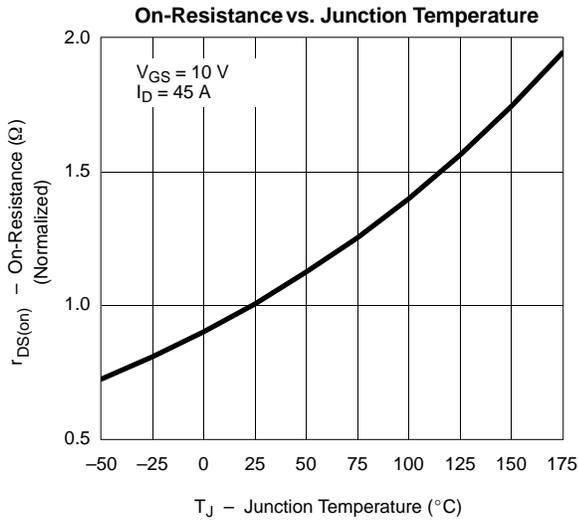


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## TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)



## THERMAL RATINGS

