TISP4082LP
SYMMETRICAL TRANSIENT VOLTAGE SUPPRESSORS

MAY 1996 - REVISED SEPTEMBER 1997

TELECOMMUNICATION SYSTEM SECONDARY PROTECTION

- **Ion-Implanted Breakdown Region**
  Precise and Stable Voltage
  Low Voltage Overshoot under Surge

DEVICE	$V_{(Z)}$ V	$V_{(BO)}$ V
'4082LP	58	82

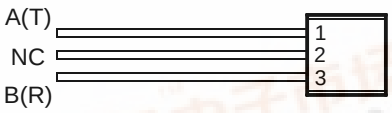
- **Planar Passivated Junctions**
  Low Off-State Current < 10 µA
- **Rated for International Surge Wave Shapes**

WAVE SHAPE	STANDARD	I_{TSP} A
10/700 µs	CCITT IX K17	25

- **Package Options**

PACKAGE	PACKING	PART # SUFFIX
LP	Bulk	None
LP with fomed leads	Tape and Reel	R

LP PACKAGE
(TOP VIEW)



NC - No internal connection

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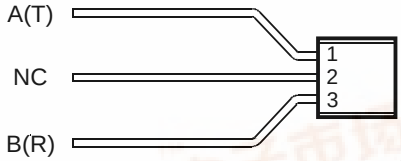
LP PACKAGE
WITH FORMED LEADS
(TOP VIEW)



NC - No internal connection

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device symbol

description

The TISP4082LP is designed specifically for telephone equipment protection against lightning and transients induced by a.c. power lines. These devices consist of a bidirectional suppressor element connecting the A and B terminals. They will suppress inter-wire voltage transients.

Transients are initially clipped by zener action until the voltage rises to the breakover level, which causes the device to crowbar. The high crowbar holding current prevents d.c. latchup as the transient subsides.

These monolithic protection devices are fabricated in ion-implanted planar structures to ensure precise and matched breakover control and are virtually transparent to the system in normal operation.

absolute maximum ratings at 25°C case temperature (unless otherwise noted))

RATING	SYMBOL	VALUE	UNIT
Non-repetitive peak on-state pulse current (see Notes 1, 2 and 3) 5/310 µs (CCITT IX K17, open-circuit voltage wave shape 1.5 kV, 10/700 µs)	I_{TSP}	25	A
Non-repetitive peak on-state current, 50 Hz, 1 s (see Notes 1 and 2)	I_{TSM}	2.5	A rms
Initial rate of rise of on-state current, Linear current ramp, Maximum ramp value < 38 A	di_T/dt	250	A/µs
Junction temperature	T_J	150	°C
Operating free - air temperature range		0 to 70	°C
Storage temperature range	T_{stg}	-40 to +150	°C
Lead temperature 1.5 mm from case for 10 s	T_{lead}	260	°C

NOTES: 1. Above 70°C, derate linearly to zero at 150°C case temperature
2. This value applies when the initial case temperature is at (or below) 70°C. The surge may be repeated after the device has returned to thermal equilibrium.
3. Most PTT's quote an unloaded voltage waveform. In operation the TISP essentially shorts the generator output. The resulting loaded current waveform is specified.

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electrical characteristics, T_J = 25°C

PARAMETER		TEST CONDITIONS		MIN	TYP	MAX	UNIT
V_Z	Reference zener voltage	I_Z = ± 1mA		± 58			V
$^{\propto}V_Z$	Temperature coefficient of reference voltage				0.1		%/°C
$V_{(BO)}$	Breakover voltage	(see Notes 4 and 5)				± 82	V
$I_{(BO)}$	Breakover current	(see Note 4)		± 0.15		± 0.6	A
V_{TM}	Peak on-state voltage	I_T = ± 5 A	(see Notes 4 and 5)		± 2.2	± 3	V
I_H	Holding current	(see Note 4)		± 150			mA
dv/dt	Critical rate of rise of off-state voltage	(see Note 6)				± 5	kV/µs
I_D	Off-state leakage current	V_D = ± 50 V				± 10	µA
C_{off}	Off-state capacitance	V_D = 0	f = 1 kHz		110	200	pF

NOTES: 4. These parameters must be measured using pulse techniques, t_w = 100 µs, duty cycle ≤ 2%.
5. These parameters are measured with voltage sensing contacts seperate from the current carrying contacts located within 3.2 mm (0.125 inch) from the device body.
6. Linear rate of rise, maximum voltage limited to 80 % V_Z (minimum).

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thermal characteristics

PARAMETER		MIN	TYP	MAX	UNIT
$R_{\theta JA}$	Junction to free air thermal resistance			156	°C/W

PARAMETER MEASUREMENT INFORMATION

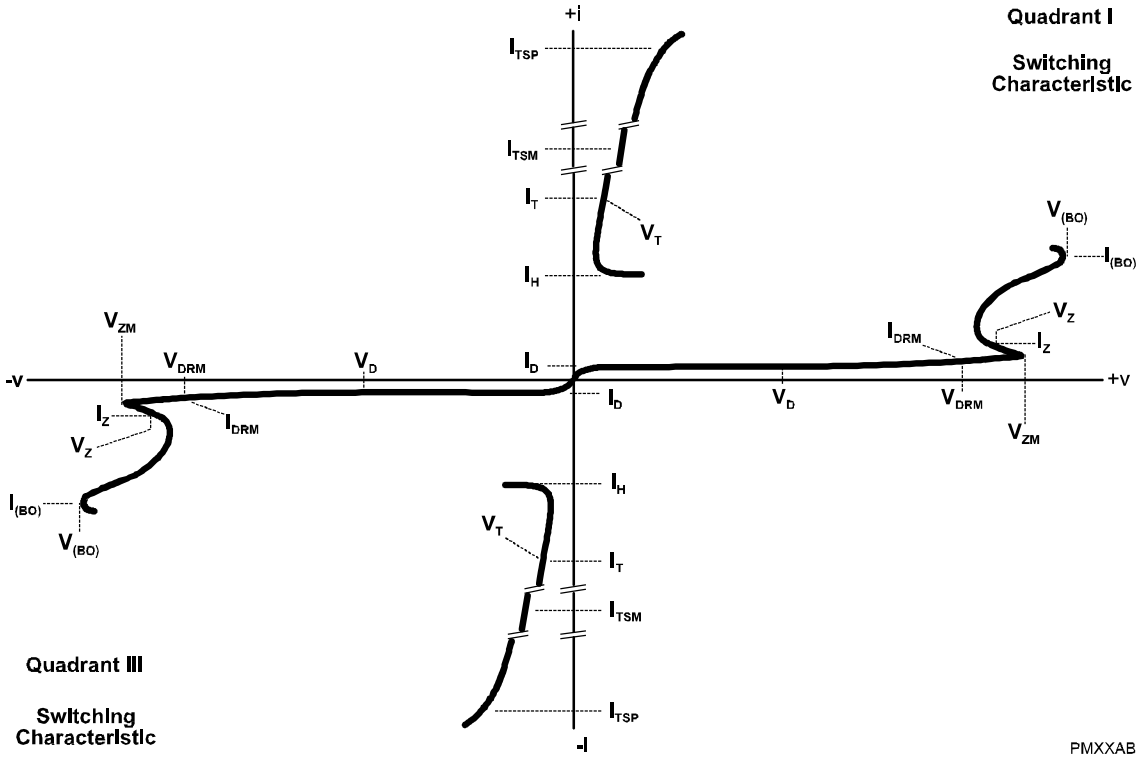
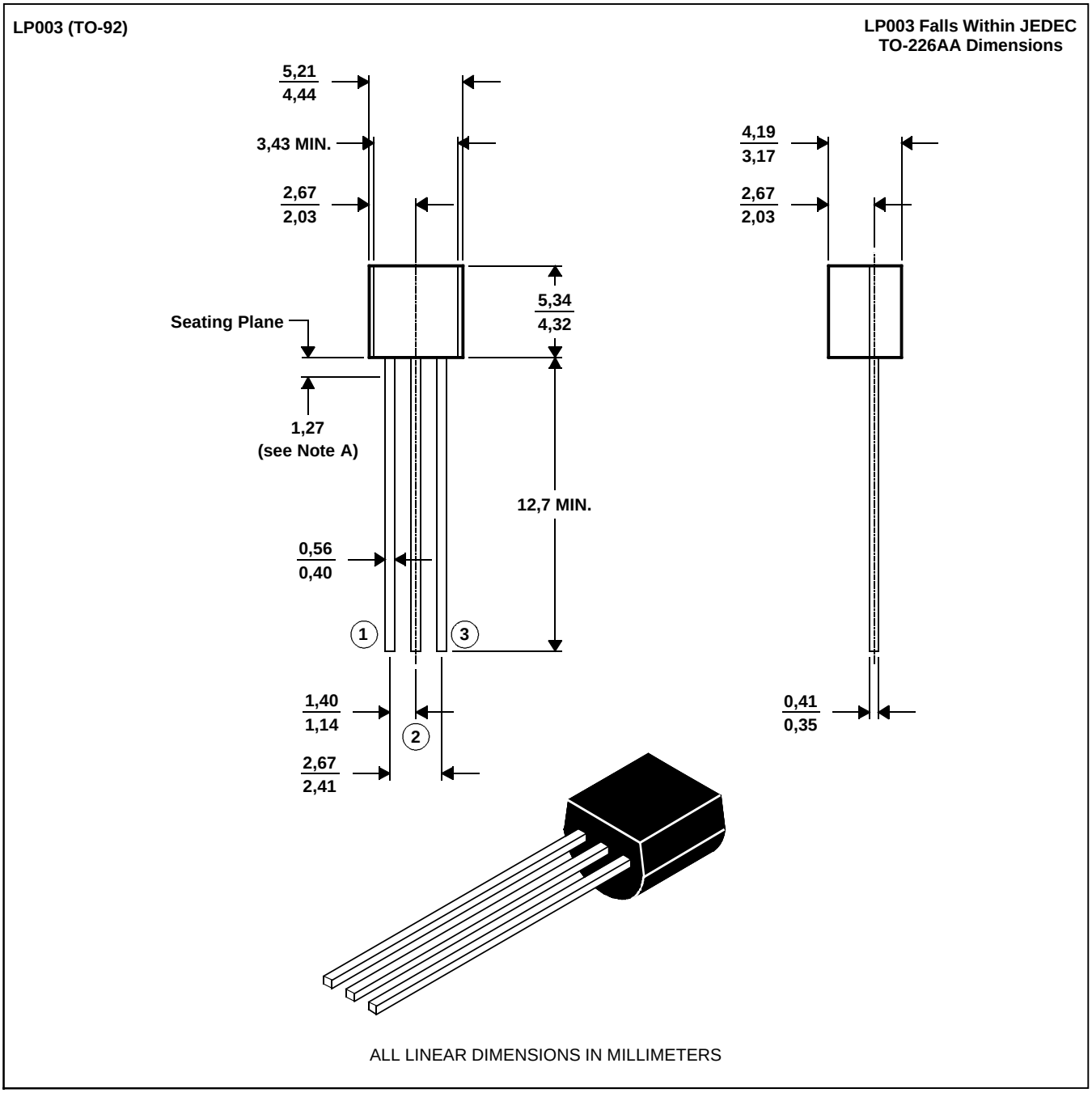


Figure 1. VOLTAGE-CURRENT CHARACTERISTICS FOR TERMINALS A AND B

MECHANICAL DATA

LP003 (TO-92)
3-pin cylindical plastic package

This single-in-line package consists of a circuit mounted on a lead frame and encapsulated within a plastic compound. The compound will withstand soldering temperature with no deformation, and circuit performance characteristics will remain stable when operated in high humidity conditions. Leads require no additional cleaning or processing when used in soldered assembly.



NOTE A: Lead dimensions are not controlled in this area.

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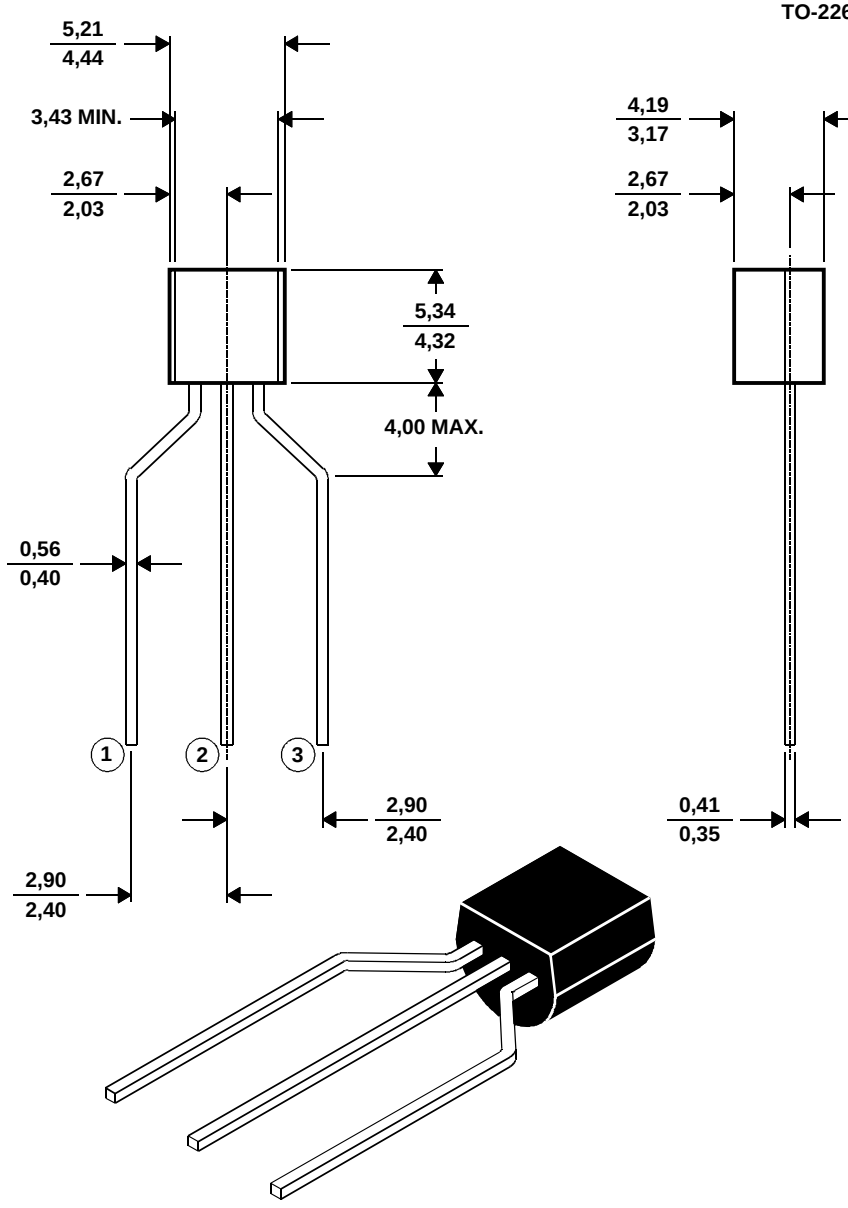
LP003 (TO-92) - Formed Leads Version

LP003 Falls Within JEDEC TO-226AA Dimensions



ALL LINEAR DIMENSIONS IN MILLIMETERS

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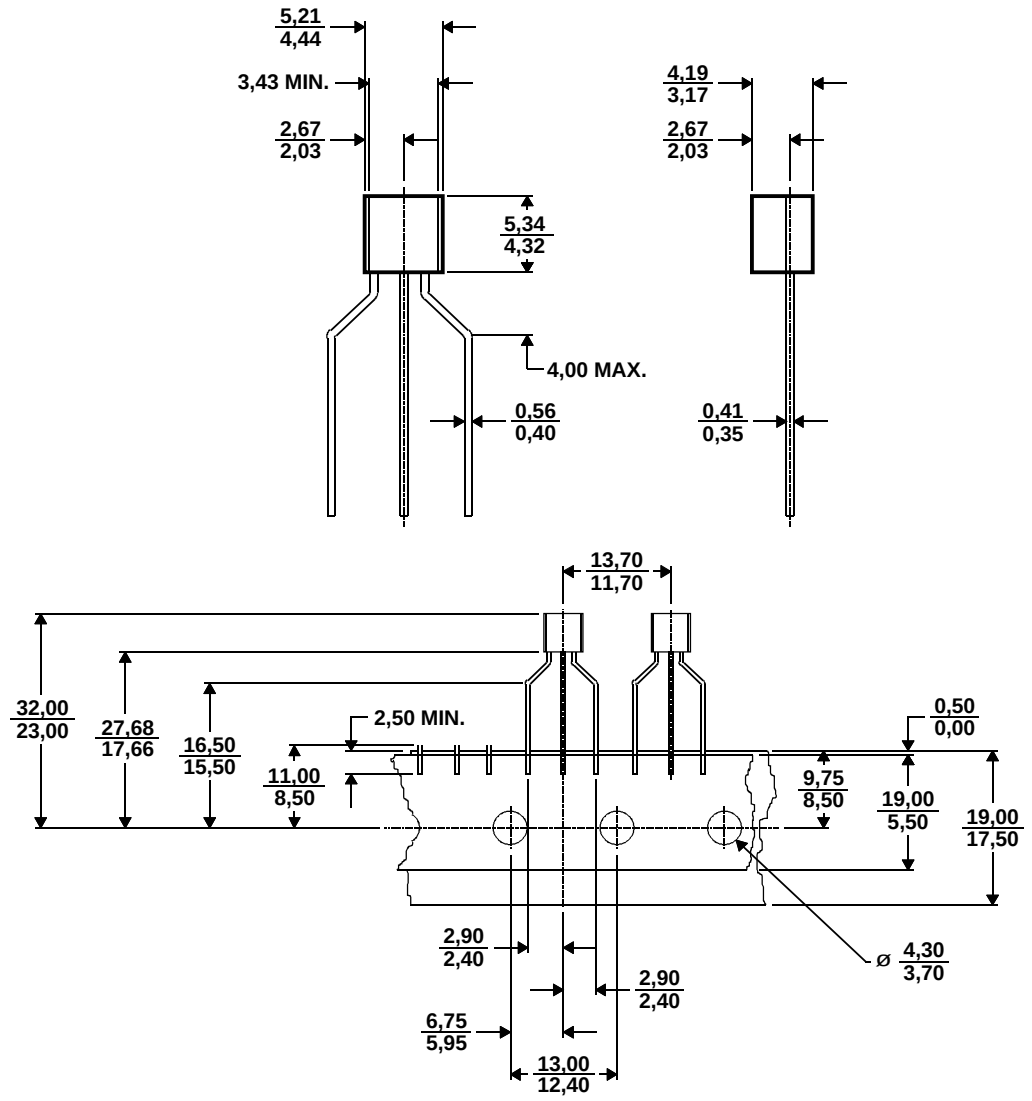
MECHANICAL DATA

LPR

tape dimensions

LP Package (TO-92) Tape (Formed Lead Version)



ALL LINEAR DIMENSIONS IN MILLIMETERS

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