

#### TOSHIBA PHOTOCOUPLER GaAs IRED & PHOTO-TRIAC

## TLP3020,TLP3021,TLP3022,TLP3023

OFFICE MACHINE
HOUSEHOLD USE EQUIPMENT
TRIAC DRIVER
SOLID STATE RELAY

The TOSHIBA TLP3020, TLP3021, TLP3022 and TLP3023 consist of a photo-triac optically coupled to a gallium arsenide infrared emitting diode in a six lead plastic DIP package.

• Peak Off-State Voltage : 400 V (Min.)

• Trigger LED Current : 30mA (Max.) (TLP3020)

15 mA (Max.) (TLP3021) 10 mA (Max.) (TLP3022) 5 mA (Max.) (TLP3023)

• On-State Current : 100 mA (Max.)

• UL Recognized : UL1577, File No. E67349

• Isolation Voltage : 5000 Vrms (Min.)

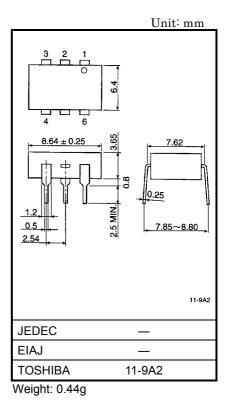
• Option (D4) Type

• VDE Approved : DIN VDE0884 / 08.87, Certificate No. 68329

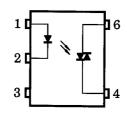
Maximum Operating Insulation Voltage: 630 VPK Highest Permissible Over Voltage: 6000 VPK

Note: When a VDE0884 approved type is needed, please designate the "Option (D4)"

			7.62mm pich	10.16 mm pich
			standard type	(LF2) type
•	Creepage Distance	: -	7.0 mm (Min.)	8.0 mm (Min.)
	Clearance	:	7.0 mm (Min.)	8.0 mm (Min.)
	<b>Insulation Thickness</b>	:	0.5 mm (Min.)	0.5 mm (Min.)



#### PIN CONFIGURATION (TOP VIEW)



: ANODE

2: CATHODE

3: N.C.

4: TERMINAL 1

6: TERMINAL 2



# MAXIMUM RATINGS (Ta=25°C)

CHARACTERISTIC			SYMBOL	RATING	UNIT
	Forward Current		I <sub>F</sub>	50	mA
	Forward Current Derating (Ta ≥ 53°C)		ΔI <sub>F</sub> /°C	-0.7	mA/°C
	Peak Forward Current (100μs pulse, 100pps)		I <sub>FP</sub>	1	Α
LED	Power Dissipation	1	$P_{D}$	100	mW
	Power Dissipation Derating (Ta ≥ 25°C)		ΔP <sub>D</sub> /°C	-1.0	mW/°C
	Reverse Voltage		$V_{R}$	5	V
	Junction Temperature		Tj	125	°C
	Off-State Output Terminal Voltage		$V_{DRM}$	400	V
	On-Stage RMS	Ta=25°C	<u> </u>	100	mΛ
	Current	Ta=70°C	I <sub>T(RMS)</sub>	50	mA
œ	On-State Current Derating (Ta ≥ 25°C)		ΔI <sub>T</sub> /°C	-1.1	mA/°C
DETECTOR	Peak On-Stage Current (100 µs pulse, 120pps)		I <sub>TP</sub>	2	А
DET	Peak Nonrepetitive Surge Current (P <sub>W</sub> =10ms, DC=10%)		I <sub>TSM</sub>	1.2	Α
	Power Dissipation		$P_{D}$	300	mW
	Power Dissipation Derating (Ta ≥ 25°C)		ΔP <sub>D</sub> /°C	-4.0	mW/°C
	Junction Temperature		Tj	115	°C
Storag	ge Temperature Range		T <sub>stg</sub>	<b>−</b> 55 ~ 150	°C
Operating Temperature Range		T <sub>opr</sub>	<b>−</b> 40 ~ 100	°C	
Lead Soldering Temperature (10s)			T <sub>sol</sub>	260	°C
Total Package Power Dissipation			P <sub>T</sub>	330	mW
Total Package Power Dissipation Derating (Ta ≥ 25°C)		age Power Dissipation Ta ≥ 25°C)		-4.4	mW/°C
Isolati (AC,	ion Voltage 1 min., R.H. ≤ 60%)	n Voltage min., R.H. ≤ 60%) (Note 1)		5000	Vrms

Note 1: Device considered a two terminal device :Pins 1, 2 and 3 shorted together and pins 4 and 6 shorted together.

### **RECOMMENDED OPERATING CONDISTIONS**

CHARACTERISTICS	SYMBOL	MIN	TYP.	MAX	UNIT
Supply Voltage	$V_{AC}$	_	_	120	Vac
Forward Current	I <sub>F</sub> *	15	20	25	mA
Peak On-Stage Current	I <sub>TP</sub>	_	_	1	Α
Operating Temperature	T <sub>opr</sub>	-25	_	85	°C

<sup>\*:</sup> In the case of TLP3022



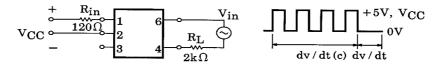
# INDIVIDUAL ELECTRICAL CHARACTERISTICS (Ta=25°C)

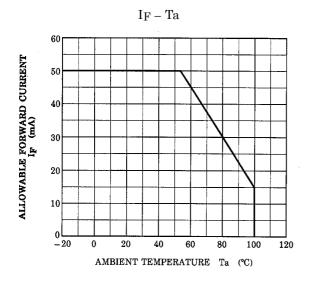
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT
	Forward Voltage	V <sub>F</sub>	I <sub>F</sub> =10mA	1.0	1.15	1.3	V
LED	Reverse Current	I <sub>R</sub>	V <sub>R</sub> =5V	_	_	10	μА
	Capacitance	C <sub>T</sub>	V=0, f=1MHz	_	10	_	pF
	Peak Off-State Current	I <sub>DRM</sub>	V <sub>DRM</sub> =400V	_	10	100	nA
<u>س</u>	Peak On-Stage Voltage	V <sub>TM</sub>	I <sub>TM</sub> =100mA	_	1.7	3.0	V
CTO	Holding Current	lн	_	_	0.6	_	mA
DETECTOR	Critical Rate of Rise of Off- State Voltage	dv / dt	V <sub>in</sub> =120Vrms, Ta=85°C (Fig.1)	200	500	_	V/µs
	Critical Rate of Rise of Commutating Voltage	dv / dt(c)	V <sub>in</sub> =30Vrms, IF=15mA (Fig.1)	_	0.2	_	V/µs

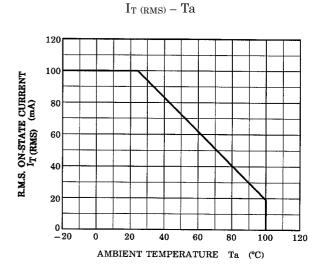
# COUPLED ELECTRICAL CHARACTERISTICS (Ta=25°C)

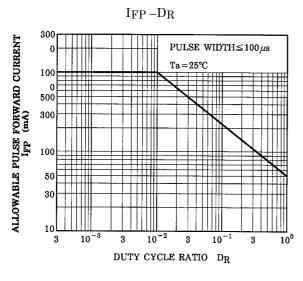
CHARACTERISTIC		SYMBOL	TEST CONDITION	MIN	TYP.	MAX	UNIT	
	TLP3020	l <sub>FT</sub>	V <sub>T</sub> =3V	_	_	30	- mA	
Trigger LED Current	TLP3021			_	_	15		
ringger LLD Current	TLP3022			_	5	10		
	TLP3023			_	_	5		
Capacitance Input to Out	put	C <sub>S</sub>	V <sub>S</sub> =0, f=1MHz	_	0.8	_	pF	
Isolation Resistance		R <sub>S</sub>	V <sub>S</sub> =500V (R.H. ≤ 60%)	5×10 <sup>10</sup>	10 <sup>14</sup>	_	Ω	
Isolation Voltage			AC, 1 minute		_	_	V <sub>rms</sub>	
		B <sub>VS</sub>	AC, 1 second (in oil)	_	10000	_	٧.	
			DC, 1 minute (in oil)	_	10000	_	V <sub>dc</sub>	

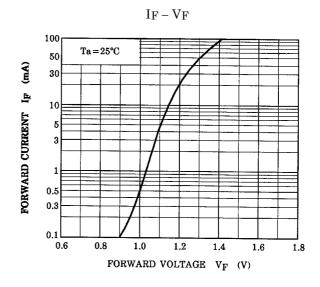
Fig. 1 dv/dt TEST CIRCUIT

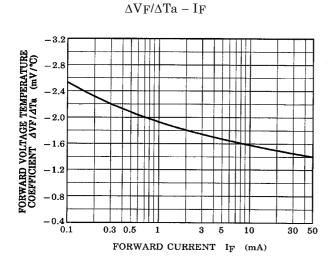


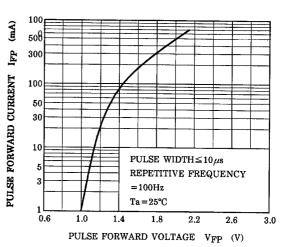






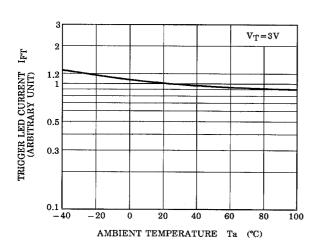




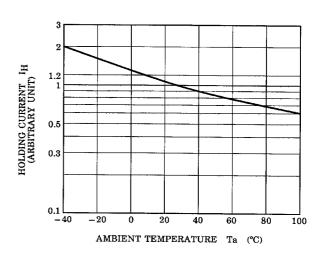


IFP - VFP

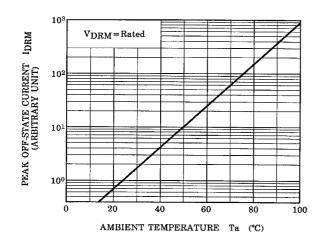
NORMALIZED IFT - Ta



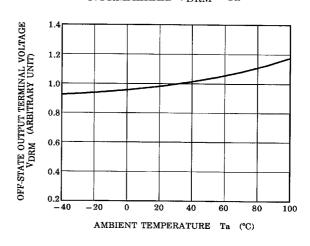
NORMALIZED I<sub>H</sub> – Ta



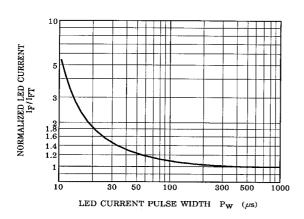
NORMALIZED IDRM - Ta



NORMALIZED V<sub>DRM</sub> - Ta



NORMALIZED LED CURRENT – LED CURRENT PULSE WIDTH



#### RESTRICTIONS ON PRODUCT USE

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