



UML1SL

NPN SILICON RF POWER TRANSISTOR

DESCRIPTION:

The ASI UML1SL is Designed for

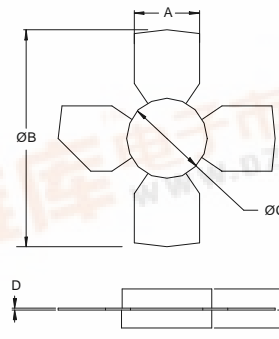
FEATURES:

-
-
- **Omnigold™** Metalization System

MAXIMUM RATINGS

I_C	2.0 A
V_{CBO}	45 V
V_{CEO}	25 V
V_{EBO}	3.5 V
P_{DISS}	31.8 W @ $T_C = 25^\circ C$
T_J	$-65^\circ C$ to $+200^\circ C$
T_{STG}	$-65^\circ C$ to $+150^\circ C$
θ_{JC}	$20^\circ C/W$

PACKAGE STYLE .280 4L PILL



DIM	MINIMUM inches / mm	MAXIMUM inches / mm
A	.220 / 5.59	.230 / 5.84
B		1.055 / 26.80
C	.275 / 6.99	.285 / 7.24
D	.004 / 0.10	.006 / 0.15
E	.050 / 1.27	.060 / 1.52
F	.118 / 3.00	.130 / 3.30

ORDER CODE: ASI10689

CHARACTERISTICS $T_C = 25^\circ C$

SYMBOL	TEST CONDITIONS	MINIMUM	TYPICAL	MAXIMUM	UNITS
BV_{CBO}	$I_C = 1.0$ mA	45			V
BV_{CEO}	$I_C = 20$ mA	24			V
BV_{EBO}	$I_E = 0.25$ mA	3.5			V
I_{CBO}	$V_{CB} = 28$ V			0.45	mA
h_{FE}	$V_{CE} = 5.0$ V $I_C = 100$ mA	15		120	---
C_{ob}	$V_{CB} = 28$ V $f = 1.0$ MHz			5.0	pF
P_G	$V_{CC} = 28$ V $P_{OUT} = 1.0$ W $f = 400$ MHz	13			dB
η_D			60		%

