

# SN54ALVTH16373, SN74ALVTH16373 2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES WITH 3-STATE OUTPUTS

SCES067F – JUNE 1996 – REVISED JANUARY 1999

查询"74ALVTH16373GRG4"供应商

- State-of-the-Art Advanced BiCMOS Technology (ABT) *Widebus*™ Design for 2.5-V and 3.3-V Operation and Low Static Power Dissipation
- Support Mixed-Mode Signal Operation (5-V Input and Output Voltages With 2.3-V to 3.6-V  $V_{CC}$ )
- Typical  $V_{OLP}$  (Output Ground Bounce)  $< 0.8$  V at  $V_{CC} = 3.3$  V,  $T_A = 25^\circ\text{C}$
- High Drive ( $-24/24$  mA at 2.5-V and  $-32/64$  mA at 3.3-V  $V_{CC}$ )
- Power Off Disables Outputs, Permitting Live Insertion
- High-Impedance State During Power Up and Power Down Prevents Driver Conflict
- Uses Bus Hold on Data Inputs in Place of External Pullup/Pulldown Resistors to Prevent the Bus From Floating
- Auto3-State Eliminates Bus Current Loading When Output Exceeds  $V_{CC} + 0.5$  V
- Latch-Up Performance Exceeds 250 mA Per JESD 17
- ESD Protection Exceeds 2000 V Per MIL-STD-883, Method 3015; Exceeds 200 V Using Machine Model; and Exceeds 1000 V Using Charged-Device Model, Robotic Method
- Flow-Through Architecture Facilitates Printed Circuit Board Layout
- Distributed  $V_{CC}$  and GND Pin Configuration Minimizes High-Speed Switching Noise
- Package Options Include Plastic Shrink Small-Outline (DL), Thin Shrink Small-Outline (DGG), Thin Very Small-Outline (DGV) Packages, and 380-mil Fine-Pitch Ceramic Flat (WD) Package

SN54ALVTH16373 . . . WD PACKAGE  
SN74ALVTH16373 . . . DGG, DGV, OR DL PACKAGE  
(TOP VIEW)

|          |    |    |          |
|----------|----|----|----------|
| 1OE      | 1  | 48 | 1LE      |
| 1Q1      | 2  | 47 | 1D1      |
| 1Q2      | 3  | 46 | 1D2      |
| GND      | 4  | 45 | GND      |
| 1Q3      | 5  | 44 | 1D3      |
| 1Q4      | 6  | 43 | 1D4      |
| $V_{CC}$ | 7  | 42 | $V_{CC}$ |
| 1Q5      | 8  | 41 | 1D5      |
| 1Q6      | 9  | 40 | 1D6      |
| GND      | 10 | 39 | GND      |
| 1Q7      | 11 | 38 | 1D7      |
| 1Q8      | 12 | 37 | 1D8      |
| 2Q1      | 13 | 36 | 2D1      |
| 2Q2      | 14 | 35 | 2D2      |
| GND      | 15 | 34 | GND      |
| 2Q3      | 16 | 33 | 2D3      |
| 2Q4      | 17 | 32 | 2D4      |
| $V_{CC}$ | 18 | 31 | $V_{CC}$ |
| 2Q5      | 19 | 30 | 2D5      |
| 2Q6      | 20 | 29 | 2D6      |
| GND      | 21 | 28 | GND      |
| 2Q7      | 22 | 27 | 2D7      |
| 2Q8      | 23 | 26 | 2D8      |
| 2OE      | 24 | 25 | 2LE      |

## description

The 'ALVTH16373 devices are 16-bit transparent D-type latches with 3-state outputs designed for 2.5-V or 3.3-V  $V_{CC}$  operation, but with the capability to provide a TTL interface to a 5-V system environment. These devices are particularly suitable for implementing buffer registers, I/O ports, bidirectional bus drivers, and working registers.

These devices can be used as two 8-bit latches or one 16-bit latch. When the latch-enable (LE) input is high, the Q outputs follow the data (D) inputs. When LE is taken low, the Q outputs are latched at the levels set up at the D inputs.



Please be aware that an important notice concerning availability, standard warranty, and use in critical applications of Texas Instruments semiconductor products and disclaimers thereto appears at the end of this data sheet.

Widebus is a trademark of Texas Instruments Incorporated.

UNLESS OTHERWISE NOTED this document contains PRODUCTION DATA information current as of publication date. Products conform to specifications per the terms of Texas Instruments standard warranty. Production processing does not necessarily include testing of all parameters.

 **TEXAS  
INSTRUMENTS**

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

Copyright © 1999, Texas Instruments Incorporated



SN54ALVTH16373, SN74ALVTH16373  
2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES  
WITH 3-STATE OUTPUTS

SCES067H JUN 1998 PREPARED BY SPRING VALLEY, TEXAS  
[查询"74ALVTH16373"Datasheet"供应商"](#)

description (continued)

A buffered output-enable ( $\overline{OE}$ ) input can be used to place the eight outputs in either a normal logic state (high or low logic levels) or a high-impedance state. In the high-impedance state, the outputs neither load nor drive the bus lines significantly. The high-impedance state and the increased drive provide the capability to drive bus lines without interface or pullup components.

$\overline{OE}$  does not affect internal operations of the latch. Old data can be retained or new data can be entered while the outputs are in the high-impedance state.

Active bus-hold circuitry is provided to hold unused or floating data inputs at a valid logic level.

When  $V_{CC}$  is between 0 and 1.2 V, the device is in the high-impedance state during power up or power down. However, to ensure the high-impedance state above 1.2 V,  $\overline{OE}$  should be tied to  $V_{CC}$  through a pullup resistor; the minimum value of the resistor is determined by the current-sinking capability of the driver.

The SN54ALVTH16373 is characterized for operation over the full military temperature range of  $-55^{\circ}\text{C}$  to  $125^{\circ}\text{C}$ . The SN74ALVTH16373 is characterized for operation from  $-40^{\circ}\text{C}$  to  $85^{\circ}\text{C}$ .

FUNCTION TABLE  
(each 8-bit section)

| INPUTS          |    |   | OUTPUT<br>Q |
|-----------------|----|---|-------------|
| $\overline{OE}$ | LE | D |             |
| L               | H  | H | H           |
| L               | H  | L | L           |
| L               | L  | X | $Q_0$       |
| H               | X  | X | Z           |

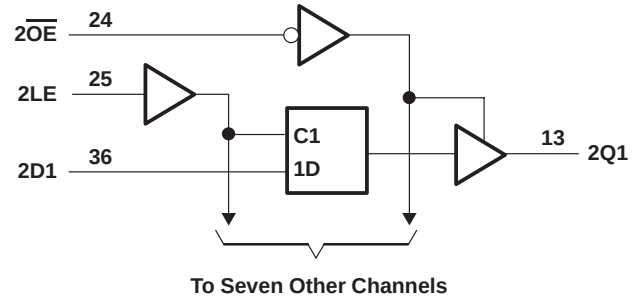
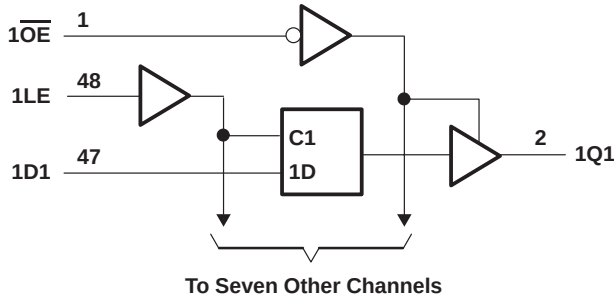


**SN54ALVTH16373, SN74ALVTH16373**  
**2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES**  
**WITH 3-STATE OUTPUTS**

SCES067F – JUNE 1996 – REVISED JANUARY 1999

查询"74ALVTH16373GRG4"供应商

**logic diagram (positive logic)**



**absolute maximum ratings over operating free-air temperature range (unless otherwise noted)<sup>†</sup>**

|                                                                                                  |                 |
|--------------------------------------------------------------------------------------------------|-----------------|
| Supply voltage range, $V_{CC}$                                                                   | –0.5 V to 4.6 V |
| Input voltage range, $V_I$ (see Note 1)                                                          | –0.5 V to 7 V   |
| Voltage range applied to any output in the high-impedance or power-off state, $V_O$ (see Note 1) | –0.5 V to 7 V   |
| Voltage range applied to any output in the high state, $V_O$ (see Note 1)                        | –0.5 V to 7 V   |
| Output current in the low state, $I_{OL}$ : SN54ALVTH16373                                       | 96 mA           |
| SN74ALVTH16373                                                                                   | 128 mA          |
| Output current in the high state, $I_{OH}$ : SN54ALVTH16373                                      | –48 mA          |
| SN74ALVTH16373                                                                                   | –64 mA          |
| Input clamp current, $I_{IK}$ ( $V_I < 0$ )                                                      | –50 mA          |
| Output clamp current, $I_{OK}$ ( $V_O < 0$ )                                                     | –50 mA          |
| Package thermal impedance, $\theta_{JA}$ (see Note 2): DGG package                               | 89°C/W          |
| DGV package                                                                                      | 93°C/W          |
| DL package                                                                                       | 94°C/W          |
| Storage temperature range, $T_{stg}$                                                             | –65°C to 150°C  |

<sup>†</sup> Stresses beyond those listed under “absolute maximum ratings” may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated under “recommended operating conditions” is not implied. Exposure to absolute-maximum-rated conditions for extended periods may affect device reliability.

- NOTES: 1. The input and output negative-voltage ratings may be exceeded if the input and output clamp-current ratings are observed.  
2. The package thermal impedance is calculated in accordance with JESD 51.

**recommended operating conditions,  $V_{CC} = 2.5 \text{ V} \pm 0.2 \text{ V}$  (see Note 3)**

|                          |                                                                                   | SN54ALVTH16373 |          |     | SN74ALVTH16373 |          |     | UNIT            |
|--------------------------|-----------------------------------------------------------------------------------|----------------|----------|-----|----------------|----------|-----|-----------------|
|                          |                                                                                   | MIN            | TYP      | MAX | MIN            | TYP      | MAX |                 |
| $V_{CC}$                 | Supply voltage                                                                    | 2.3            |          | 2.7 | 2.3            |          | 2.7 | V               |
| $V_{IH}$                 | High-level input voltage                                                          | 1.7            |          |     | 1.7            |          |     | V               |
| $V_{IL}$                 | Low-level input voltage                                                           |                |          | 0.7 |                |          | 0.7 | V               |
| $V_I$                    | Input voltage                                                                     | 0              | $V_{CC}$ | 5.5 | 0              | $V_{CC}$ | 5.5 | V               |
| $I_{OH}$                 | High-level output current                                                         |                |          | –6  |                |          | –8  | mA              |
| $I_{OL}$                 | Low-level output current                                                          |                |          | 6   |                |          | 8   | mA              |
|                          | Low-level output current; current duty cycle $\leq 50\%$ ; $f \geq 1 \text{ kHz}$ |                |          | 18  |                |          | 24  |                 |
| $\Delta t/\Delta v$      | Input transition rise or fall rate                                                |                |          | 10  |                |          | 10  | ns/V            |
| $\Delta t/\Delta V_{CC}$ | Power-up ramp rate                                                                | 200            |          |     | 200            |          |     | $\mu\text{s/V}$ |
| $T_A$                    | Operating free-air temperature                                                    | –55            |          | 125 | –40            |          | 85  | °C              |

NOTE 3: All unused control inputs of the device must be held at  $V_{CC}$  or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

SCES0677-1 JUNE 1996 - REVISED JANUARY 1999

SCES0671 - JUNE 1996

**recommended operating conditions,  $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$  (see Note 3)**

NOTE 3: All unused control inputs of the device must be held at V<sub>CC</sub> or GND to ensure proper device operation. Refer to the TI application report, *Implications of Slow or Floating CMOS Inputs*, literature number SCBA004.

POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

**SN54ALVTH16373, SN74ALVTH16373**  
**2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES**  
**WITH 3-STATE OUTPUTS**

SCES067F – JUNE 1996 – REVISED JANUARY 1999

[查询"74ALVTH16373GRG4"供应商](#)

**electrical characteristics over recommended operating free-air temperature range,  
 $V_{CC} = 2.5 \text{ V} \pm 0.2 \text{ V}$  (unless otherwise noted)**

| PARAMETER             |                | TEST CONDITIONS                                                                                                                         | SN54ALVTH16373   |           |         | SN74ALVTH16373 |           |           | UNIT          |
|-----------------------|----------------|-----------------------------------------------------------------------------------------------------------------------------------------|------------------|-----------|---------|----------------|-----------|-----------|---------------|
|                       |                |                                                                                                                                         | MIN              | TYP†      | MAX     | MIN            | TYP†      | MAX       |               |
| $V_{IK}$              |                | $V_{CC} = 2.3 \text{ V}$ ,<br>$I_I = -18 \text{ mA}$                                                                                    |                  |           | -1.2    |                |           | -1.2      | V             |
| $V_{OH}$              |                | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ ,<br>$I_{OH} = -100 \mu\text{A}$                                                             | $V_{CC}-0.2$     |           |         | $V_{CC}-0.2$   |           |           | V             |
|                       |                | $V_{CC} = 2.3 \text{ V}$ ,<br>$I_{OH} = -6 \text{ mA}$                                                                                  | 1.8              |           |         |                |           |           |               |
|                       |                | $V_{CC} = 2.3 \text{ V}$ ,<br>$I_{OH} = -8 \text{ mA}$                                                                                  |                  |           |         | 1.8            |           |           |               |
| $V_{OL}$              |                | $V_{CC} = 2.3 \text{ V to } 2.7 \text{ V}$ ,<br>$I_{OL} = 100 \mu\text{A}$                                                              |                  |           | 0.2     |                |           | 0.2       | V             |
|                       |                | $V_{CC} = 2.3 \text{ V}$                                                                                                                |                  |           | 0.4     |                |           |           |               |
|                       |                |                                                                                                                                         |                  |           |         |                |           | 0.4       |               |
|                       |                |                                                                                                                                         |                  |           | 0.5     |                |           |           |               |
|                       |                |                                                                                                                                         |                  |           |         |                |           | 0.5       |               |
| $I_I$                 | Control inputs | $V_{CC} = 2.7 \text{ V}$ ,<br>$V_I = V_{CC} \text{ or GND}$                                                                             |                  |           | $\pm 1$ |                |           | $\pm 1$   | $\mu\text{A}$ |
|                       |                | $V_{CC} = 0 \text{ or } 2.7 \text{ V}$ ,<br>$V_I = 5.5 \text{ V}$                                                                       |                  |           | 10      |                |           | 10        |               |
|                       | Data inputs    | $V_{CC} = 2.7 \text{ V}$                                                                                                                |                  |           | 10      |                |           | 10        |               |
|                       |                |                                                                                                                                         |                  |           | 1       |                |           | 1         |               |
|                       |                |                                                                                                                                         |                  |           | -5      |                |           | -5        |               |
| $I_{off}$             |                | $V_{CC} = 0$ ,<br>$V_I \text{ or } V_O = 0 \text{ to } 4.5 \text{ V}$                                                                   |                  |           |         |                |           | $\pm 100$ | $\mu\text{A}$ |
| $I_{BHL}^\ddagger$    |                | $V_{CC} = 2.3 \text{ V}$ ,<br>$V_I = 0.7 \text{ V}$                                                                                     |                  | 115       |         |                | 115       |           | $\mu\text{A}$ |
| $I_{BHH}^\S$          |                | $V_{CC} = 2.3 \text{ V}$ ,<br>$V_I = 1.7 \text{ V}$                                                                                     |                  | -10       |         |                | -10       |           | $\mu\text{A}$ |
| $I_{BHLO}^\P$         |                | $V_{CC} = 2.7 \text{ V}$ ,<br>$V_I = 0 \text{ to } V_{CC}$                                                                              | 300              |           |         | 300            |           |           | $\mu\text{A}$ |
| $I_{BHHO}^\#$         |                | $V_{CC} = 2.7 \text{ V}$ ,<br>$V_I = 0 \text{ to } V_{CC}$                                                                              | -300             |           |         | -300           |           |           | $\mu\text{A}$ |
| $I_{EX}^\P$           |                | $V_{CC} = 2.3 \text{ V}$ ,<br>$V_O = 5.5 \text{ V}$                                                                                     |                  | 125       |         |                | 125       |           | $\mu\text{A}$ |
| $I_{OZ(PU/PD)}^\star$ |                | $V_{CC} \leq 1.2 \text{ V}$ , $V_O = 0.5 \text{ V to } V_{CC}$ ,<br>$V_I = \text{GND or } V_{CC}$ , $\overline{OE} = \text{don't care}$ |                  | $\pm 100$ |         |                | $\pm 100$ |           | $\mu\text{A}$ |
| $I_{OZH}$             |                | $V_{CC} = 2.7 \text{ V}$ ,<br>$V_O = 2.3 \text{ V}$ ,<br>$V_I = 0.7 \text{ V or } 1.7 \text{ V}$                                        |                  | 5         |         |                | 5         |           | $\mu\text{A}$ |
| $I_{OZL}$             |                | $V_{CC} = 2.7 \text{ V}$ ,<br>$V_O = 0.5 \text{ V}$ ,<br>$V_I = 0.7 \text{ V or } 1.7 \text{ V}$                                        |                  | -5        |         |                | -5        |           | $\mu\text{A}$ |
| $I_{CC}$              |                | $V_{CC} = 2.7 \text{ V}$ ,<br>$I_O = 0$ ,<br>$V_I = V_{CC} \text{ or GND}$                                                              | Outputs high     | 0.04      | 0.1     | 0.04           | 0.1       |           | mA            |
|                       |                |                                                                                                                                         | Outputs low      | 2.3       | 4.5     | 2.3            | 4.5       |           |               |
|                       |                |                                                                                                                                         | Outputs disabled | 0.04      | 0.1     | 0.04           | 0.1       |           |               |
| $C_i$                 |                | $V_{CC} = 2.5 \text{ V}$ ,<br>$V_I = 2.5 \text{ V or } 0$                                                                               |                  | 3.5       |         |                | 3.5       |           | pF            |
| $C_o$                 |                | $V_{CC} = 2.5 \text{ V}$ ,<br>$V_O = 2.5 \text{ V or } 0$                                                                               |                  | 6         |         |                | 6         |           | pF            |

† All typical values are at  $V_{CC} = 2.5 \text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

‡ The bus-hold circuit can sink at least the minimum low sustaining current at  $V_{IL}$  max.  $I_{BHL}$  should be measured after lowering  $V_{IN}$  to GND and then raising it to  $V_{IL}$  max.

§ The bus-hold circuit can source at least the minimum high sustaining current at  $V_{IH}$  min.  $I_{BHH}$  should be measured after raising  $V_{IN}$  to  $V_{CC}$  and then lowering it to  $V_{IH}$  min.

¶ An external driver must source at least  $I_{BHLO}$  to switch this node from low to high.

# An external driver must sink at least  $I_{BHHO}$  to switch this node from high to low.

|| Current into an output in the high state when  $V_O > V_{CC}$

☆ High-impedance state during power up or power down

PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.



POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

# SN54ALVTH16373, SN74ALVTH16373

## 2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES

### WITH 3-STATE OUTPUTS

SCES067B JUN 1998 (SN54ALVTH16373) (SN74ALVTH16373) 供应商

electrical characteristics over recommended operating free-air temperature range,  
 $V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$  (unless otherwise noted)

| PARAMETER                           |                | TEST CONDITIONS                                                                                                                             |                                                       | SN54ALVTH16373                   |      |      | SN74ALVTH16373       |      |     | UNIT |    |
|-------------------------------------|----------------|---------------------------------------------------------------------------------------------------------------------------------------------|-------------------------------------------------------|----------------------------------|------|------|----------------------|------|-----|------|----|
|                                     |                |                                                                                                                                             |                                                       | MIN                              | TYP† | MAX  | MIN                  | TYP† | MAX |      |    |
| V <sub>IK</sub>                     |                | V <sub>CC</sub> = 3 V, I <sub>I</sub> = −18 mA                                                                                              |                                                       | −1.2                             |      |      | −1.2                 |      |     | V    |    |
| V <sub>OH</sub>                     |                | V <sub>CC</sub> = 3 V to 3.6 V, I <sub>OH</sub> = −100 μA                                                                                   |                                                       | V <sub>CC</sub> −0.2             |      |      | V <sub>CC</sub> −0.2 |      |     | V    |    |
|                                     |                | V <sub>CC</sub> = 3 V                                                                                                                       |                                                       | I <sub>OH</sub> = −24 mA         |      |      |                      |      |     |      |    |
|                                     |                |                                                                                                                                             |                                                       | I <sub>OH</sub> = −32 mA         |      |      | 2                    |      |     |      |    |
| V <sub>OL</sub>                     |                | V <sub>CC</sub> = 3 V to 3.6 V, I <sub>OL</sub> = 100 μA                                                                                    |                                                       | 0.2                              |      |      | 0.2                  |      |     | V    |    |
|                                     |                | V <sub>CC</sub> = 3 V                                                                                                                       |                                                       | I <sub>OL</sub> = 16 mA          |      |      | 0.4                  |      |     |      |    |
|                                     |                |                                                                                                                                             |                                                       | I <sub>OL</sub> = 24 mA          |      |      | 0.5                  |      |     |      |    |
|                                     |                |                                                                                                                                             |                                                       | I <sub>OL</sub> = 32 mA          |      |      | 0.5                  |      |     |      |    |
|                                     |                |                                                                                                                                             |                                                       | I <sub>OL</sub> = 48 mA          |      |      | 0.55                 |      |     |      |    |
|                                     |                |                                                                                                                                             |                                                       | I <sub>OL</sub> = 64 mA          |      |      | 0.55                 |      |     |      |    |
| I <sub>I</sub>                      | Control inputs | V <sub>CC</sub> = 3.6 V, V <sub>I</sub> = V <sub>CC</sub> or GND                                                                            |                                                       | ±1                               |      |      | ±1                   |      |     | μA   |    |
|                                     |                | V <sub>CC</sub> = 0 or 3.6 V, V <sub>I</sub> = 5.5 V                                                                                        |                                                       | 10                               |      |      | 10                   |      |     |      |    |
|                                     | Data inputs    | V <sub>CC</sub> = 3.6 V                                                                                                                     |                                                       | V <sub>I</sub> = 5.5 V           |      |      | 10                   |      |     |      |    |
|                                     |                |                                                                                                                                             |                                                       | V <sub>I</sub> = V <sub>CC</sub> |      |      | 1                    |      |     |      |    |
|                                     |                |                                                                                                                                             |                                                       | V <sub>I</sub> = 0               |      |      | −5                   |      |     |      |    |
| I <sub>off</sub>                    |                | V <sub>CC</sub> = 0, V <sub>I</sub> or V <sub>O</sub> = 0 to 4.5 V                                                                          |                                                       |                                  |      |      | ±100                 |      |     | μA   |    |
| I <sub>BHL</sub> <sup>‡</sup>       |                | V <sub>CC</sub> = 3 V, V <sub>I</sub> = 0.8 V                                                                                               |                                                       | 75                               |      |      | 75                   |      |     | μA   |    |
| I <sub>BHH</sub> <sup>§</sup>       |                | V <sub>CC</sub> = 3 V, V <sub>I</sub> = 2 V                                                                                                 |                                                       | −75                              |      |      | −75                  |      |     | μA   |    |
| I <sub>BHLO</sub> <sup>¶</sup>      |                | V <sub>CC</sub> = 3.6 V, V <sub>I</sub> = 0 to V <sub>CC</sub>                                                                              |                                                       | 500                              |      |      | 500                  |      |     | μA   |    |
| I <sub>BHHO</sub> <sup>#</sup>      |                | V <sub>CC</sub> = 3.6 V, V <sub>I</sub> = 0 to V <sub>CC</sub>                                                                              |                                                       | −500                             |      |      | −500                 |      |     | μA   |    |
| I <sub>EX</sub> <sup>  </sup>       |                | V <sub>CC</sub> = 3 V, V <sub>O</sub> = 5.5 V                                                                                               |                                                       | 125                              |      |      | 125                  |      |     | μA   |    |
| I <sub>OZ(PU/PD)</sub> <sup>☆</sup> |                | V <sub>CC</sub> ≤ 1.2 V, V <sub>O</sub> = 0.5 V to V <sub>CC</sub> , V <sub>I</sub> = GND or V <sub>CC</sub> , $\overline{OE}$ = don't care |                                                       | ±100                             |      |      | ±100                 |      |     | μA   |    |
| I <sub>OZH</sub>                    |                | V <sub>CC</sub> = 3.6 V                                                                                                                     | V <sub>O</sub> = 3 V, V <sub>I</sub> = 0.8 V or 2 V   | 5                                |      |      | 5                    |      |     | μA   |    |
| I <sub>OZL</sub>                    |                | V <sub>CC</sub> = 3.6 V                                                                                                                     | V <sub>O</sub> = 0.5 V, V <sub>I</sub> = 0.8 V or 2 V | −5                               |      |      | −5                   |      |     | μA   |    |
| I <sub>CC</sub>                     |                | V <sub>CC</sub> = 3.6 V, I <sub>O</sub> = 0, V <sub>I</sub> = V <sub>CC</sub> or GND                                                        |                                                       | Outputs high                     |      | 0.07 | 0.1                  | 0.07 |     | 0.1  | mA |
|                                     |                |                                                                                                                                             |                                                       | Outputs low                      |      | 3.2  | 5.5                  | 3.2  |     | 5    |    |
|                                     |                |                                                                                                                                             |                                                       | Outputs disabled                 |      | 0.07 | 0.1                  | 0.07 |     | 0.1  |    |
| ΔI <sub>CC</sub> <sup>□</sup>       |                | V <sub>CC</sub> = 3 V to 3.6 V, One input at V <sub>CC</sub> − 0.6 V, Other inputs at V <sub>CC</sub> or GND                                |                                                       | 0.4                              |      |      | 0.4                  |      |     | mA   |    |
| C <sub>i</sub>                      |                | V <sub>CC</sub> = 3.3 V, V <sub>I</sub> = 3.3 V or 0                                                                                        |                                                       | 3.5                              |      |      | 3.5                  |      |     | pF   |    |
| C <sub>O</sub>                      |                | V <sub>CC</sub> = 3.3 V, V <sub>O</sub> = 3.3 V or 0                                                                                        |                                                       | 6                                |      |      | 6                    |      |     | pF   |    |

† All typical values are at  $V_{CC} = 3.3 \text{ V}$ ,  $T_A = 25^\circ\text{C}$ .

‡ The bus-hold circuit can sink at least the minimum low sustaining current at  $V_{IL} \text{ max}$ .  $I_{BHL}$  should be measured after lowering  $V_{IN}$  to GND and then raising it to  $V_{IL} \text{ max}$ .

§ The bus-hold circuit can source at least the minimum high sustaining current at  $V_{IH} \text{ min}$ .  $I_{BHH}$  should be measured after raising  $V_{IN}$  to  $V_{CC}$  and then lowering it to  $V_{IH} \text{ min}$ .

¶ An external driver must source at least  $I_{BHLO}$  to switch this node from low to high.

# An external driver must sink at least  $I_{BHHO}$  to switch this node from high to low.

|| Current into an output in the high state when  $V_O > V_{CC}$

★ High-impedance state during power up or power down

□ This is the increase in supply current for each input that is at the specified TTL voltage level rather than  $V_{CC}$  or GND.

PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.



**SN54ALVTH16373, SN74ALVTH16373**  
**2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES**  
**WITH 3-STATE OUTPUTS**

SCES067F – JUNE 1996 – REVISED JANUARY 1999

[查询"74ALVTH16373GRG4"供应商](#)

**timing requirements over recommended operating free-air temperature range,  $V_{CC} = 2.5\text{ V} \pm 0.2\text{ V}$  (unless otherwise noted) (see Figure 1)**

|          |                             | SN54ALVTH16373 |     | SN74ALVTH16373 |     | UNIT |
|----------|-----------------------------|----------------|-----|----------------|-----|------|
|          |                             | MIN            | MAX | MIN            | MAX |      |
| $t_W$    | Pulse duration, LE high     | 1.5            |     | 1.5            |     | ns   |
| $t_{su}$ | Setup time, data before LE↓ | 1.1            |     | 1              |     | ns   |
|          |                             | 1.6            |     | 1.5            |     |      |
| $t_h$    | Hold time, data after LE↓   | 1              |     | 0.9            |     | ns   |
|          |                             | 1.6            |     | 1.5            |     |      |

**timing requirements over recommended operating free-air temperature range,  $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$  (unless otherwise noted) (see Figure 2)**

|          |                             | SN54ALVTH16373 |     | SN74ALVTH16373 |     | UNIT |
|----------|-----------------------------|----------------|-----|----------------|-----|------|
|          |                             | MIN            | MAX | MIN            | MAX |      |
| $t_W$    | Pulse duration, LE high     | 1.5            |     | 1.5            |     | ns   |
| $t_{su}$ | Setup time, data before LE↓ | 1.5            |     | 1.4            |     | ns   |
|          |                             | 1              |     | 0.9            |     |      |
| $t_h$    | Hold time, data after LE↓   | 1              |     | 0.9            |     | ns   |
|          |                             | 1.5            |     | 1.4            |     |      |

**switching characteristics over recommended operating free-air temperature range,  $C_L = 30\text{ pF}$ ,  $V_{CC} = 2.5\text{ V} \pm 0.2\text{ V}$  (unless otherwise noted) (see Figure 1)**

| PARAMETER | FROM (INPUT)    | TO (OUTPUT) | SN54ALVTH16373 |     | SN74ALVTH16373 |     | UNIT |
|-----------|-----------------|-------------|----------------|-----|----------------|-----|------|
|           |                 |             | MIN            | MAX | MIN            | MAX |      |
| $t_{PLH}$ | D               | Q           | 1              | 3.4 | 1              | 3.3 | ns   |
| $t_{PHL}$ |                 |             | 1              | 4.3 | 1              | 4.2 |      |
| $t_{PLH}$ | LE              | Q           | 1.4            | 3.9 | 1.5            | 3.8 | ns   |
| $t_{PHL}$ |                 |             | 1.4            | 4.6 | 1.5            | 4.5 |      |
| $t_{PZH}$ | $\overline{OE}$ | Q           | 1.7            | 4.4 | 1.8            | 4.3 | ns   |
| $t_{PZL}$ |                 |             | 1.4            | 4.1 | 1.5            | 4   |      |
| $t_{PHZ}$ | OE              | Q           | 1.4            | 4.7 | 1.5            | 4.6 | ns   |
| $t_{PLZ}$ |                 |             | 1              | 3.7 | 1              | 3.6 |      |

**switching characteristics over recommended operating free-air temperature range,  $C_L = 50\text{ pF}$ ,  $V_{CC} = 3.3\text{ V} \pm 0.3\text{ V}$  (unless otherwise noted) (see Figure 2)**

| PARAMETER | FROM (INPUT)    | TO (OUTPUT) | SN54ALVTH16373 |     | SN74ALVTH16373 |     | UNIT |
|-----------|-----------------|-------------|----------------|-----|----------------|-----|------|
|           |                 |             | MIN            | MAX | MIN            | MAX |      |
| $t_{PLH}$ | D               | Q           | 1              | 3.2 | 1              | 3.1 | ns   |
| $t_{PHL}$ |                 |             | 1              | 3.4 | 1              | 3.3 |      |
| $t_{PLH}$ | LE              | Q           | 1              | 3.4 | 1              | 3.3 | ns   |
| $t_{PHL}$ |                 |             | 1              | 3.6 | 1              | 3.5 |      |
| $t_{PZH}$ | $\overline{OE}$ | Q           | 1.3            | 4.1 | 1.4            | 4   | ns   |
| $t_{PZL}$ |                 |             | 1              | 3.5 | 1              | 3.4 |      |
| $t_{PHZ}$ | $\overline{OE}$ | Q           | 1.4            | 5   | 1.5            | 4.9 | ns   |
| $t_{PLZ}$ |                 |             | 1.4            | 4.6 | 1.5            | 4.5 |      |

PRODUCT PREVIEW information concerns products in the formative or design phase of development. Characteristic data and other specifications are design goals. Texas Instruments reserves the right to change or discontinue these products without notice.

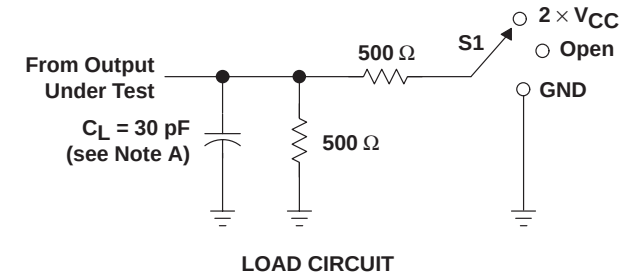


POST OFFICE BOX 655303 • DALLAS, TEXAS 75265

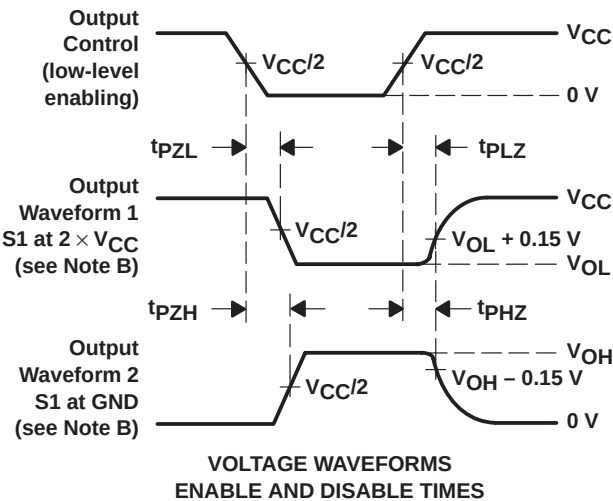
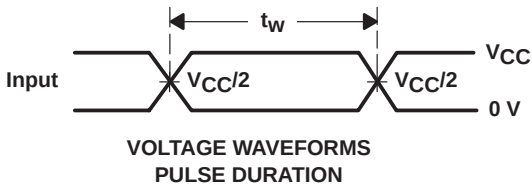
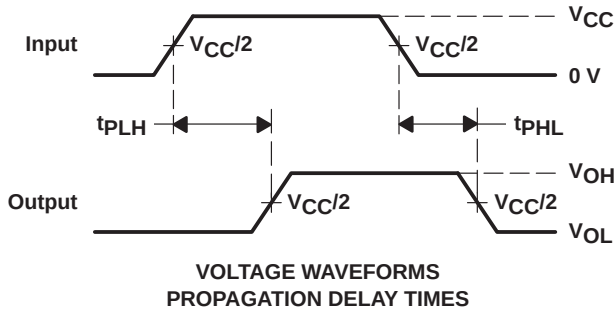
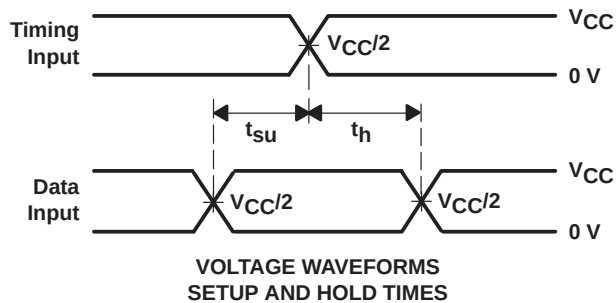
SN54ALVTH16373, SN74ALVTH16373  
2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES  
WITH 3-STATE OUTPUTS

SCES057B JUN 1998 PREPARED BY: SN74ALVTH16373 AND SN54ALVTH16373 供应商

PARAMETER MEASUREMENT INFORMATION  
 $V_{CC} = 2.5\text{ V} \pm 0.2\text{ V}$



| TEST              | S1                |
|-------------------|-------------------|
| $t_{PLH}/t_{PHL}$ | Open              |
| $t_{PLZ}/t_{PZL}$ | $2 \times V_{CC}$ |
| $t_{PHZ}/t_{PZH}$ | GND               |



- NOTES: A.  $C_L$  includes probe and jig capacitance.  
B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.  
C. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10\text{ MHz}$ ,  $Z_O = 50\ \Omega$ ,  $t_r \leq 2\text{ ns}$ ,  $t_f \leq 2\text{ ns}$ .  
D. The outputs are measured one at a time with one transition per measurement.

Figure 1. Load Circuit and Voltage Waveforms



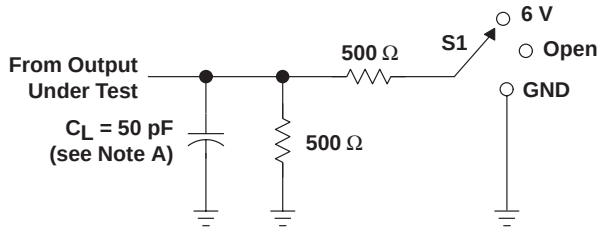
# SN54ALVTH16373, SN74ALVTH16373 2.5-V/3.3-V 16-BIT TRANSPARENT D-TYPE LATCHES WITH 3-STATE OUTPUTS

SCES067F – JUNE 1996 – REVISED JANUARY 1999

查询"74ALVTH16373GRG4"供应商

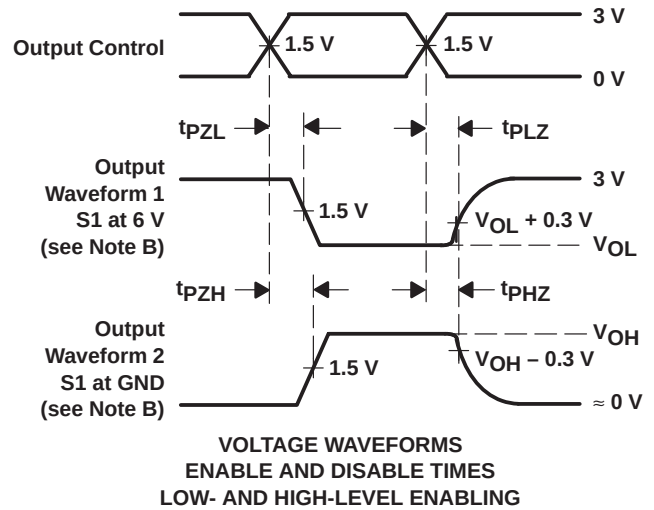
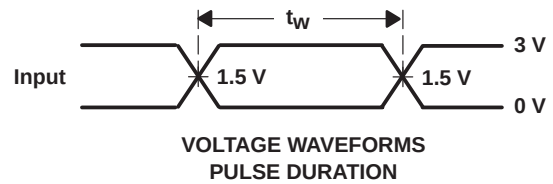
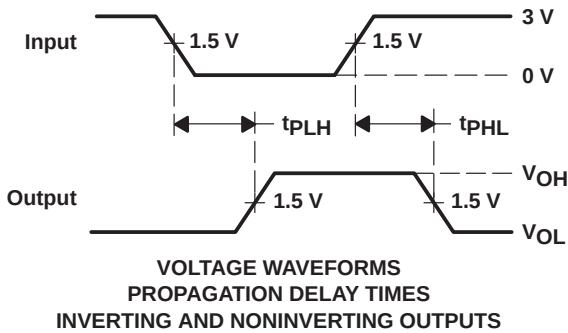
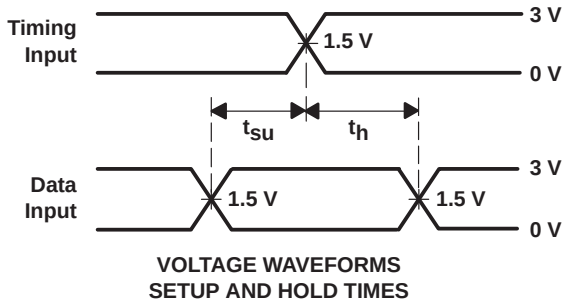
## PARAMETER MEASUREMENT INFORMATION

$$V_{CC} = 3.3 \text{ V} \pm 0.3 \text{ V}$$



LOAD CIRCUIT

| TEST              | S1   |
|-------------------|------|
| $t_{PLH}/t_{PHL}$ | Open |
| $t_{PLZ}/t_{PZL}$ | 6 V  |
| $t_{PHZ}/t_{PZH}$ | GND  |



- NOTES:
- A.  $C_L$  includes probe and jig capacitance.
  - B. Waveform 1 is for an output with internal conditions such that the output is low except when disabled by the output control. Waveform 2 is for an output with internal conditions such that the output is high except when disabled by the output control.
  - C. All input pulses are supplied by generators having the following characteristics:  $PRR \leq 10 \text{ MHz}$ ,  $Z_O = 50 \Omega$ ,  $t_r \leq 2.5 \text{ ns}$ ,  $t_f \leq 2.5 \text{ ns}$ .
  - D. The outputs are measured one at a time with one transition per measurement.

Figure 2. Load Circuit and Voltage Waveforms

**PACKAGING INFORMATION**

| Orderable Device  | Status <sup>(1)</sup> | Package Type         | Package Drawing | Pins | Package Qty | Eco Plan <sup>(2)</sup> | Lead/Ball Finish | MSL Peak Temp <sup>(3)</sup> |
|-------------------|-----------------------|----------------------|-----------------|------|-------------|-------------------------|------------------|------------------------------|
| 74ALVTH16373DLG4  | ACTIVE                | SSOP                 | DL              | 48   | 25          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ALVTH16373DLRG4 | ACTIVE                | SSOP                 | DL              | 48   | 1000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ALVTH16373GRE4  | ACTIVE                | TSSOP                | DGG             | 48   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ALVTH16373GRG4  | ACTIVE                | TSSOP                | DGG             | 48   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ALVTH16373VRE4  | ACTIVE                | TVSOP                | DGV             | 48   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ALVTH16373VRG4  | ACTIVE                | TVSOP                | DGV             | 48   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| 74ALVTH16373ZQLR  | ACTIVE                | BGA MICROSTAR JUNIOR | ZQL             | 56   | 1000        | Green (RoHS & no Sb/Br) | SNAGCU           | Level-1-260C-UNLIM           |
| SN74ALVTH16373DL  | ACTIVE                | SSOP                 | DL              | 48   | 25          | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74ALVTH16373DLR | ACTIVE                | SSOP                 | DL              | 48   | 1000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74ALVTH16373GR  | ACTIVE                | TSSOP                | DGG             | 48   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |
| SN74ALVTH16373KR  | NRND                  | BGA MICROSTAR JUNIOR | GQL             | 56   | 1000        | TBD                     | SNPB             | Level-1-240C-UNLIM           |
| SN74ALVTH16373VR  | ACTIVE                | TVSOP                | DGV             | 48   | 2000        | Green (RoHS & no Sb/Br) | CU NIPDAU        | Level-1-260C-UNLIM           |

<sup>(1)</sup> The marketing status values are defined as follows:

**ACTIVE:** Product device recommended for new designs.

**LIFEBUY:** TI has announced that the device will be discontinued, and a lifetime-buy period is in effect.

**NRND:** Not recommended for new designs. Device is in production to support existing customers, but TI does not recommend using this part in a new design.

**PREVIEW:** Device has been announced but is not in production. Samples may or may not be available.

**OBSOLETE:** TI has discontinued the production of the device.

<sup>(2)</sup> Eco Plan - The planned eco-friendly classification: Pb-Free (RoHS), Pb-Free (RoHS Exempt), or Green (RoHS & no Sb/Br) - please check <http://www.ti.com/productcontent> for the latest availability information and additional product content details.

**TBD:** The Pb-Free/Green conversion plan has not been defined.

**Pb-Free (RoHS):** TI's terms "Lead-Free" or "Pb-Free" mean semiconductor products that are compatible with the current RoHS requirements for all 6 substances, including the requirement that lead not exceed 0.1% by weight in homogeneous materials. Where designed to be soldered at high temperatures, TI Pb-Free products are suitable for use in specified lead-free processes.

**Pb-Free (RoHS Exempt):** This component has a RoHS exemption for either 1) lead-based flip-chip solder bumps used between the die and package, or 2) lead-based die adhesive used between the die and leadframe. The component is otherwise considered Pb-Free (RoHS compatible) as defined above.

**Green (RoHS & no Sb/Br):** TI defines "Green" to mean Pb-Free (RoHS compatible), and free of Bromine (Br) and Antimony (Sb) based flame retardants (Br or Sb do not exceed 0.1% by weight in homogeneous material)

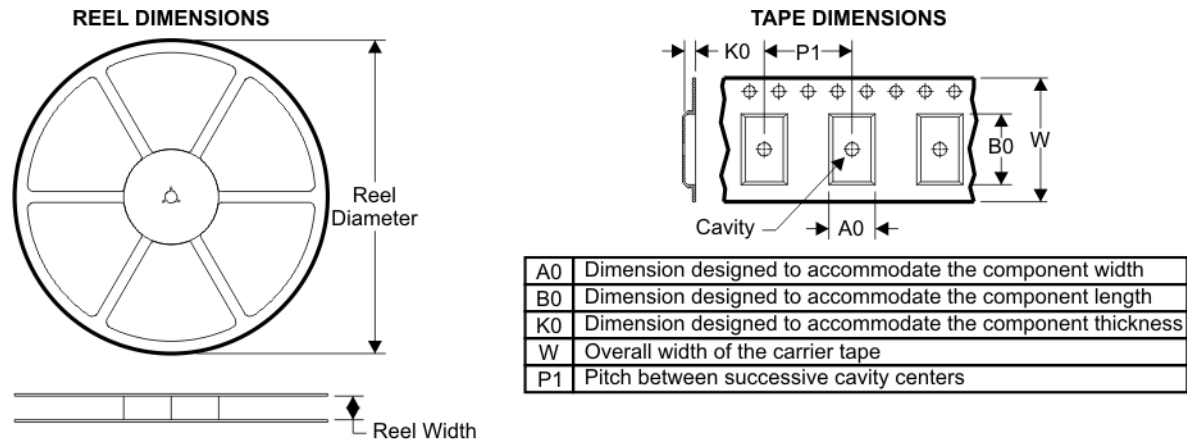
<sup>(3)</sup> MSL, Peak Temp. -- The Moisture Sensitivity Level rating according to the JEDEC industry standard classifications, and peak solder temperature.

**Important Information and Disclaimer:** The information provided on this page represents TI's knowledge and belief as of the date that it is provided. TI bases its knowledge and belief on information provided by third parties, and makes no representation or warranty as to the

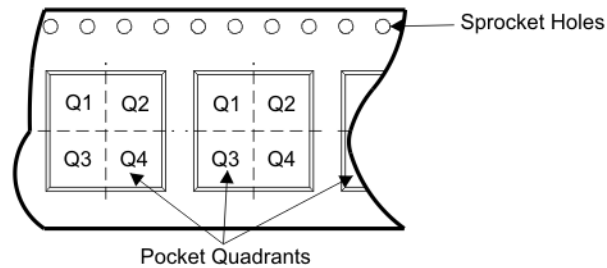
accuracy of such information. Efforts are underway to better integrate information from third parties. TI has taken and continues to take reasonable steps to provide representative and accurate information but may not have conducted destructive testing or chemical analysis on incoming materials and chemicals. TI and TI suppliers consider certain information to be proprietary, and thus CAS numbers and other limited information may not be available for release.

In no event shall TI's liability arising out of such information exceed the total purchase price of the TI part(s) at issue in this document sold by TI to Customer on an annual basis.

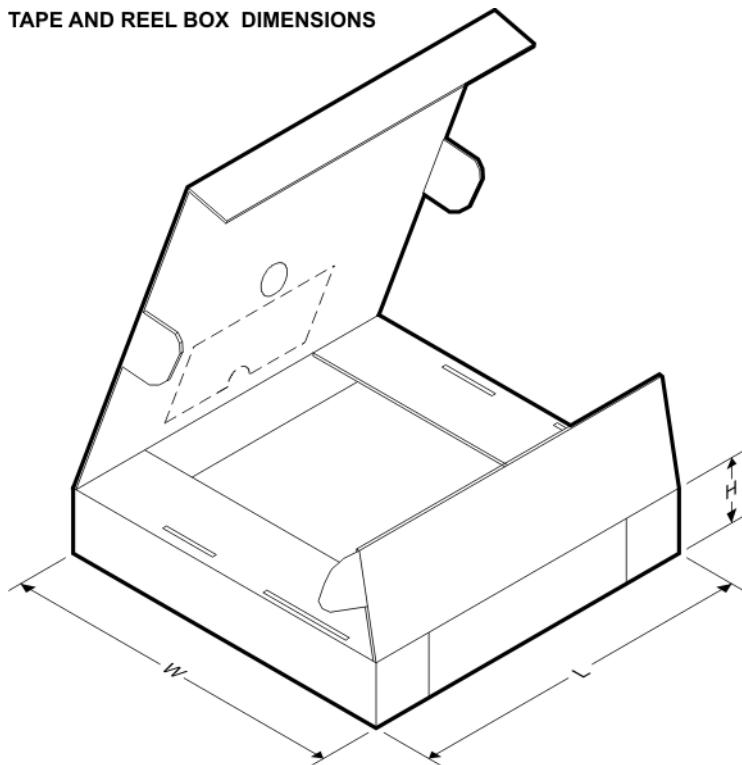
## TAPE AND REEL BOX INFORMATION



### QUADRANT ASSIGNMENTS FOR PIN 1 ORIENTATION IN TAPE



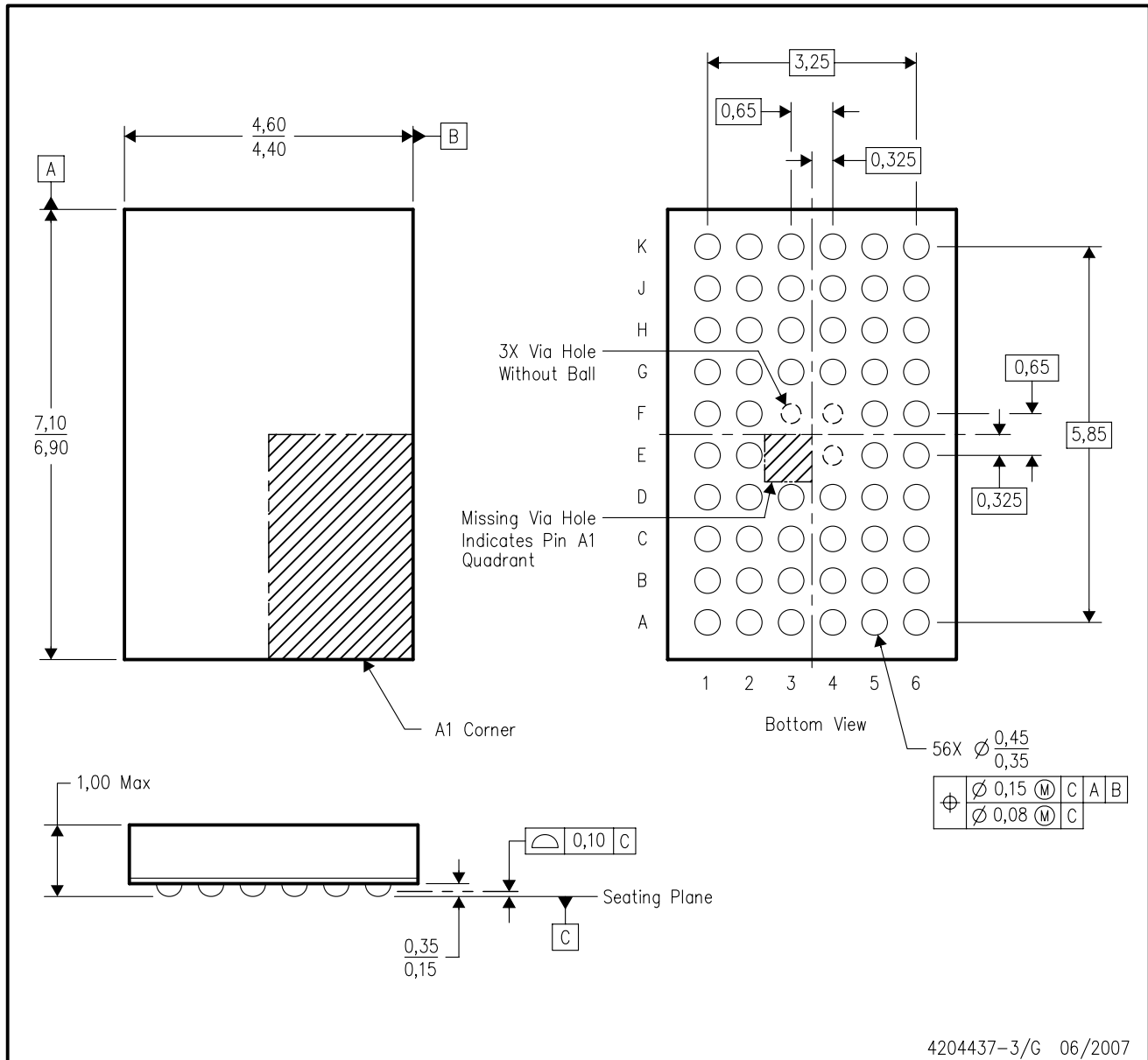
| Device            | Package | Pins | Site    | Reel Diameter (mm) | Reel Width (mm) | A0 (mm) | B0 (mm) | K0 (mm) | P1 (mm) | W (mm) | Pin1 Quadrant |
|-------------------|---------|------|---------|--------------------|-----------------|---------|---------|---------|---------|--------|---------------|
| 74ALVTH16373ZQLR  | ZQL     | 56   | SITE 32 | 330                | 16              | 4.8     | 7.3     | 1.45    | 8       | 16     | Q1            |
| SN74ALVTH16373DLR | DL      | 48   | SITE 41 | 330                | 32              | 11.35   | 16.2    | 3.1     | 16      | 32     | Q1            |
| SN74ALVTH16373GR  | DGG     | 48   | SITE 41 | 330                | 24              | 8.6     | 15.8    | 1.8     | 12      | 24     | Q1            |
| SN74ALVTH16373KR  | GQL     | 56   | SITE 32 | 330                | 16              | 4.8     | 7.3     | 1.45    | 8       | 16     | Q1            |
| SN74ALVTH16373VR  | DGV     | 48   | SITE 41 | 330                | 24              | 6.8     | 10.1    | 1.6     | 12      | 24     | Q1            |

**TAPE AND REEL BOX DIMENSIONS**


| Device            | Package | Pins | Site    | Length (mm) | Width (mm) | Height (mm) |
|-------------------|---------|------|---------|-------------|------------|-------------|
| 74ALVTH16373ZQLR  | ZQL     | 56   | SITE 32 | 346.0       | 346.0      | 0.0         |
| SN74ALVTH16373DLR | DL      | 48   | SITE 41 | 346.0       | 346.0      | 0.0         |
| SN74ALVTH16373GR  | DGG     | 48   | SITE 41 | 346.0       | 346.0      | 0.0         |
| SN74ALVTH16373KR  | GQL     | 56   | SITE 32 | 346.0       | 346.0      | 0.0         |
| SN74ALVTH16373VR  | DGV     | 48   | SITE 41 | 346.0       | 346.0      | 0.0         |

## ZQL (R-PBGA-N56)

## PLASTIC BALL GRID ARRAY

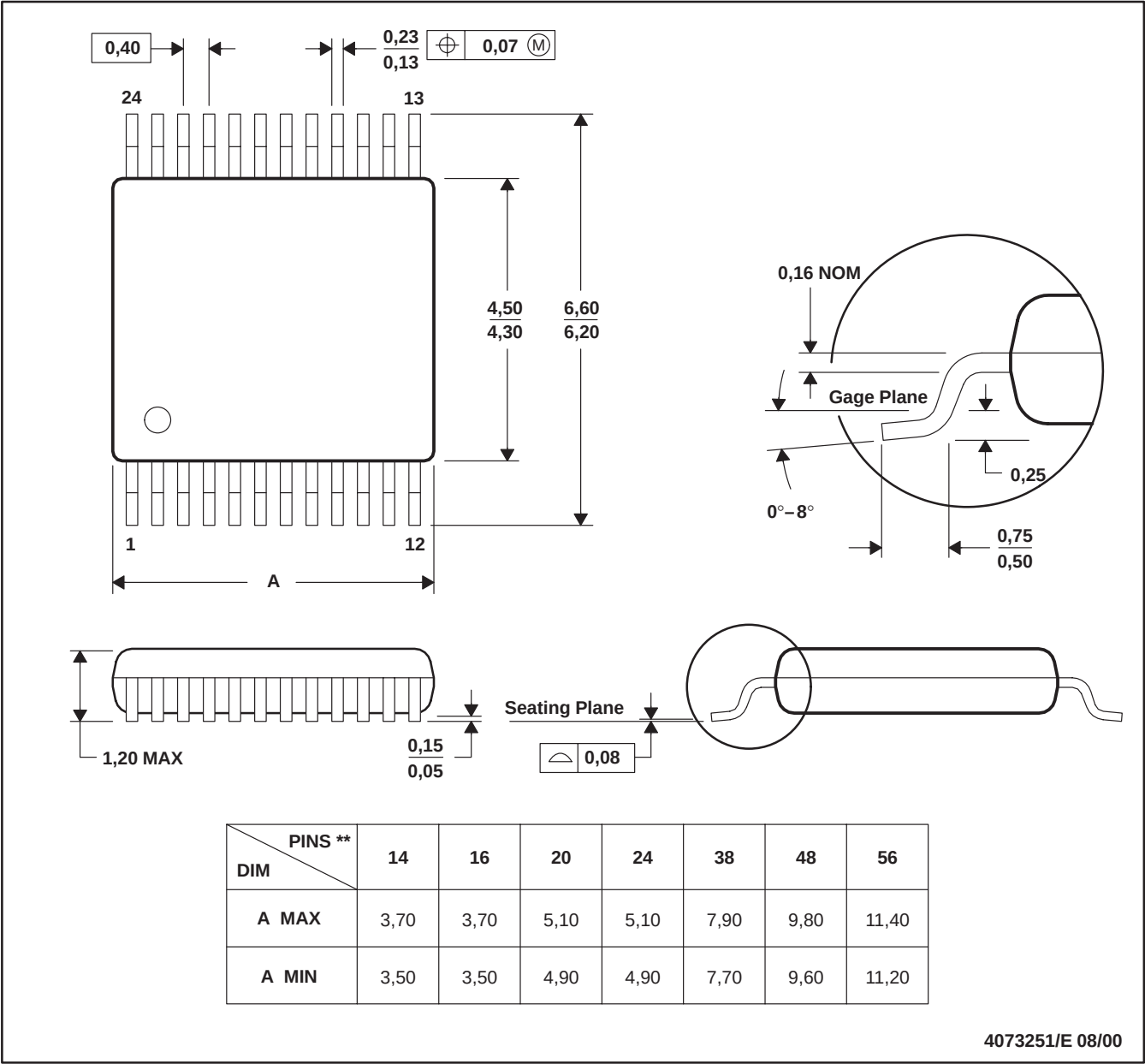


- NOTES:
- All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
  - This drawing is subject to change without notice.
  - Falls within JEDEC MO-285 variation BA-2.
  - This package is lead-free. Refer to the 56 GQL package (drawing 4200583) for tin-lead (SnPb).

DGV (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE

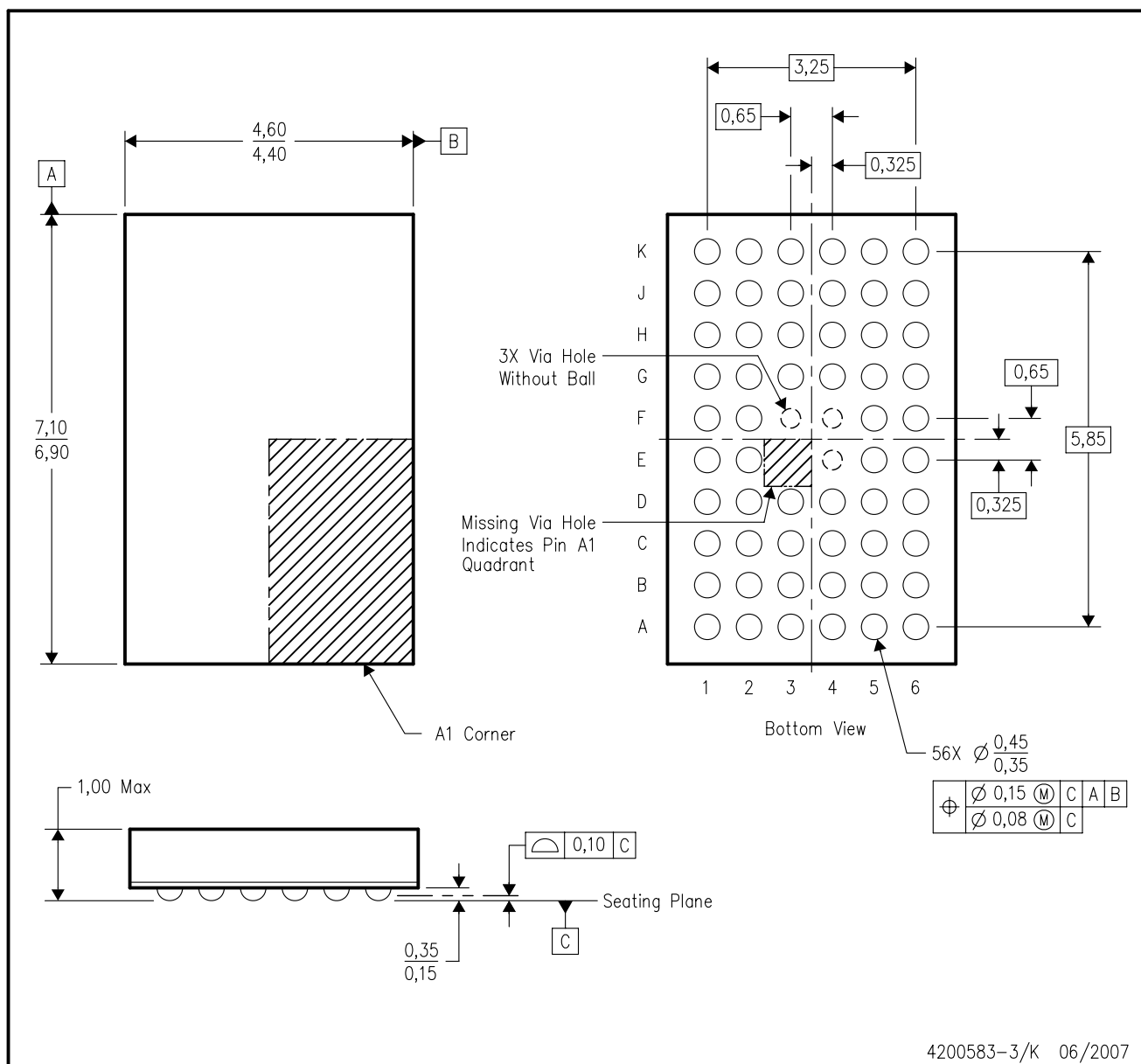
24 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
B. This drawing is subject to change without notice.  
C. Body dimensions do not include mold flash or protrusion, not to exceed 0,15 per side.  
D. Falls within JEDEC: 24/48 Pins – MO-153  
14/16/20/56 Pins – MO-194

## GQL (R-PBGA-N56)

## PLASTIC BALL GRID ARRAY



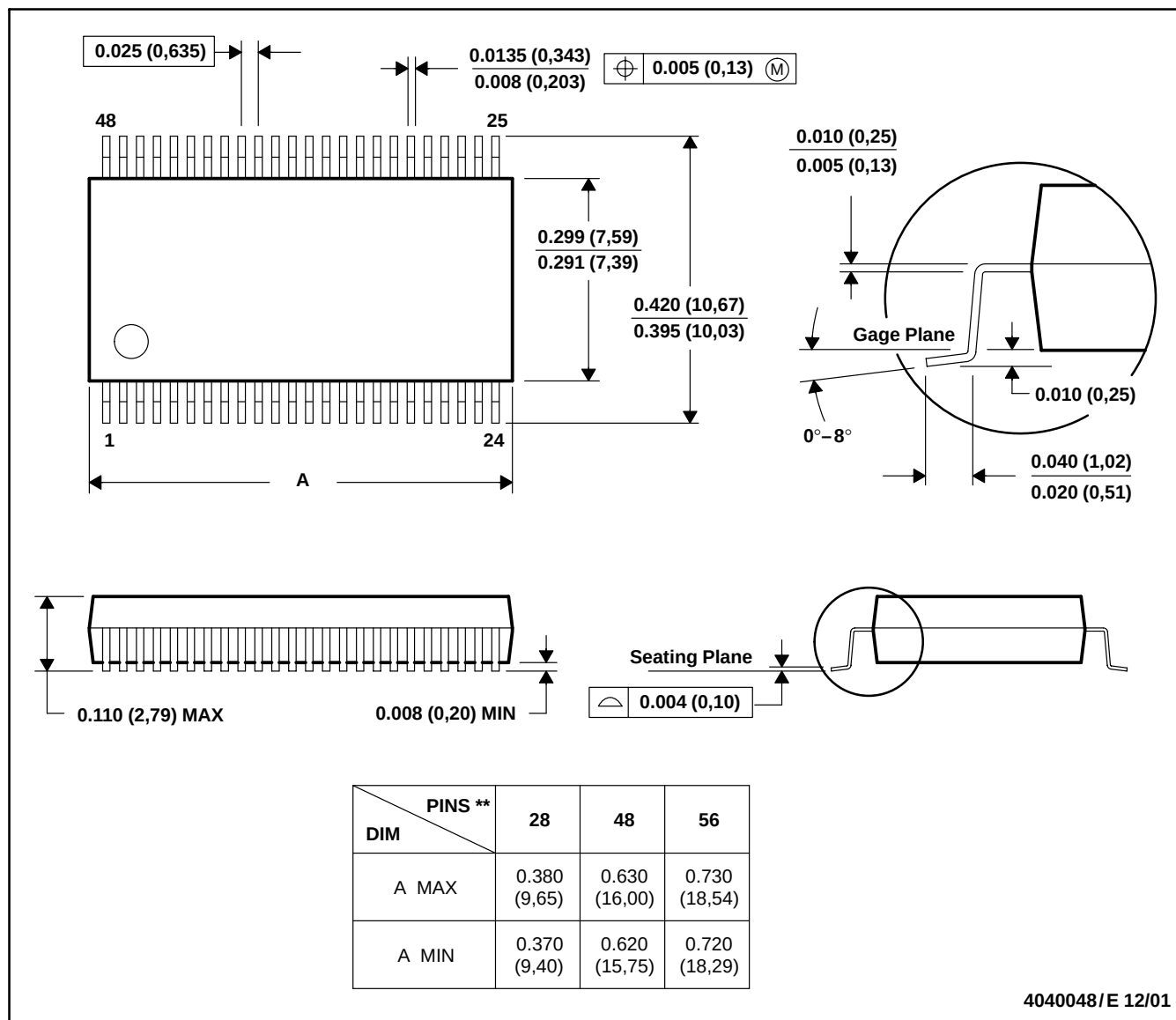
- NOTES:
- All linear dimensions are in millimeters. Dimensioning and tolerancing per ASME Y14.5M-1994.
  - This drawing is subject to change without notice.
  - Falls within JEDEC MO-285 variation BA-2.
  - This package is tin-lead (SnPb). Refer to the 56 ZQL package (drawing 4204437) for lead-free.



DL (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

48 PINS SHOWN



- NOTES: A. All linear dimensions are in inches (millimeters).  
 B. This drawing is subject to change without notice.  
 C. Body dimensions do not include mold flash or protrusion not to exceed 0.006 (0,15).  
 D. Falls within JEDEC MO-118

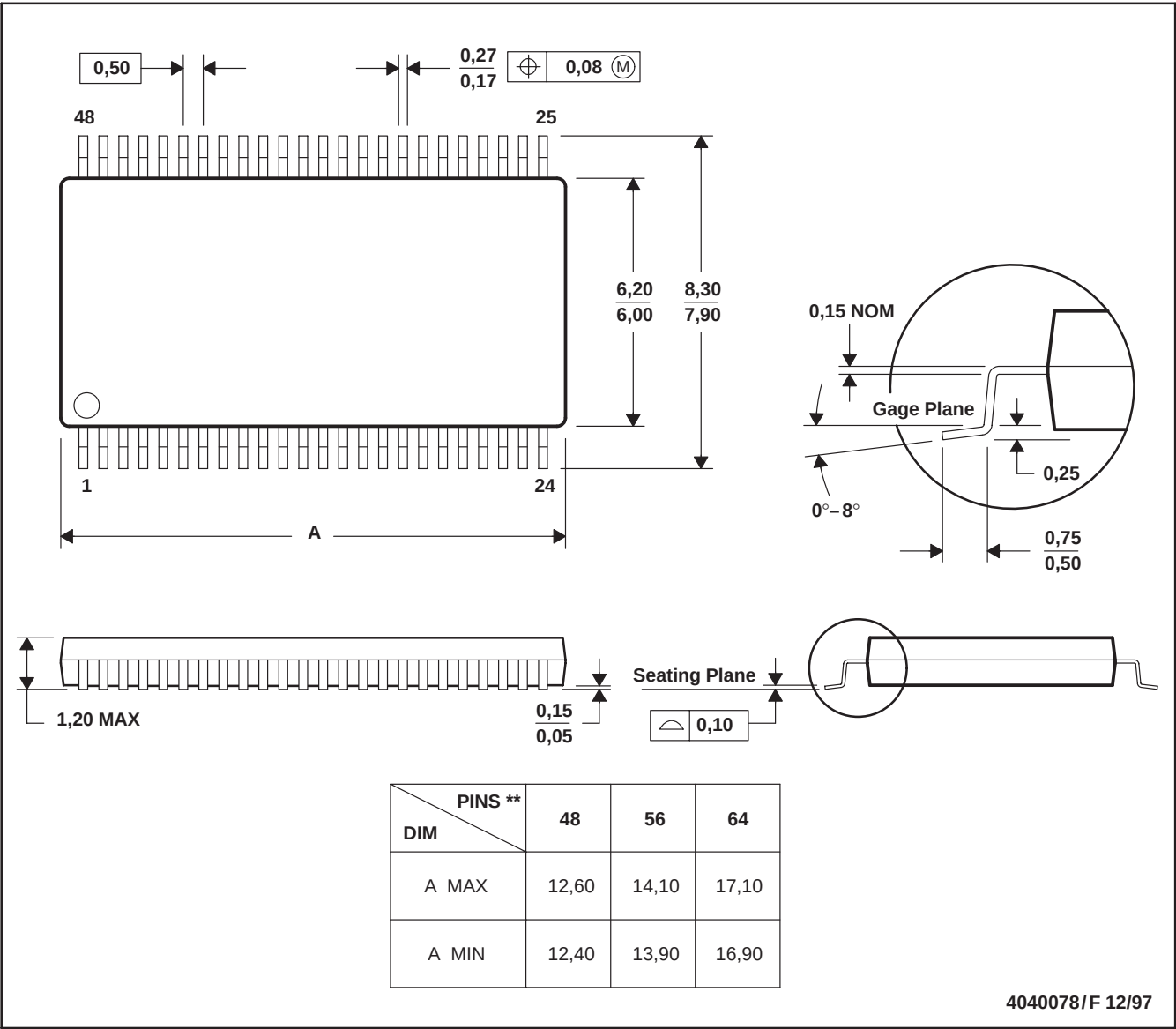
查询"74ALVTH16373GRG4"供应商

MTSS003D – JANUARY 1995 – REVISED JANUARY 1998

DGG (R-PDSO-G\*\*)

PLASTIC SMALL-OUTLINE PACKAGE

48 PINS SHOWN



- NOTES: A. All linear dimensions are in millimeters.  
B. This drawing is subject to change without notice.  
C. Body dimensions do not include mold protrusion not to exceed 0,15.  
D. Falls within JEDEC MO-153

## IMPORTANT NOTICE

Texas Instruments Incorporated and its subsidiaries (TI) reserve the right to make corrections, modifications, enhancements, improvements, and other changes to its products and services at any time and to discontinue any product or service without notice. Customers should obtain the latest relevant information before placing orders and should verify that such information is current and complete. All products are sold subject to TI's terms and conditions of sale supplied at the time of order acknowledgment.

TI warrants performance of its hardware products to the specifications applicable at the time of sale in accordance with TI's standard warranty. Testing and other quality control techniques are used to the extent TI deems necessary to support this warranty. Except where mandated by government requirements, testing of all parameters of each product is not necessarily performed.

TI assumes no liability for applications assistance or customer product design. Customers are responsible for their products and applications using TI components. To minimize the risks associated with customer products and applications, customers should provide adequate design and operating safeguards.

TI does not warrant or represent that any license, either express or implied, is granted under any TI patent right, copyright, mask work right, or other TI intellectual property right relating to any combination, machine, or process in which TI products or services are used. Information published by TI regarding third-party products or services does not constitute a license from TI to use such products or services or a warranty or endorsement thereof. Use of such information may require a license from a third party under the patents or other intellectual property of the third party, or a license from TI under the patents or other intellectual property of TI.

Reproduction of TI information in TI data books or data sheets is permissible only if reproduction is without alteration and is accompanied by all associated warranties, conditions, limitations, and notices. Reproduction of this information with alteration is an unfair and deceptive business practice. TI is not responsible or liable for such altered documentation. Information of third parties may be subject to additional restrictions.

Resale of TI products or services with statements different from or beyond the parameters stated by TI for that product or service voids all express and any implied warranties for the associated TI product or service and is an unfair and deceptive business practice. TI is not responsible or liable for any such statements.

TI products are not authorized for use in safety-critical applications (such as life support) where a failure of the TI product would reasonably be expected to cause severe personal injury or death, unless officers of the parties have executed an agreement specifically governing such use. Buyers represent that they have all necessary expertise in the safety and regulatory ramifications of their applications, and acknowledge and agree that they are solely responsible for all legal, regulatory and safety-related requirements concerning their products and any use of TI products in such safety-critical applications, notwithstanding any applications-related information or support that may be provided by TI. Further, Buyers must fully indemnify TI and its representatives against any damages arising out of the use of TI products in such safety-critical applications.

TI products are neither designed nor intended for use in military/aerospace applications or environments unless the TI products are specifically designated by TI as military-grade or "enhanced plastic." Only products designated by TI as military-grade meet military specifications. Buyers acknowledge and agree that any such use of TI products which TI has not designated as military-grade is solely at the Buyer's risk, and that they are solely responsible for compliance with all legal and regulatory requirements in connection with such use.

TI products are neither designed nor intended for use in automotive applications or environments unless the specific TI products are designated by TI as compliant with ISO/TS 16949 requirements. Buyers acknowledge and agree that, if they use any non-designated products in automotive applications, TI will not be responsible for any failure to meet such requirements.

Following are URLs where you can obtain information on other Texas Instruments products and application solutions:

| Products              |                                                                    | Applications       |                                                                          |
|-----------------------|--------------------------------------------------------------------|--------------------|--------------------------------------------------------------------------|
| Amplifiers            | <a href="http://amplifier.ti.com">amplifier.ti.com</a>             | Audio              | <a href="http://www.ti.com/audio">www.ti.com/audio</a>                   |
| Data Converters       | <a href="http://dataconverter.ti.com">dataconverter.ti.com</a>     | Automotive         | <a href="http://www.ti.com/automotive">www.ti.com/automotive</a>         |
| DSP                   | <a href="http://dsp.ti.com">dsp.ti.com</a>                         | Broadband          | <a href="http://www.ti.com/broadband">www.ti.com/broadband</a>           |
| Interface             | <a href="http://interface.ti.com">interface.ti.com</a>             | Digital Control    | <a href="http://www.ti.com/digitalcontrol">www.ti.com/digitalcontrol</a> |
| Logic                 | <a href="http://logic.ti.com">logic.ti.com</a>                     | Military           | <a href="http://www.ti.com/military">www.ti.com/military</a>             |
| Power Mgmt            | <a href="http://power.ti.com">power.ti.com</a>                     | Optical Networking | <a href="http://www.ti.com/opticalnetwork">www.ti.com/opticalnetwork</a> |
| Microcontrollers      | <a href="http://microcontroller.ti.com">microcontroller.ti.com</a> | Security           | <a href="http://www.ti.com/security">www.ti.com/security</a>             |
| RFID                  | <a href="http://www.ti-rfid.com">www.ti-rfid.com</a>               | Telephony          | <a href="http://www.ti.com/telephony">www.ti.com/telephony</a>           |
| Low Power<br>Wireless | <a href="http://www.ti.com/lpw">www.ti.com/lpw</a>                 | Video & Imaging    | <a href="http://www.ti.com/video">www.ti.com/video</a>                   |
|                       |                                                                    | Wireless           | <a href="http://www.ti.com/wireless">www.ti.com/wireless</a>             |