

## DELPHI SERIES



### FEATURES

- ◆ High Efficiency:  
94.0% @ 12Vin, 5V/10A out
- ◆ Size: 10.4mm x 16.5mm x 11.0 mm  
(0.41"×0.65"×0.43")
- ◆ Wide input range: 3.0V~13.8V
- ◆ Output voltage programmable from 0.59Vdc to 5.1Vdc via external resistors
- ◆ No minimum load required
- ◆ Fixed frequency operation
- ◆ Input UVLO, output OCP
- ◆ Remote ON/OFF (Positive, 5pin version)
- ◆ ISO 9001, TL 9000, ISO 14001, QS9000, OHSAS18001 certified manufacturing facility
- ◆ UL/cUL 60950 (US & Canada), TUV (EN60950) --pending

### Delphi NE Series Non-Isolated Point of Load DC/DC Modules: 3.0~13.8Vin, 0.59V-5.1Vout, 10Aout

The Delphi NE 10A Series, 3.0~13.8V wide input, wide trim single output, non-isolated point of load (POL) DC/DC converters are the latest offering from a world leader in power systems technology and manufacturing — Delta Electronics, Inc. The NE product family is the second generation, non-isolated point-of-load DC/DC power modules for the datacom applications which cut the module size by almost 50% in most of the cases compared to the first generation NC series POL modules. The NE 10A product family provides an ultra wide input range to support 3.3V, 5V, 8V, 9.6V, and 12V bus voltage point-of-load applications and it offers up to 10A of output current in a vertically or horizontally mounted through-hole miniature package and the output can be resistor trimmed from 0.59Vdc to 5.1Vdc. It provides a very cost effective, high efficiency, and high density point of load solution. With creative design technology and optimization of component placement, these converters possess outstanding electrical and thermal performance, as well as extremely high reliability under highly stressful operating conditions.

### OPTIONS

- ◆ Vertical or horizontal versions

### APPLICATIONS

- ◆ DataCom
- ◆ Distributed power architectures
- ◆ Servers and workstations
- ◆ LAN/WAN applications
- ◆ Data processing applications

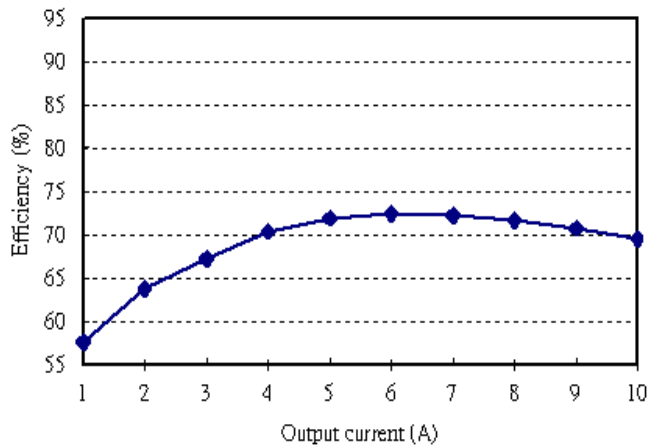
# TECHNICAL SPECIFICATIONS

(Ambient Temperature=25°C, minimum airflow=200LFM, nominal  $V_{in}$ =12Vdc unless otherwise specified.)

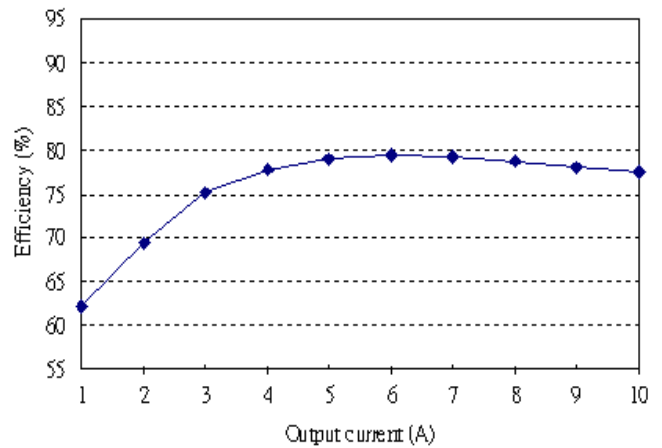
PARAMETER	NOTES and CONDITIONS	NE12S0A0V/H10			
		Min.	Typ.	Max.	Units
<b>ABSOLUTE MAXIMUM RATINGS</b>					
Input Voltage		3.0		13.8	Vdc
Operating Temperature (Vertical)	Refer to Fig.25 for the measuring point	0		109	°C
Storage Temperature		-55		125	°C
<b>INPUT CHARACTERISTICS</b>					
Operating Input Voltage		3.0		13.8	V
Input Under-Voltage Lockout					
Turn-On Voltage Threshold			3.1		V
Turn-Off Voltage Threshold			2.8		V
Lockout Hysteresis Voltage			0.3		V
Maximum Input Current	12Vin, 5Vo, operating, full load		4.5		A
No-Load Input Current	Vin=12V, Vout=5V		80		mA
Off Converter Input Current	Remote OFF		10		mA
Input Reflected-Ripple Current			5	10	mA
Input Ripple Rejection	120Hz		60		dB
<b>OUTPUT CHARACTERISTICS</b>					
Output Voltage Adjustment Range		0.59		5.1	V
Output Voltage Set Point	With a 0.1% trim resistor	-1		+1	%
Output Voltage Regulation					
Over Load	Io=Io_min to Io_max		± 0.5	± 1	%
Over Line	Vin=Vin_min to Vin_max		± 0.2	± 0.4	%
Over temperature	Ta=0~70°C		± 0.3	± 0.6	%
Total output range	Over load, line, temperature regulation and set point	-3		+3	%
Output Voltage Ripple and Noise	5Hz to 20MHz bandwidth				
Peak-to-Peak	Full Load, 10uF Tan cap, 12Vin, 0.5Vo		10		mV
Peak-to-Peak	Full Load, 10uF Tan cap, 12Vin, 0.9Vo		15		mV
Peak-to-Peak	Full Load, 10uF Tan cap, 12Vin, 2.5Vo		30		mV
Peak-to-Peak	Full Load, 10uF Tan cap, 12Vin, 5Vo		60		mV
RMS	Full Load, 10uF Tan cap, 12Vin, 5Vo		10		mV
Output Current Range		0		10	A
Output Voltage Over-shoot at Start-up	Vin=12V, Turn ON			0.5	%
Output Voltage Under-shoot at Power-Off	Vin=12V, Turn OFF			100	mV
Output DC Current-Limit Inception	Hiccup mode	110		200	%Iomax
Output short-circuit current RMS value			4		Arms
<b>DYNAMIC CHARACTERISTICS</b>					
Output Dynamic Load Response	12Vin, 5Vout, 10uF ceramic cap				
Positive Step Change in Output Current	50~100% load, 10A/uS		300		mV
Negative Step Change in Output Current	50~100% load, 10A/uS		300		mV
Settling Time	Settling to be within regulation band (to 10% Vo deviation)		100		us
Turn-On Transient					
Start-Up Time, from On/Off Control	From Enable high to 90% of Vo			3	ms
Start-Up Time, from input power	From Vin=12V to 90% of Vo			3	ms
Minimum Output Capacitance		0			uF
Maximum Output Startup Capacitive Load	Full Load, 12Vin, 5Vo			1000	uF
<b>EFFICIENCY</b>					
Vo=0.59V	Vin=12V, Io=10A		70		%
Vo=0.9V	Vin=12V, Io=10A		77.5		%
Vo=2.5V	Vin=12V, Io=10A		89.5		%
Vo=5.0V	Vin=12V, Io=10A		94		%
<b>SINK EFFICIENCY</b>					
Vo=5.0V	Vin=12V, Io=10A		91		%
<b>FEATURE CHARACTERISTICS</b>					
Switching Frequency	Fixed		600		KHz
ON/OFF Control	Positive logic (internally pulled high)				
Logic High	Module On (or leave the pin open)	0.8			V
Logic Low	Module Off	0		0.3	V
<b>GENERAL SPECIFICATIONS</b>					
Calculated MTBF	25°C, 300LFM, 80% load		TBD		Mhours
Weight			2		grams



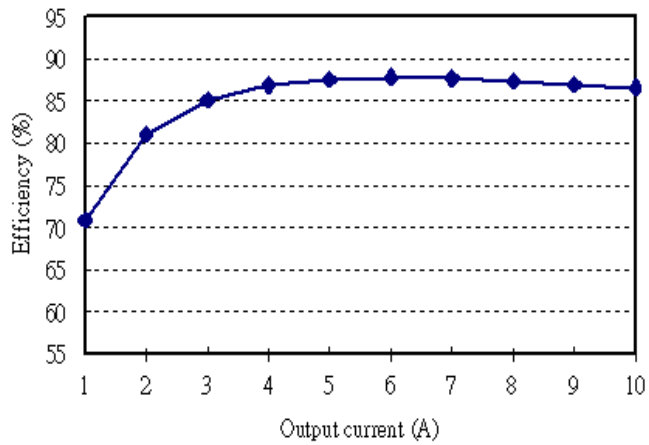
# ELECTRICAL CHARACTERISTICS CURVES



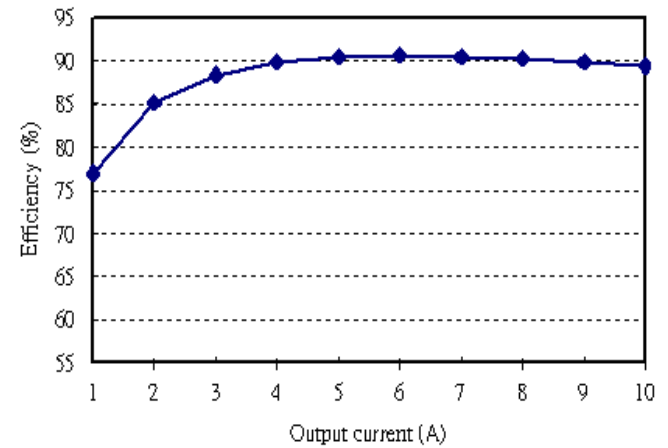
**Figure 1:** Converter efficiency vs. output current (0.59V output voltage, 12V input)



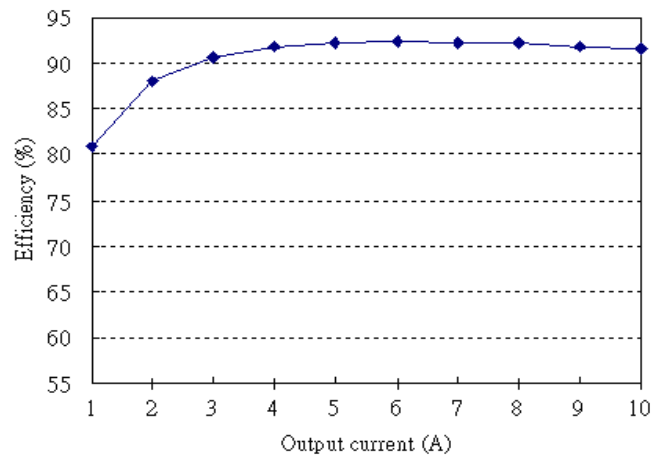
**Figure 2:** Converter efficiency vs. output current (0.9V output voltage, 12V input)



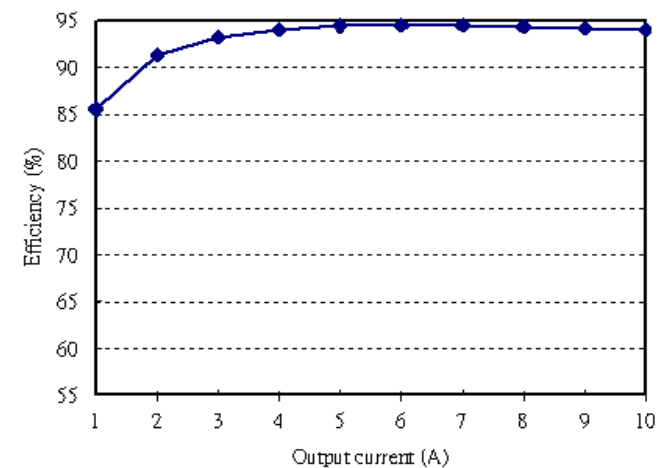
**Figure 3:** Converter efficiency vs. output current (1.8V output voltage, 12V input)



**Figure 4:** Converter efficiency vs. output current (2.5V output voltage, 12V input)



**Figure 5:** Converter efficiency vs. output current (3.3V output voltage, 12V input)



**Figure 6:** Converter efficiency vs. output current (5.0V output voltage, 12V input)



# ELECTRICAL CHARACTERISTICS CURVES (CON.)

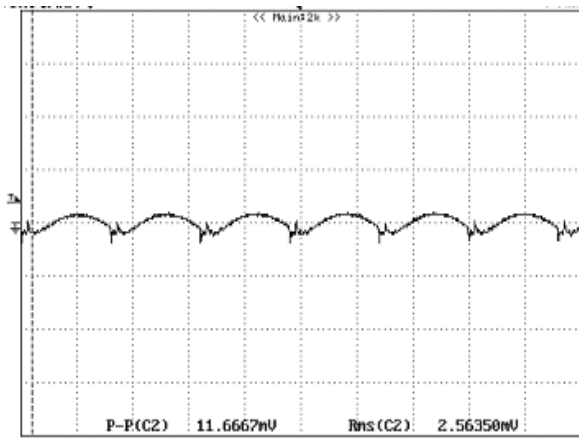


Figure 7: Output ripple & noise at 12Vin, 0.59V/10A out

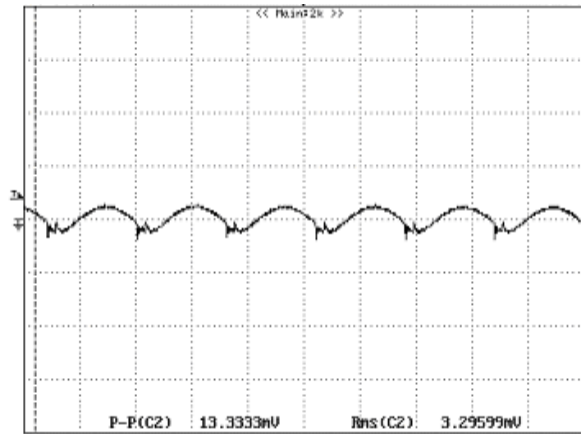


Figure 8: Output ripple & noise at 12Vin, 0.9V/10A out

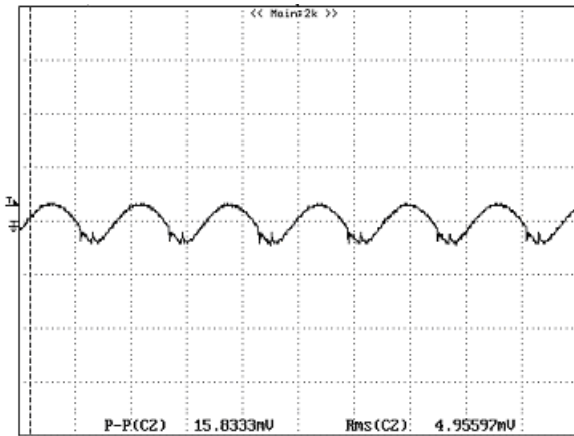


Figure 9: Output ripple & noise at 12Vin, 1.8V/10A out

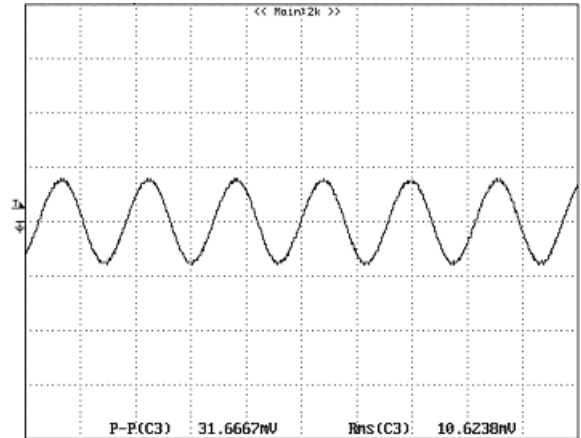


Figure 10: Output ripple & noise at 12Vin, 2.5V/10A out

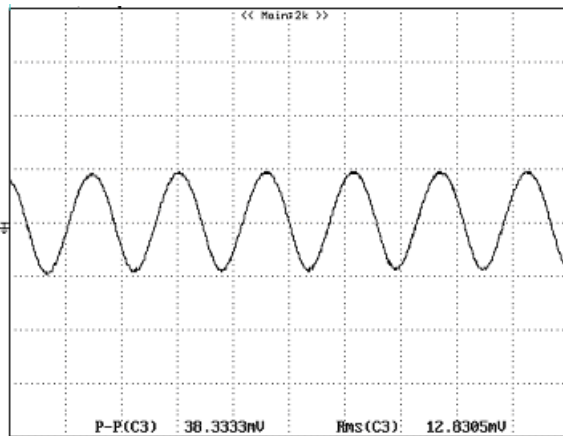


Figure 11: Output ripple & noise at 12Vin, 3.3V/10A out

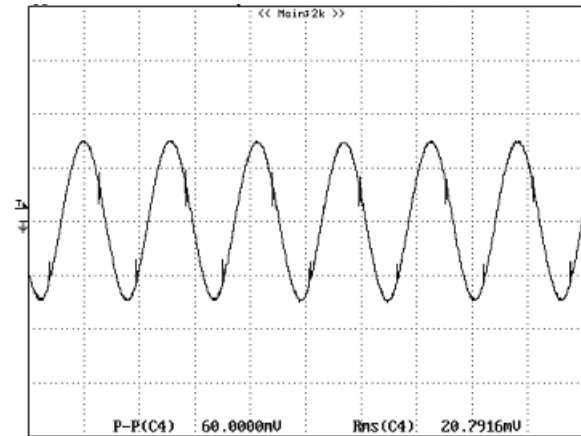
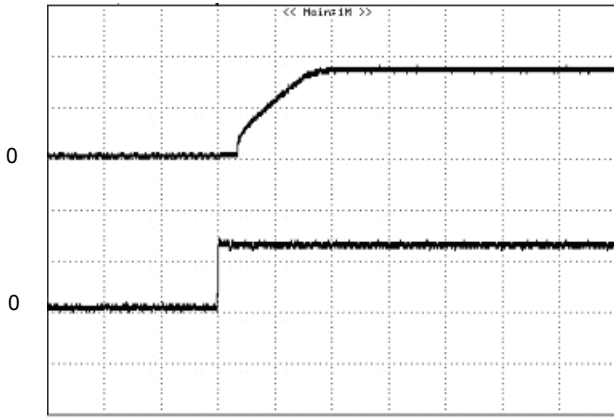


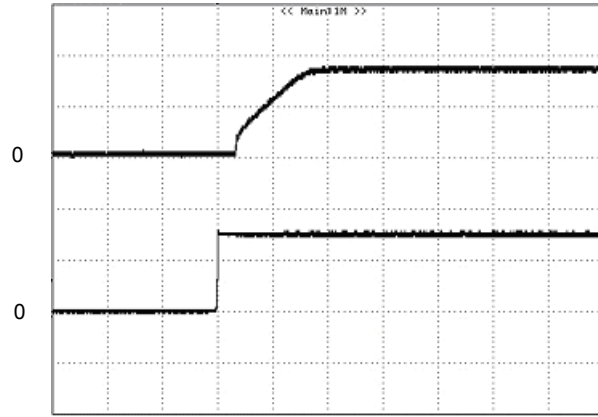
Figure 12: Output ripple & noise at 12Vin, 5.0V/10A out



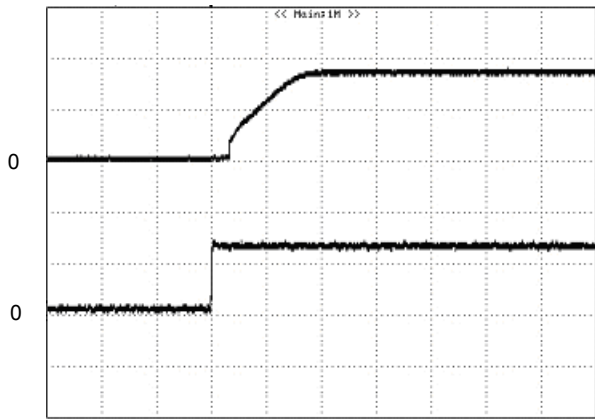
# ELECTRICAL CHARACTERISTICS CURVES (CON.)



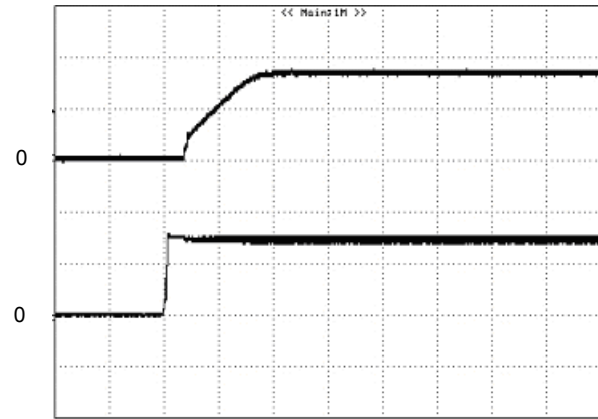
**Figure 13:** Turn on delay time at 12Vin, 1.0V/10A out  
Ch1: Vin Ch4: Vout



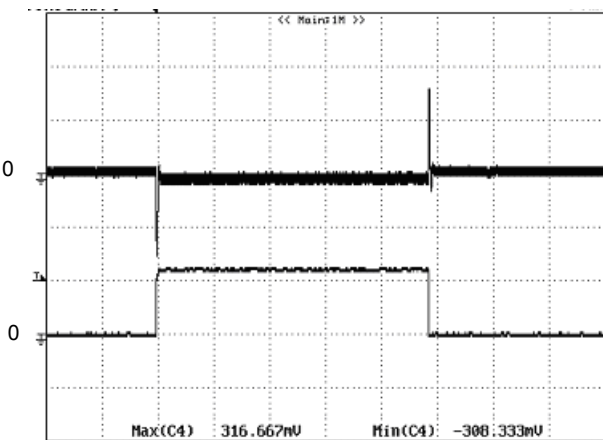
**Figure 14:** Turn on delay time Remote On/Off, 1.0V/10A out  
Ch1: Enable Ch4: Vout



**Figure 15:** Turn on delay time at 12Vin, 3.3V/10A out  
Ch1: Vin Ch4: Vout



**Figure 16:** Turn on delay time at Remote On/Off, 3.3V/10A out  
Ch1: Enable Ch4: Vout



**Figure 17:** Typical transient response to step load change at  
10A/μS from 50%~100% load, at 12Vin, 2.5V out



## DESIGN CONSIDERATIONS

The NE10 is a single phase and voltage mode controlled Buck topology. The output can be trimmed in the range of 0.59Vdc to 5.1Vdc by a resistor from Trim pin to Ground.

The converter can be turned ON/OFF by remote control with positive on/off (ENABLE pin) logic. The converter DC output is disabled when the signal is driven low (below 0.3V). This pin is also used as the input turn on threshold judgment. Its voltage is percent of Input voltage during floating due to internal connection. So we do not suggest using an active high signal (higher than 0.8V) to turn on the module because this high level voltage will disable UVLO function. The module will turn on when this pin is floating and the input voltage is higher than the threshold.

The converter can protect itself by entering hiccup mode against over current and short circuit condition. Also, the converter will shut down when an over voltage protection is detected.

### Safety Considerations

It is recommended that the user to provide a very fast-acting type fuse in the input line for safety. The output voltage set-point and the output current in the application could define the amperage rating of the fuse.

## FEATURES DESCRIPTIONS

### Enable (On/Off)

The ENABLE (on/off) input allows external circuitry to put the NE converter into a low power dissipation (sleep) mode. Positive ENABLE is available as standard. With the active high function, the output is guaranteed to turn on if the ENABLE pin is driven above 0.8V. The output will turn off if the ENABLE pin voltage is pulled below 0.3V.

### Undervoltage Lockout

The ENABLE pin is also used as input UVLO function. Leaving the enable floating, the module will turn on if the input voltage is higher than the turn-on threshold and turn off if the input voltage is lower than the turn-off threshold. The default turn-on voltage is 3.1V with 300mV hysteresis.

The turn-on voltage may be adjusted with a resistor placed between the "Enable" pin and "Ground" pin. The equation for calculating the value of this resistor is:

$$V_{EN\_RTH} = \frac{15.05 \times (R + 6.34)}{6.34 \times R} + 0.8$$

$$V_{EN\_FTH} = V_{EN\_RTH} - 0.3V$$

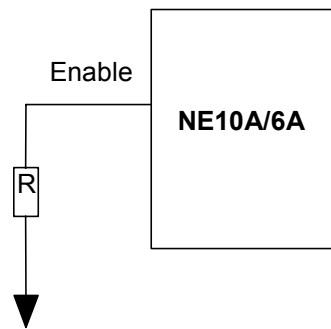


Fig. 18. UVLO setting

$V_{EN\_FTH}$  is the turn-off threshold

$V_{EN\_RTH}$  is the turn-on threshold

R (Kohm) is the outen resistor connected from Enable pin to the GND

An active high voltage will disable the input UVLO function.



## FEATURES DESCRIPTIONS (CON.)

The ENABLE input can be driven in a variety of ways as shown in Figures 19 and 20. If the ENABLE signal comes from the primary side of the circuit, the ENABLE can be driven through either a bipolar signal transistor (Figure 18). If the enable signal comes from the secondary side, then an opto-coupler or other isolation devices must be used to bring the signal across the voltage isolation (please see Figure 19).

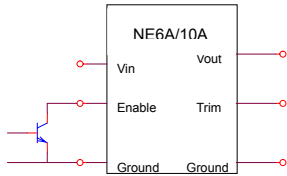


Figure 19: Enable Input drive circuit for NE series

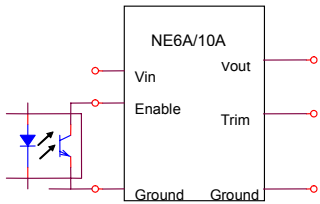


Figure 20: Enable input drive circuit example with isolation.

### Input Under-Voltage Lockout

The input under-voltage lockout prevents the converter from being damaged while operating when the input voltage is too low. The lockout occurs between 2.8V to 3.1V.

### Over-Current and Short-Circuit Protection

The NE series modules have non-latching over-current and short-circuit protection circuitry. When over current condition occurs, the module goes into the non-latching hiccup mode. When the over-current condition is removed, the module will resume normal operation.

An over current condition is detected by measuring the voltage drop across the MOSFETs. The voltage drop across the MOSFET is also a function of the MOSFET's Rds(on). Rds(on) is affected by temperature, therefore ambient temperature will affect the current limit inception point. Please see the electrical characteristics for details of the OCP function.

The detection of the Rds(on) of MOSFETs also acts as an over temperature protection since high temperature will cause the Rds(on) of the MOSFETs to increase, eventually triggering over-current protection.

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## Output Voltage Programming

The output voltage of the NE series is trimmable by connecting an external resistor between the trim pin and output ground as shown Figure 21 and the typical trim resistor values are shown in Table 1.

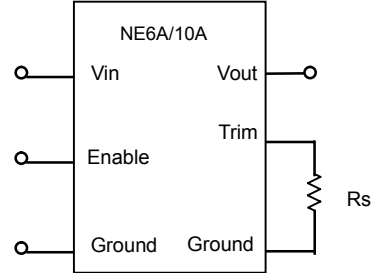


Figure 21: Trimming Output Voltage

The NE10 module has a trim range of 1.0V to 3.3V. The trim resistor equation for the NE10A is :

$$R_s(\Omega) = \frac{1184}{V_{out} - 0.592}$$

Vout is the output voltage setpoint

Rs is the resistance between Trim and Ground

Rs values should not be less than 240Ω

Output Voltage	Rs (Ω)
0.59V	open
+1 V	2.4k
+1.5 V	1.3K
+2.5 V	619
+3.3 V	436
+5.0V	268
+5.5V	240

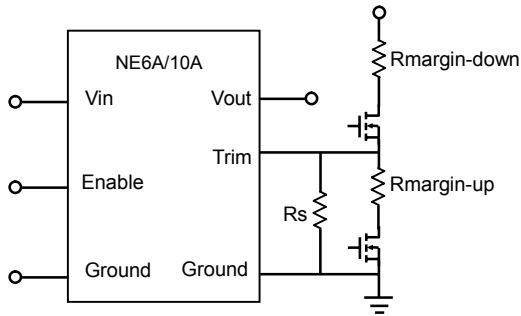
Table 1: Typical trim resistor values

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## FEATURES DESCRIPTIONS (CON.)

### Voltage Margining Adjustment

Output voltage margin adjusting can be implemented in the NE modules by connecting a resistor,  $R_{\text{margin-up}}$ , from the Trim pin to the Ground for margining up the output voltage. Also, the output voltage can be adjusted lower by connecting a resistor,  $R_{\text{margin-down}}$ , from the Trim pin to the voltage source  $V_t$ . Figure 22 shows the circuit configuration for output voltage margining adjustment.



**Figure 22:** Circuit configuration for output voltage margining

### Paralleling

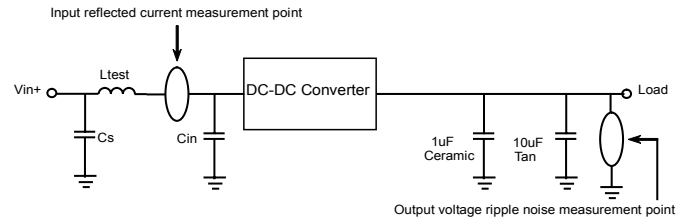
NE10 converters do not have built-in current sharing (paralleling) ability. Hence, paralleling of multiple NE10 converter is not recommended.

### Output Capacitance

There is output capacitor on the NE series modules. Hence, an external output capacitor is required for stable operation.

### Reflected Ripple Current and Output Ripple and Noise Measurement

The measurement set-up outlined in Figure 23 has been used for both input reflected/ terminal ripple current and output voltage ripple and noise measurements on NE series converters.



$C_s=270\mu\text{F}^*1$ ,  $L_{\text{test}}=2\mu\text{H}$ ,  $C_{\text{in}}=270\mu\text{F}^*1$

**Figure 23:** Input reflected ripple/ capacitor ripple current and output voltage ripple and noise measurement setup for NE10



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## THERMAL CONSIDERATION

Thermal management is an important part of the system design. To ensure proper, reliable operation, sufficient cooling of the power module is needed over the entire temperature range of the module. Convection cooling is usually the dominant mode of heat transfer.

Hence, the choice of equipment to characterize the thermal performance of the power module is a wind tunnel.

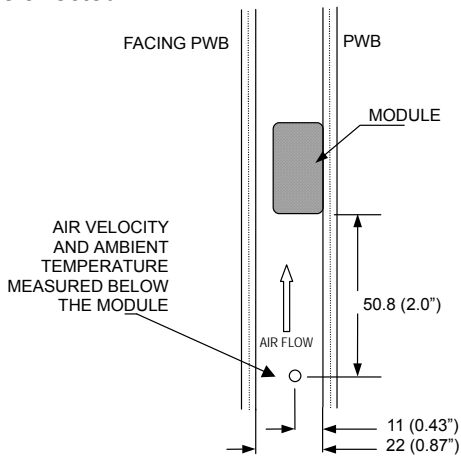
### Thermal Testing Setup

Delta's DC/DC power modules are characterized in heated vertical wind tunnels that simulate the thermal environments encountered in most electronics equipment. This type of equipment commonly uses vertically mounted circuit cards in cabinet racks in which the power modules are mounted.

The following figure shows the wind tunnel characterization setup. The power module is mounted on a test PWB and is vertically positioned within the wind tunnel. The space between the neighboring PWB and the top of the power module is constantly kept at 6.35mm (0.25").

### Thermal Derating

Heat can be removed by increasing airflow over the module. To enhance system reliability, the power module should always be operated below the maximum operating temperature. If the temperature exceeds the maximum module temperature, reliability of the unit may be affected.

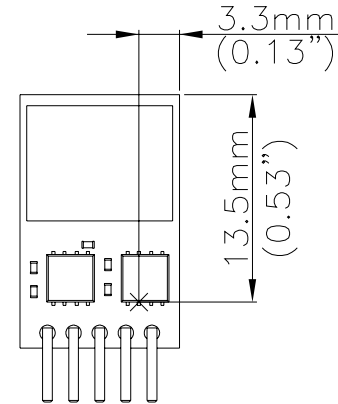


**Note:** Wind tunnel test setup figure dimensions are in millimeters and (Inches)

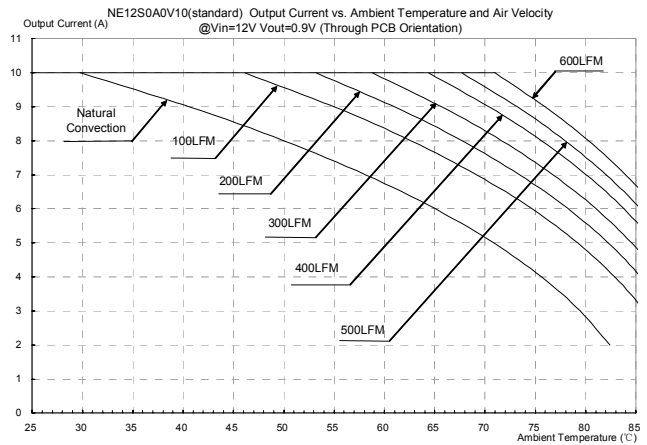
**Figure 24:** Wind tunnel test setup

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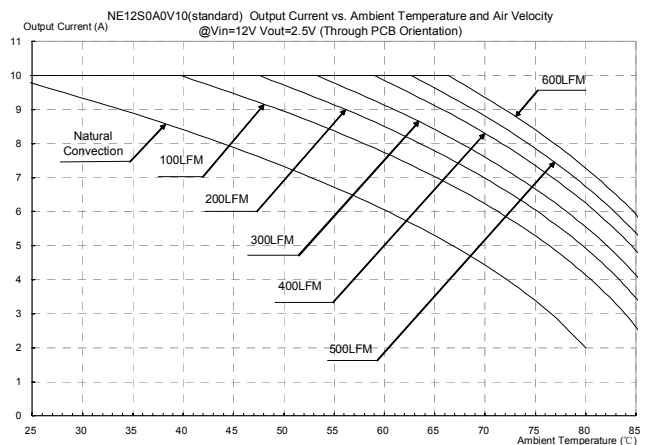
## THERMAL CURVES (NE12S0A0V10)



**Figure 25:** Temperature measurement location\* The allowed maximum hot spot temperature is defined at 109°C



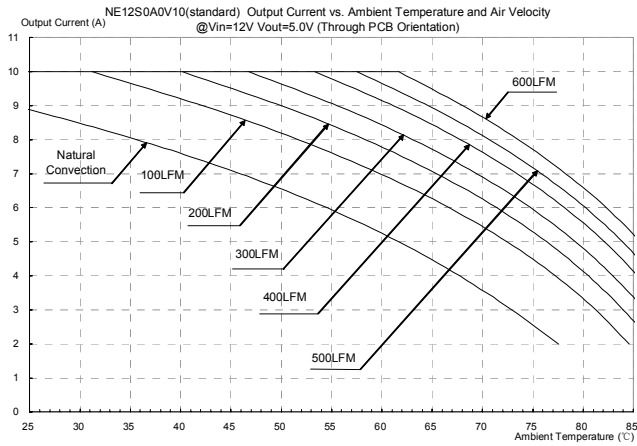
**Figure 26:** Output current vs. ambient temperature and air velocity @ Vin=12V, Vout=0.9V(Through PCB Orientation)



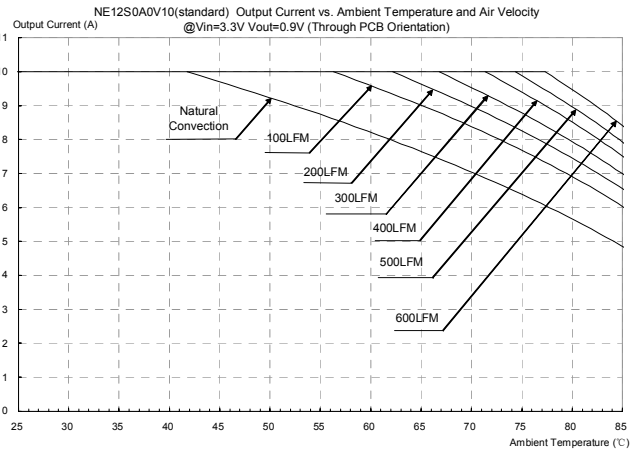
**Figure 27:** Output current vs. ambient temperature and air velocity @ Vin=12V, Vout=2.5V(Through PCB Orientation)



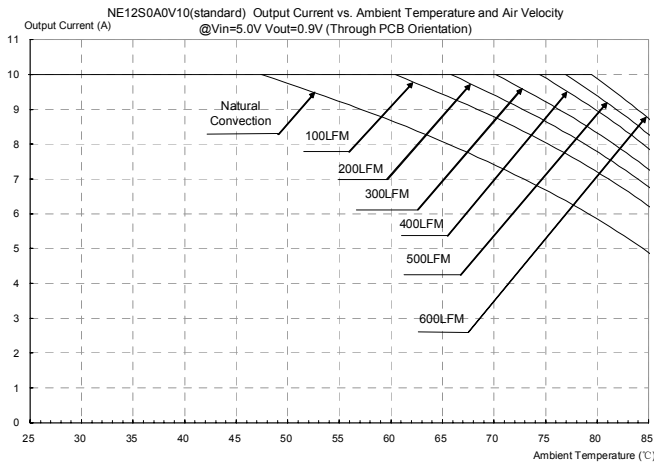
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**THERMAL CURVES (NE12S0A0V10)**



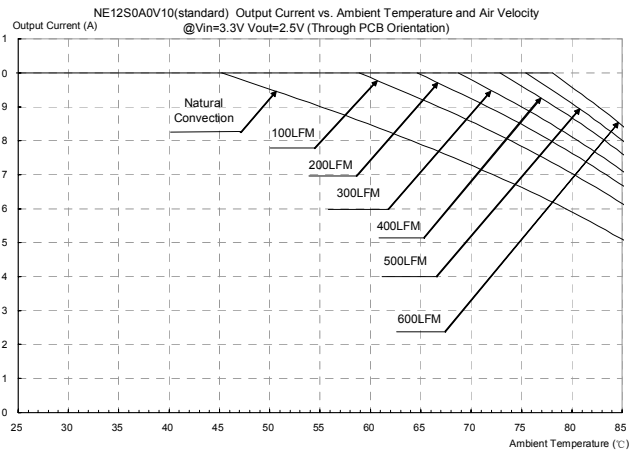
**Figure 28:** Output current vs. ambient temperature and air velocity @ Vin=12V, Vout=5.0V(Through PCB Orientation)



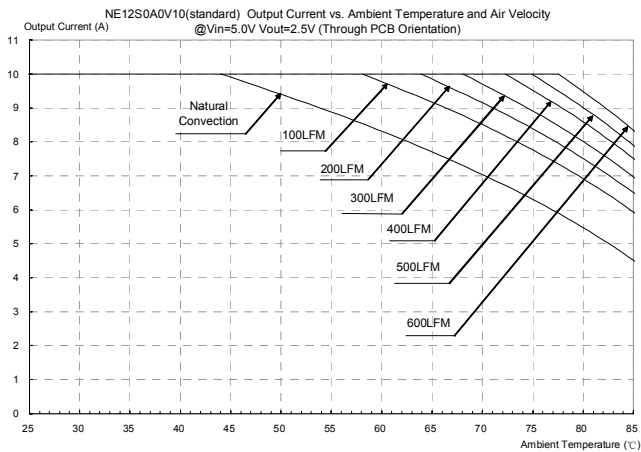
**Figure 31:** Output current vs. ambient temperature and air velocity @ Vin=3.3V, Vout=0.9V(Through PCB Orientation)



**Figure 29:** Output current vs. ambient temperature and air velocity @ Vin=5.0V, Vout=0.9V(Through PCB Orientation)



**Figure 32:** Output current vs. ambient temperature and air velocity @ Vin=3.3V, Vout=2.5V(Through PCB Orientation)

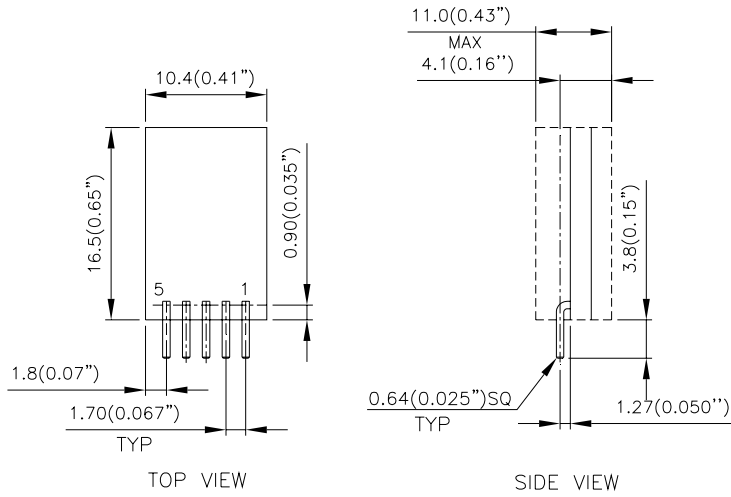


**Figure 30:** Output current vs. ambient temperature and air velocity @ Vin=5.0V, Vout=2.5V(Through PCB Orientation)



**VERTICAL**

**HORIZONTAL**



TBD

PIN ASSIGNMENT

PIN#	FUNCTION
1	Enable
2	Vin
3	Common/RTN
4	Vout
5	PG/Trim

NOTES:  
 DIMENSIONS ARE IN MILLIMETERS AND (INCHS)  
 TOLERANCE: X.X mm±0.5 mm(X.XX in.±0.02 in.)  
 X.XX mm±0.25 mm(X.XXX in.±0.010 in.)



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## PART NUMBERING SYSTEM

NE	12	S	0A0	V	10	P	N	F	A
Product Series	Input Voltage	Number of outputs	Output Voltage	Mounting	Output Current	ON/OFF Logic	Pin Length		Option Code
NE-Non-isolated Series	12- 3.0~13.8V	S- Single output	0A0 - programmable	H- Horizontal V- Vertical	10-10A	P- Positive	N- 0.150"	F- RoHS 6/6 (Lead Free)	A- 5 pins

## MODEL LIST

Model Name	Packaging	Input Voltage	Output Voltage	Output Current	Efficiency 12Vin @ 100% load
NE12S0A0V10PNFA	Vertical	3.0V~ 13.8Vdc	0.59V~ 5.1Vdc	10A	94.0%@5Vout
NE12S0A0H10PNFA	Horizontal	3.0V~ 13.8Vdc	0.59V~ 5.1Vdc	10A	94.0%@5Vout

**CONTACT:** [www.delta.com.tw/dcdc](http://www.delta.com.tw/dcdc)

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