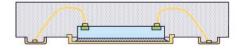


BCC

Bump Chip Carrier

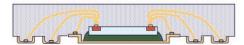
BCC+ (exposed paddle without ground ring)



BCC++ (exposed paddle with ground ring)



BCCs++ (staggered dual row design)



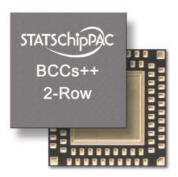
FEATURES

- Body sizes: 4 x 4mm to 9 x 9mm
- · Lead pitch: 0.50mm and 0.80mm
- Custom body / lead / pitch configurations available
- Package profile heights (overall): maximum 0.80mm
- Both single row & dual row design
- Ni / Pd / Au plated bumps
- Excellent thermal and electrical performance
- · Full in-house package and leadframe design capability
- Full in-house electrical, thermal and mechanical simulation and measurement capability
- · JEDEC standard compliant

APPLICATIONS

- RF
- Power Management
- Analog/Linear
- Logic
- Applications requiring enhanced electrical and thermal performance and reduced package size and weight

- Saw singulated format
- Package height 0.8mm max.
- Square body size (rectangular body designable)
- Staggered dual row or single row bump design



DESCRIPTION

STATS ChipPAC's Bump Chip Carrier (BCC) technology produces a chip scale leadframe based molded package with bumps which are formed after the leadframe is etched away. An exposed die pad coupled with extremely low RLC provides excellent electrical and thermal performance enhancements which are ideal for high frequency and high power applications especially for handheld portable applications such as cell phones. The BCC is manufactured in a molded array format that maximizes product throughput and material utilization. The BCC is available with single row and dual row bumps in BCC++ and BCC+. Overall package profile height is 0.80mm maximum.

www.statschippac.com



BCC

Bump Chip Carrier

168 hrs

SPECIFICATIONS

 $\begin{array}{lll} \mbox{Die Thickness} & 279 \mu \mbox{m nominal, } 355 \mu \mbox{m max.} \\ \mbox{Gold Wire} & 25 \mu \mbox{m (1.0mils) diameter} \\ \mbox{Lead Finish} & \mbox{Preplated Ni/Pd/Au bumps} \\ \end{array}$

Marking Laser

Packing Options Tape & reel / JEDEC tray

RELIABILITY

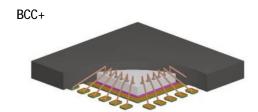
Moisture Sensitivity Level

Temperature Cycling High Temp Storage Temp / Humidity Test Pressure Cooker Test JEDEC Level 2a/2/1@260°C (depending upon package) -65°C/150°C, 1000 cycles 150°C, 1000 hrs 85°C/85% RH, 1000 cycles 121°C, 100% RH, 2 atm,

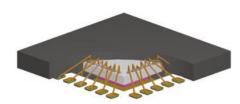
PACKAGE CONFIGURATIONS

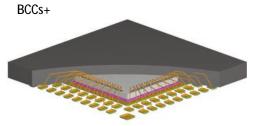
Package Size	Lead Pitch	Lead Count
(mm)	(mm)	
4 x 4	0.80	16
5 x 5	0.50	32
6 x 6	0.50	40
7 x 7	0.50	48 / 84
8 x 8	0.50	56
9 x 9	0.50	116

CROSS-SECTIONS

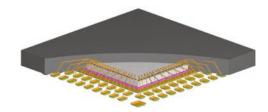








BCCs++



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