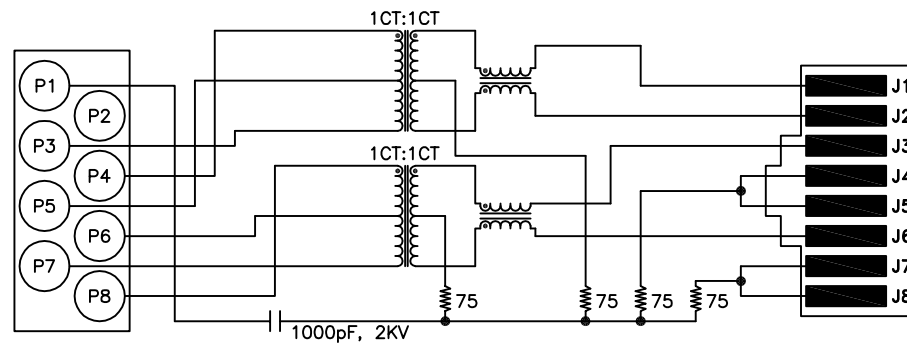


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NOTES:

- 1.0 PINS WITHOUT ELECTRICAL CONNECTION ARE OMITTED.
- 2.0 ALL RESISTORES ARE  $\pm 5\%$  TOLERANCE.

ELECTRICAL SPECIFICATIONS:

- 1.0 TURNS RATIO: (P8-P6-P7) : (J3-J6) : 1CT : 1CT  $\pm 3\%$   
(P4-P5-P3) : (J1-J2) : 1CT : 1CT  $\pm 3\%$
- 2.0 INDUCTANCE: (P7-P8) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias  
(P4-P3) : 350uH MIN. @ 0.1V, 100KHz, 8mA DC Bias
- 3.0 LEAKAGE INDUCTANCE: P7-P8 (WITH J6 AND J3 SHORT) : 0.3uH MAX. @ 1MHz  
P4-P3 (WITH J2 AND J1 SHORT) : 0.3uH MAX. @ 1MHz
- 4.0 INTERWINDING CAPACITANCE: (P8,P6,P7) TO (J6,J3) : 25pf TYP @ 1MHz  
(P4,P5,P3) TO (J2,J1) : 25pf TYP @ 1MHz
- 5.0 DC RESISTANCE: (J6-J3)=(J2-J1) : 1.2 ohms Max.
- 6.0 RETURN LOSS: (P7-P8)=100 OHMS AND (P4-P3)=100 OHM REF.  
1MHz TO 30MHz : -18dB MIN.  
30MHz TO 60MHz : -(19-20 LOG (f/30MHz))  
60MHz TO 80MHz : -12dB MIN.  
NOTE: 100 OHMS CONNECTED TO (J2-J1) OR (J6-J3).
- 7.0 DIELECTRIC WITHSTAND: (J1, J2) TO (P7, P8) : 1500 VAC  
(J3, J6) TO (P4, P3) : 1500 VAC
- 8.0 INSERTION LOSS: RS=RL=100 ohms  
1-65MHz : -1 dB MAX
- 9.0 RISE TIME: RISE TIME: RS=100 OHMS AND RL = 100 OHMS : 3.0 nS MAX  
OUTPUT VOLTAGE = 1 V peak : 3.0 nS MAX  
PULSE WIDTH= 112nS
- 10.0 CROSS TALK: 1-100 MHz : -35 dB TYP
- 11.0 COMMON TO COMMON MODE ATTENUATION: 30MHz TO 100MHz : -35 dB TYP

Bel Stewart Connector  
11118 Susquehanna Trail, South  
Glen Rock, Pa 17327-9199  
717.234.7512

**MagJack**

<http://www.stewartconnector.com>

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SI-50102

REV.  
01

Diagram of the back of a MagLock unit. The unit is rectangular with two mounting brackets at the bottom. The following dimensions and labels are shown:

- Top Label:** Part Number, COUNTRY YMWV FC, Bel Stewart
- Right Side Dimension:** 1,000 [25,40] MAX.
- Bottom Left Dimension:** \*635 [16,13] MAX
- Bottom Right Dimension:** \*708 [17,98] (REF.)

- TOLERANCES COMPLY WITH F.C.C. DIMENSION REQUIREMENTS
- DIMENSIONS SHOWN WITH "\*" TO BE CENTRAL ABOUT CENTER LINE
- DIMENSIONS SHOWN ARE SUBJECT TO CHANGE WITHOUT NOTICE.
- PIN NOT ELECTRICALLY CONNECTED MAYBE OMITTED. SEE ELECTRICAL DRAWING FOR OMITTED PINS.
- STANDARD 50 MICRO-INCH SELECTIVE GOLD PLATING
- REFLOW COMPATIBLE - 230°C/90 SEC.
- ALL POLYMERS FLAMMABILITY - UL94V0

STANDARD LED	WAVELENGTH	* Forward V (MAX)	(TYP)
YELLOW	590 nm	2.5 V	2.1 V
GREEN	565 nm	2.5 V	2.2 V
GREEN 5V	565 nm		6 V

Diagram of the J1 connector showing pin layout and dimensions. The connector has a central row of pins labeled GRN/YEL and GRN. The width of the connector is indicated as .550 [13.97] MAX. The bottom row of pins is labeled PIN: J1.

POINT OF CONTACT WITH PANEL

.080

.599 [15.21]

.014 [0.36] (REF.)

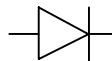
.083 [2.12] HIGHEST POINT OF BEAM (REF.)

.425 [10.79] +.005

.040 ±.005 [1.02 ±.13]

.125 ±.010 [3.18 ±.25]

GRN



YEL

P.C.B. RECOMMENDED HOLE LAYOUT  
SEEN FROM COMPONENT SIDE  
TOLERANCE  $\pm 0.003$  [0.08] UNLESS OTHERWISE SPECIFIED

CT750006

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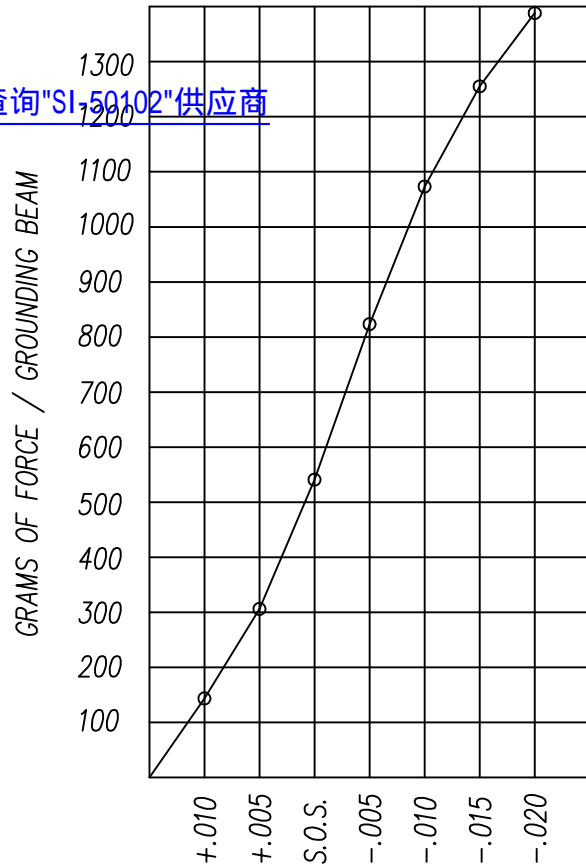
<http://www.stewartconnector.com>

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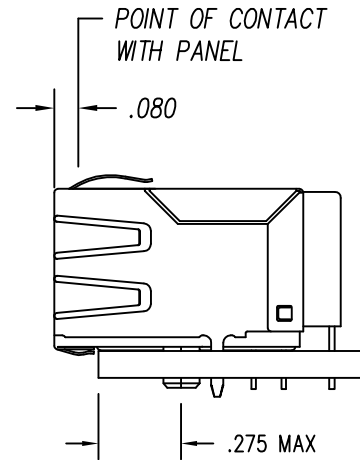
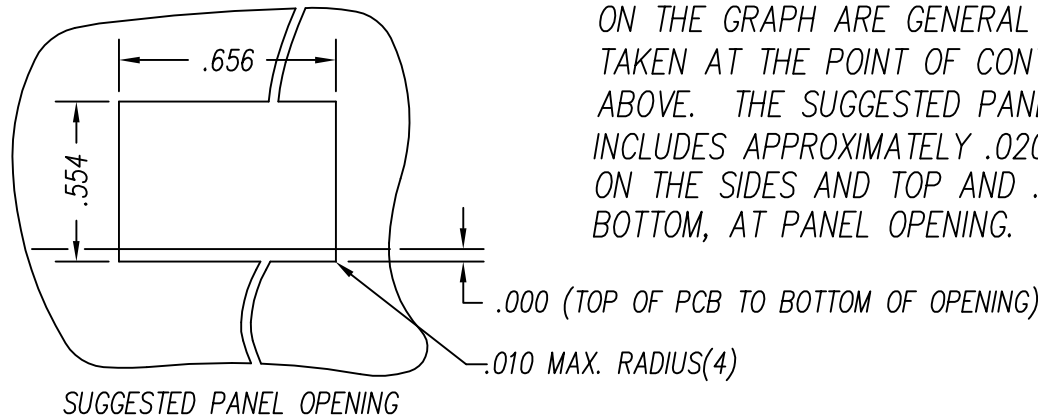
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PANEL GROUNDING BEAM DEFLECTION  
S.O.S. = SUGGESTED OPENING SIZE



THE SUGGESTED PANEL OPENING IS INTENDED TO GIVE THE USER THE ABILITY TO HAVE REASONABLE JACK / PANEL CLEARANCES YET MAINTAIN RELIABLE GROUNDING CAPABILITY. THESE VARIABLES CAN BE ADJUSTED IN EITHER DIRECTION BUT MAY CARRY SOME CONSEQUENCES IN THE FORM OF LOWER MATING FORCES OR TIGHTER ASSEMBLY TOLERANCES. FORCE VALUES ON THE GRAPH ARE GENERAL AVERAGES TAKEN AT THE POINT OF CONTACT SHOWN ABOVE. THE SUGGESTED PANEL OPENING INCLUDES APPROXIMATELY .020 CLEARANCE ON THE SIDES AND TOP AND .013 ON THE BOTTOM, AT PANEL OPENING.

CT720034X1/24-001302

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