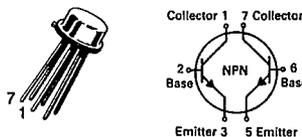


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T-29-27

2N2721

CASE 654-07, STYLE 1



DUAL AMPLIFIER TRANSISTOR

NPN SILICON

Refer to 2N2060 for graphs.

MAXIMUM RATINGS

Rating	Symbol	Value	Unit
Collector-Emitter Voltage	V_{CE0}	60	Vdc
Collector-Base Voltage	V_{CB0}	80	Vdc
Emitter-Base Voltage	V_{EB0}	6.0	Vdc
Collector Current — Continuous	I_C	40	mAdc
		One Die	Both Die
Total Device Dissipation @ $T_A = 25^\circ\text{C}$ Derate above 25°C	P_D	0.3 1.71	Watt mW/ $^\circ\text{C}$
Total Device Dissipation @ $T_C = 25^\circ\text{C}$ Derate above 25°C	P_D	0.6 3.4	Watt mW/ $^\circ\text{C}$
Operating and Storage Junction Temperature Range	T_J, T_{stg}	-65 to +200	$^\circ\text{C}$

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ELECTRICAL CHARACTERISTICS ($T_A = 25^\circ\text{C}$ unless otherwise noted.)

Characteristic	Symbol	Min	Max	Unit
OFF CHARACTERISTICS				
Collector-Emitter Breakdown Voltage(1) ($I_C = 10\text{ mAdc}, I_B = 0$)	$V_{(BR)CEO}$	60	—	Vdc
Collector Cutoff Current ($V_{CE} = 5.0\text{ Vdc}, I_B = 0$)	I_{CEO}	—	10	nAdc
Collector Cutoff Current ($V_{CB} = 60\text{ Vdc}, I_E = 0$) ($V_{CB} = 60\text{ Vdc}, I_E = 0, T_A = 150^\circ\text{C}$)	I_{CBO}	—	0.01 10	μAdc
Emitter Cutoff Current ($V_{EB} = 5.0\text{ Vdc}, I_C = 0$)	I_{EBO}	—	10	nAdc
ON CHARACTERISTICS				
DC Current Gain ($I_C = 100\ \mu\text{Adc}, V_{CE} = 5.0\text{ Vdc}$) ($I_C = 1.0\text{ mAdc}, V_{CE} = 5.0\text{ Vdc}$) ($I_C = 10\text{ mAdc}, V_{CE} = 5.0\text{ Vdc}$)	h_{FE}	30 35 42	120 — —	—
Collector-Emitter Saturation Voltage ($I_C = 10\text{ mAdc}, I_B = 1.0\text{ mAdc}$)	$V_{CE(sat)}$	—	1.0	Vdc
Base-Emitter Saturation Voltage ($I_C = 10\text{ mAdc}, I_B = 1.0\text{ mAdc}$)	$V_{BE(sat)}$	0.65	0.85	Vdc
SMALL-SIGNAL CHARACTERISTICS				
Current-Gain — Bandwidth Product ($I_C = 10\text{ mAdc}, V_{CE} = 10\text{ Vdc}, f = 20\text{ MHz}$)	f_T	80	—	MHz
Output Capacitance ($V_{CB} = 5.0\text{ Vdc}, I_E = 0, f = 1.0\text{ MHz}$)	C_{obo}	—	6.0	pF
Input Impedance ($I_E = 1.0\text{ mAdc}, V_{CB} = 5.0\text{ Vdc}, f = 1.0\text{ kHz}$)	h_{ib}	25	32	ohms
Voltage Feedback Ratio ($I_E = 1.0\text{ mAdc}, V_{CB} = 5.0\text{ Vdc}, f = 1.0\text{ kHz}$)	h_{rb}	—	500	$\times 10^{-6}$
Small-Signal Current Gain ($I_C = 1.0\text{ mAdc}, V_{CE} = 5.0\text{ Vdc}, f = 1.0\text{ kHz}$)	h_{fe}	30	200	—
Output Admittance ($I_E = 1.0\text{ mAdc}, V_{CB} = 5.0\text{ Vdc}, f = 1.0\text{ kHz}$)	h_{ob}	—	1.0	μmhos
MATCHING CHARACTERISTICS				
DC Current Gain Ratio(2) ($I_C = 100\ \mu\text{Adc}, V_{CE} = 5.0\text{ Vdc}$)	h_{FE1}/h_{FE2}	0.8	1.0	—
Base-Emitter Voltage Differential ($I_C = 100\ \mu\text{Adc}, V_{CE} = 5.0\text{ Vdc}$)	$ V_{BE1} - V_{BE2} $	—	10	mVdc
Base-Emitter Voltage Differential Change Due to Temperature ($I_C = 100\ \mu\text{Adc}, V_{CE} = 5.0\text{ Vdc}, T_A = -55\text{ to }+25^\circ\text{C}$)	$\Delta(V_{BE1} - V_{BE2})$	—	1.6	mV
($I_C = 100\ \mu\text{Adc}, V_{CE} = 5.0\text{ Vdc}, T_A = +25\text{ to }+125^\circ\text{C}$)		—	2.0	

(1) Pulse Test: Pulse Width $\leq 300\ \mu\text{s}$, Duty Cycle $\leq 2.0\%$.

(2) The lower of the two h_{FE} readings is taken as h_{FE1} for the purpose of measurement.